Product data sheet

1. General description

PNP medium power transistor series encapsulated in an ultra thin DFN2020D-3 (SOT1061D) leadless small Surface-Mounted Device (SMD) plastic package with medium power capability and visible and solderable side pads.

2. Features and benefits

- High collector current capability I_C and I_{CM}
- Reduced Printed-Circuit Board (PCB) area requirements
- · Exposed heat sink for excellent thermal and electrical conductivity
- Two current gain selections
- Leadless very small SMD plastic package with medium power capability
- Suitable for Automatic Optical Inspection (AOI) of solder joint

3. Applications

- Linear voltage regulators
- Battery driven devices
- MOSFET drivers
- · High-side switches
- Power management
- Amplifiers

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-80	V
I _C	collector current			-	-	-1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-2	Α
h _{FE}	DC current gain						
	BC53PAS	V _{CE} = -2 V; I _C = -150 mA; T _{amb} = 25 °C	[1]	63	-	250	
	BC53-10PAS		[1]	63	-	160	
	BC53-16PAS		[1]	100	-	250	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	Е	emitter		С
3	С	collector	Transparent top view DFN2020D-3 (SOT1061D)	B — E sym013

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BC53PAS	DFN2020D-3	plastic, leadless thermal enhanced ultra thin small outline	SOT1061D
BC53-10PAS		package with side-wettable flanks (SWF); no leads; 3 terminals; 1.3 mm pitch; 2 mm x 2 mm x 0.65 mm body	
BC53-16PAS		terminais, 1.5 mm piton, 2 mm x 2 mm x 0.05 mm body	

7. Marking

Table 4. Marking codes

Type number	Marking code					
BC53PAS	CA					
BC53-10PAS	СВ					
BC53-16PAS	СС					

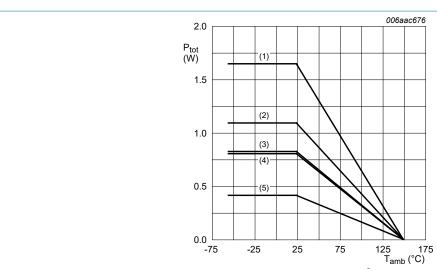
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-100	V
V_{CEO}	collector-emitter voltage	open base		-	-80	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-2	А
I _B	base current			-	-0.3	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.42	W
			[2]	-	0.81	W
			[3]	-	0.83	W
			[4]	-	1.1	W
			[5]	-	1.65	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm². Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm². Device mounted on an FR4 PCB, 4-layer copper, tin-plated and mounting pad for collector 1 cm². [3]
- [5]



- (1) FR4 PCB, 4-layer copper, mounting pad for collector 1 cm²
- (2) FR4 PCB, single-sided copper, mounting pad for collector 6 cm²
- (3) FR4 PCB, single-sided copper, mounting pad for collector 1 cm²
- (4) FR4 PCB, 4-layer copper, standard footprint
- (5) FR4 PCB, single-sided copper, standard footprint

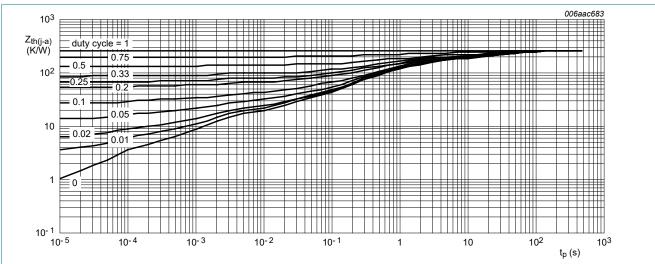
Fig. 1. **Power derating curves**

9. Thermal characteristics

Table 6. Thermal characteristics

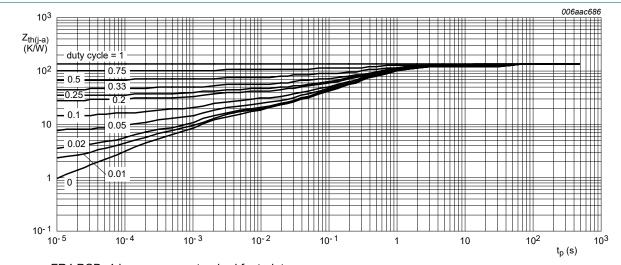
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui() u/	thermal resistance from	[1] [2] [3] [4] [5]	[1]	-	-	298	K/W
	junction to ambient		[2]	-	-	154	K/W
			[3]	-	-	151	K/W
			[4]	-	-	114	K/W
			-	-	76	K/W	
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm²
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².
- [5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and mounting pad for collector 1 cm²



FR4 PCB, single-sided copper, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer copper, standard footprint

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

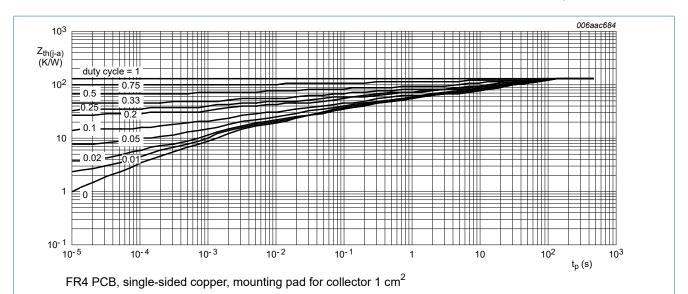
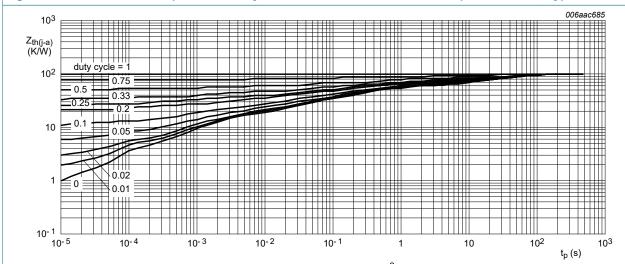
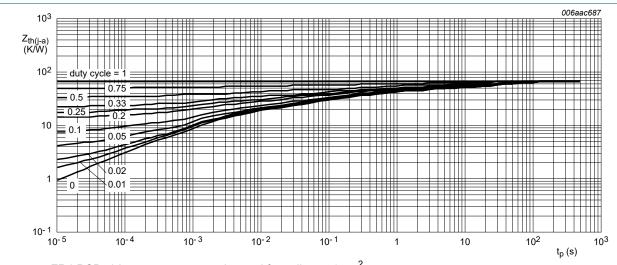


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, single-sided copper, mounting pad for collector 6 cm²

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer copper, mounting pad for collector 1 cm²

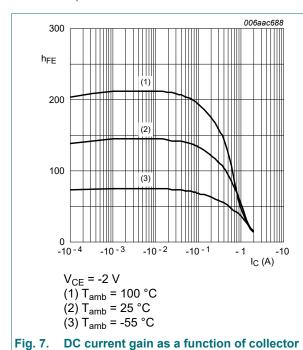
Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
I _{CBO}	collector-base cut-off	V _{CB} = -30 V; I _E = 0 A; T _{amb} = 25 °C		-	-	-100	nA	
	current (emitter open)	V _{CB} = -30 V; I _E = 0 A; T _{amb} = 150 °C		-	-	-10	μΑ	
I _{EBO}	emitter-base cut-off current (collector open)	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	-100	nA	
h _{FE}	DC current gain	DC current gain						
	BC53PAS	V _{CE} = -2 V; I _C = -5 mA; T _{amb} = 25 °C		63	-	-		
	BC53-10PAS			63	-	-		
	BC53-16PAS			63	-	-		
	BC53PAS	V _{CE} = -2 V; I _C = -150 mA; T _{amb} = 25 °C		63	-	250		
	BC53-10PAS			63	-	160		
	BC53-16PAS			100	-	250		
	BC53PAS	V_{CE} = -2 V; I_{C} = -500 mA; T_{amb} = 25 °C	[1]	40	-	-		
	BC53-10PAS		[1]	40	-	-		
	BC53-16PAS			40	-	-		
V _{CEsat}	collector-emitter saturation voltage	$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}; T_{amb} = 25 \text{ °C}$	[1]	-	-	-500	mV	
V _{BE}	base-emitter voltage	V_{CE} = -2 V; I_{C} = -500 mA; T_{amb} = 25 °C	[1]	-	-	-1	V	
C _c	collector capacitance	V_{CB} = -10 V; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	15	-	pF	
f _T	transition frequency	V_{CE} = -5 V; I_{C} = -50 mA; f = 100 MHz; T_{amb} = 25 °C		-	145	-	MHz	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$



current; typical values

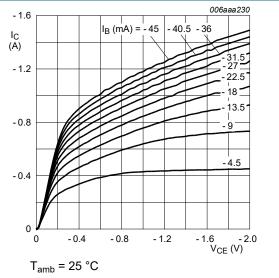


Fig. 8. Collector current as a function of collectoremitter voltage; typical values

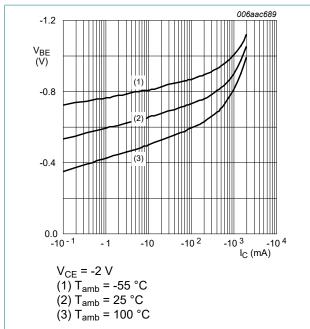


Fig. 9. Base-emitter voltage as a function of collector current; typical values

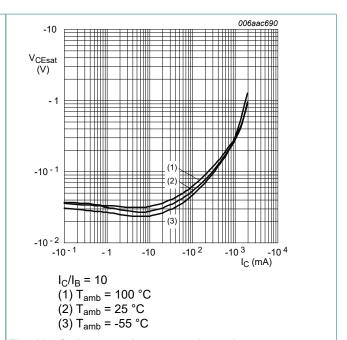
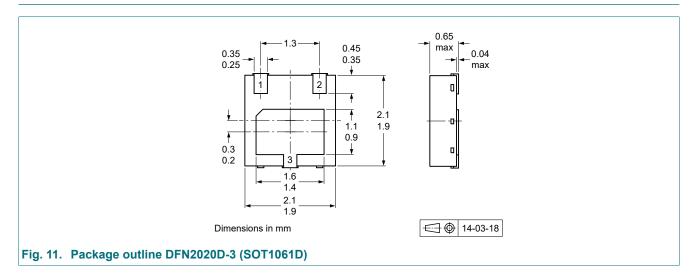
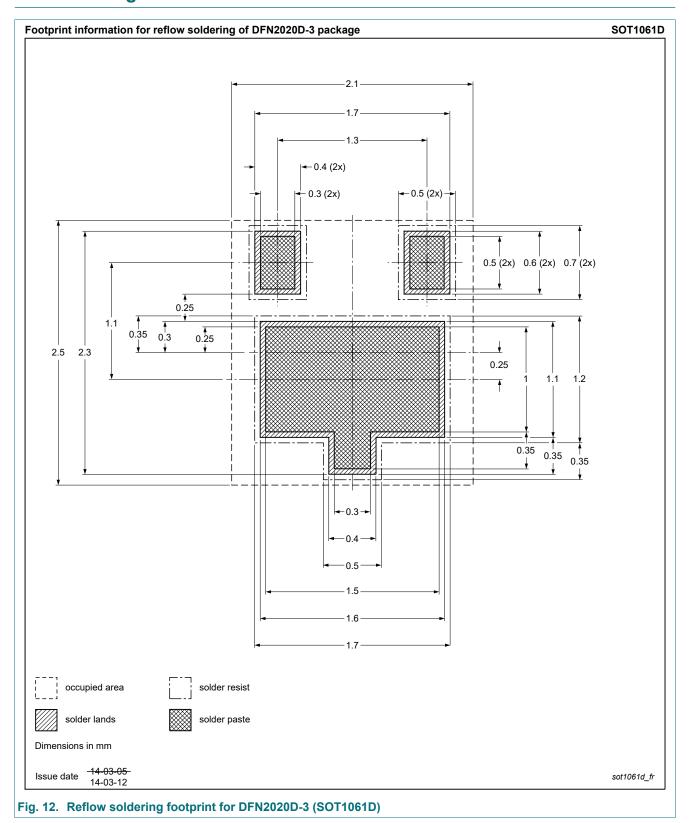


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

11. Package outline



12. Soldering



13. Revision history

Table 8. Revision history

lable 8. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC53XPAS_SER v.3	20240206	Product data sheet	-	BC53XPAS_SER v.2
Modifications:	` ,	nanged to non-automo e (-Q) product alterna	•	ation. Please refer to nexperia.com
BC53XPAS_SER v.2	20221206	Product data sheet	-	BC51_52_53PAS_SER v.1
BC51_52_53PAS_SER v.1	20150619	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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