

20 V, 2 A NPN medium power transistors Rev. 1 — 19 June 2015

Product data sheet

#### **Product profile** 1.

### **1.1 General description**

NPN medium power transistors in an ultra thin DFN2020D-3 (SOT1061D) leadless small Surface-Mounted Device (SMD) plastic package with medium power capability and visible and solderable side pads.

PNP complement: BC69PAS series

### 1.2 Features and benefits

- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- Reduced Printed-Circuit Board (PCB) area requirements
- Exposed heat sink for excellent thermal and electrical conductivity
- AEC-Q101 qualified

### 1.3 Applications

- Linear voltage regulators
- Battery driven devices
- MOSFET drivers

### 1.4 Quick reference data

#### Table 1. Quick reference data

### $_{mb} = 25 \ ^{\circ}C$ unless otherwise specified

$T_{amb} = 25$	C unless otherwise speci	leu				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	20	V
I <sub>C</sub>	collector current		-	-	2	A
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-	3	A
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA [1]	85	-	375	
	h <sub>FE</sub> selection -25	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA [1]	160	-	375	

[1] Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ .

- Two current gain selections
- Leadless very small SMD plastic package with medium power capability
  - Suitable for Automatic Optical Inspection (AOI) of solder joint
- Low-side switches
- Power management
- Amplifiers



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## 2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	base		
2	emitter	3	3
3	collector		
		1 2	sym021
		Transparent top view	

## 3. Ordering information

Table 3.	Ordering informa	ation
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Type number Package			
	Name	Description	Version
BC68PAS	DFN2020D-3	plastic thermal enhanced ultra thin small outline	SOT1061D
BC68-25PAS	-	package; no leads; 3 terminals; body $2 \times 2 \times 0.65$ mm.	

### 4. Marking

Table 4.   Marking codes	
Type number	Marking code
BC68PAS	BY
BC68-25PAS	BZ

## 5. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	-	32	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	20	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	5	V
I <sub>C</sub>	collector current		-	2	А
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	3	A
I <sub>B</sub>	base current		-	0.4	A

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Symbol	Parameter	Conditions		Min	Max	Unit
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u>	-	420	mW
			[2]	-	830	mW
			[3]	-	1.1	W
			[4]	-	810	mW
			[5]	-	1.65	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

#### Table 5. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

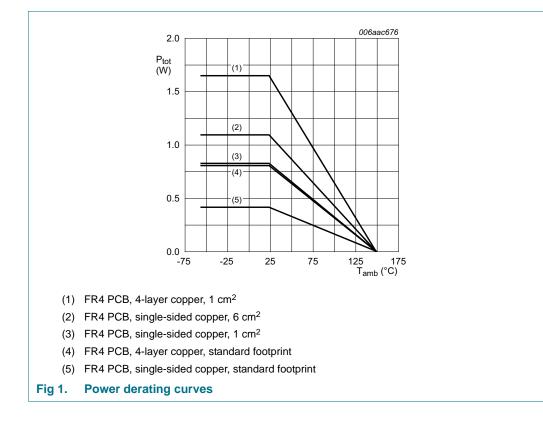
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.



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### 6. Thermal characteristics

Symbol	Parameter	Conditions		Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	298	K/W
			[2]	151	K/W
			[3]	114	K/W
			[4]	154	K/W
			[5]	76	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point	in free air		20	K/W

#### Table 6. Thermal characteristics

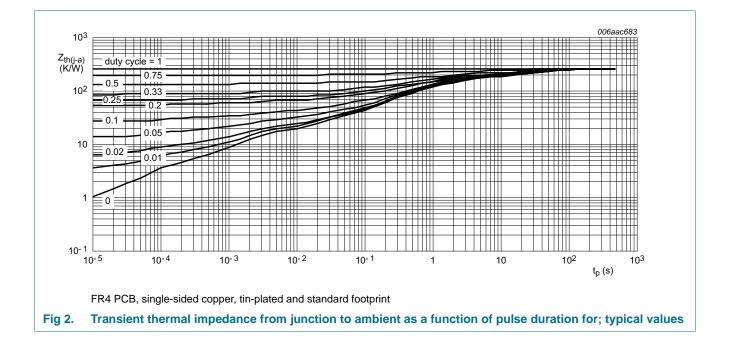
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.

[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

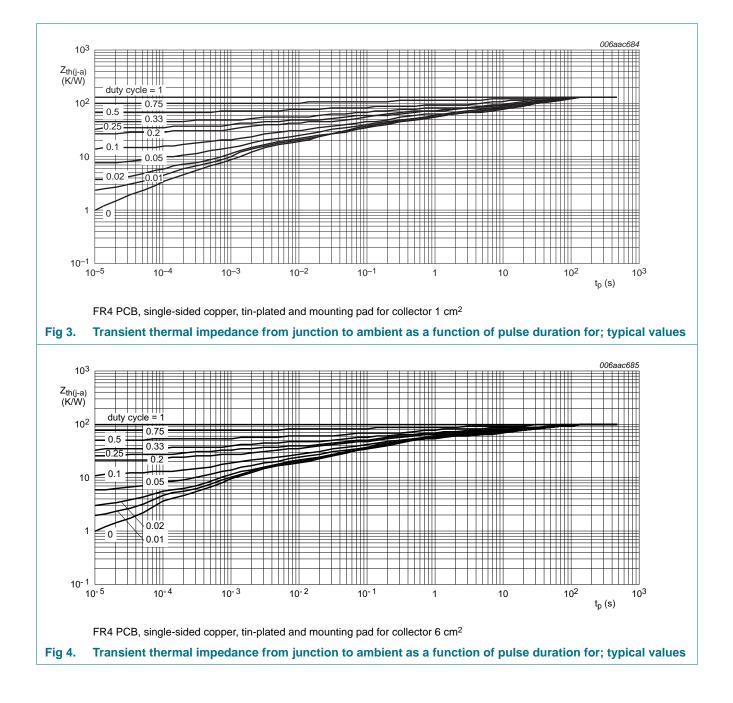
[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and mounting pad for collector 1 cm<sup>2</sup>



### Nexperia

# **BC68PAS series**

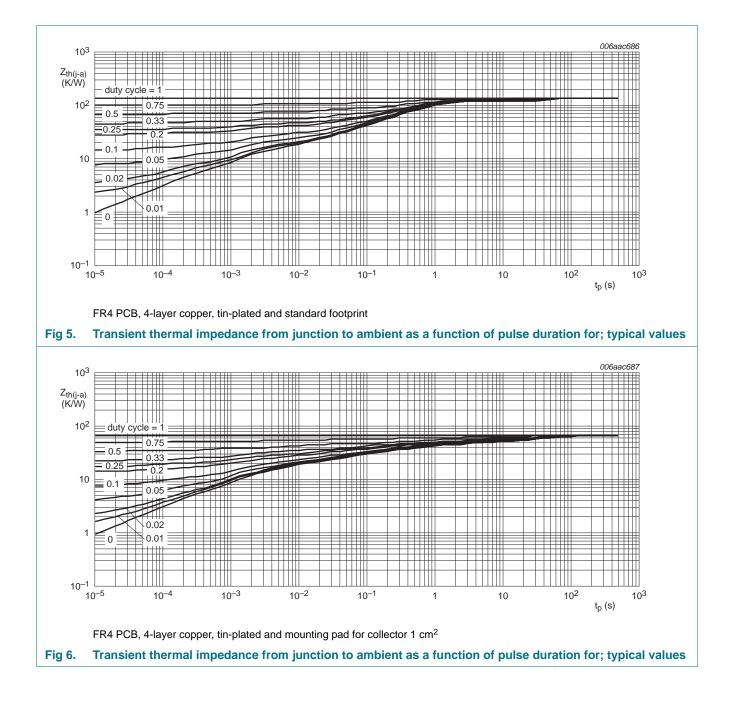
### 20 V, 2 A NPN medium power transistors



### Nexperia

# **BC68PAS series**

### 20 V, 2 A NPN medium power transistors



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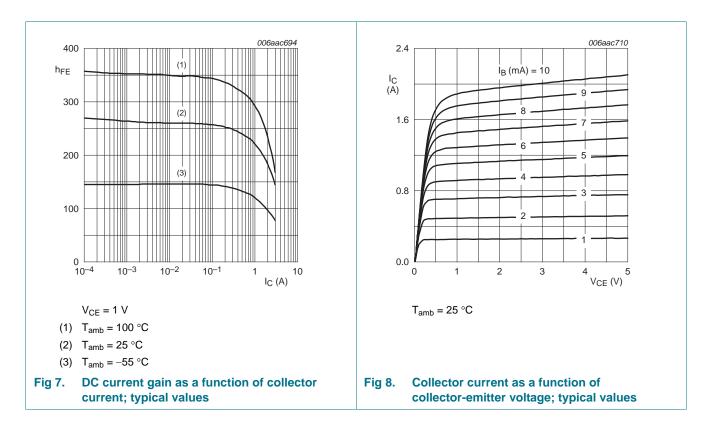
## 7. Characteristics

#### Table 7. Characteristics

 $T_{amb} = 25 \ ^{\circ}C$  unless otherwise specified

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = 25 \text{ V}; \text{ I}_{E} = 0 \text{ A}$		-	-	100	nA
		$V_{CB} = 25 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{j} = 150 ^{\circ}\text{C}$		-	-	10	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	100	nA
h <sub>FE</sub> DC current gain	$V_{CE} = 10 \text{ V}; \text{ I}_{C} = 5 \text{ mA}$		50	-	-		
	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	<u>[1]</u>	85	-	375		
	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 1 A	<u>[1]</u>	60	-	-		
	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 2 A	<u>[1]</u>	40	-	-		
	h <sub>FE</sub> selection -25	V <sub>CE</sub> = 1 V; I <sub>C</sub> = 500 mA	<u>[1]</u>	160	-	375	
V <sub>CEsat</sub>	collector-emitter saturation	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA	<u>[1]</u>	-	-	0.5	V
	voltage	I <sub>C</sub> = 2 A; I <sub>B</sub> = 200 mA	<u>[1]</u>	-	-	0.6	V
V <sub>BE</sub>	base-emitter voltage	I <sub>C</sub> = 5 mA; V <sub>CE</sub> = 10 V	<u>[1]</u>	-	-	0.7	V
		I <sub>C</sub> = 1 A; V <sub>CE</sub> = 1 V	<u>[1]</u>	-	-	1	V
f <sub>T</sub>	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 50 \text{ mA}; f = 100 \text{ MHz}$		40	170	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; \text{ I}_{E} = \text{i}_{e} = 0 \text{ A}; \text{ f} = 1 \text{ MHz}$		-	22	-	pF

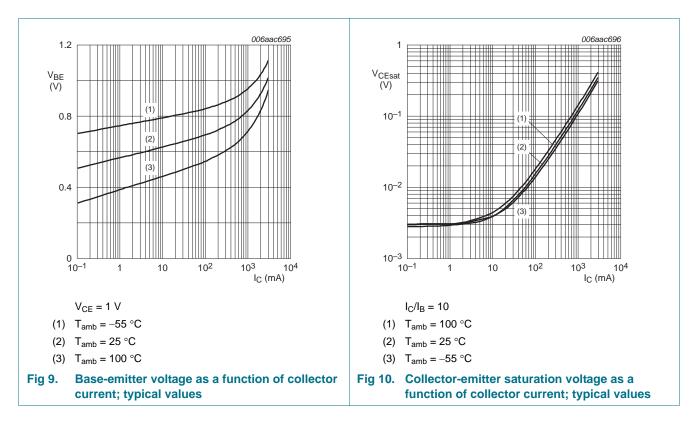
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### **Nexperia**

# **BC68PAS** series

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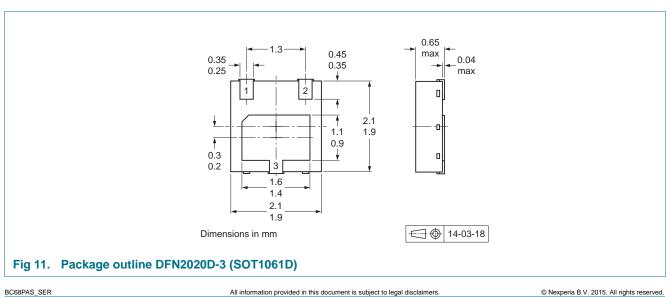


#### **Test information** 8.

### 8.1 Quality information

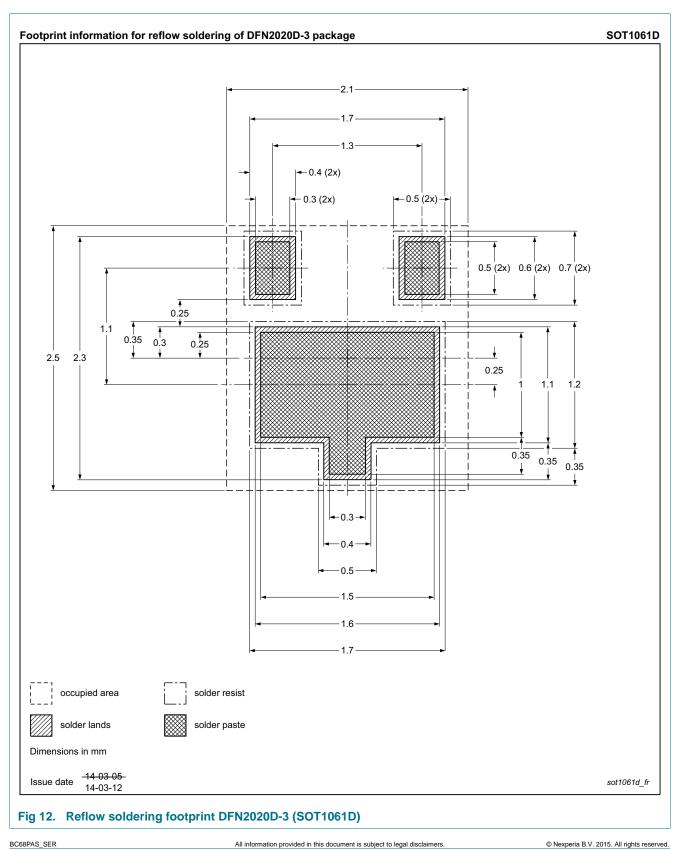
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

#### **Package outline** 9.



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## 10. Soldering



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## **11. Revision history**

### Table 8.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BC68PAS_SER v.1	20150619	Product data sheet	-	-

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## **12. Legal information**

### 12.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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