

45 V, 100 mA PNP general-purpose transistor Rev. 1 — 27 October 2021

Product data sheet

1. General description

PNP general-purpose transistor in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview

Type number	Package		Package N		NPN complement:
	Nexperia	JEDEC			
BC857AQC	SOT8009	MO-340CA	BC847AQC		
BC857BQC			BC847BQC		
BC857CQC			BC847CQC		

2. Features and benefits

- High power dissipation capability •
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm

3. Applications

- General-purpose switching and amplification
- Space restricted applications

4. Quick reference data

Table 2. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	-200	mA
h _{FE}	DC current gain			- i		
	BC857AQC	V _{CE} = -5 V; I _C = -2 mA	125	-	250	
	BC857BQC		220	-	475	
	BC857CQC		420	-	800	

nexperia

5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		ç
2	E	emitter	3	вщ
3	С	collector		۲ by
			1 2	E sym132
				6,62
			Transparent top view	

6. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
BC857AQC	DFN1412D-3	plastic leadless ultra small outline package with side-	SOT8009
BC857BQC]	wettable flanks (SWF); 3 terminals; 0.8 mm pitch; body: 1.4 mm x 1.2 mm x 0.48 mm	
BC857CQC	1		

7. Marking

Table 5. Marking	
Type number	Marking code
BC857AQC	9F
BC857BQC	9G
BC857CQC	9H

8. Limiting values

Table 6. Limiting values

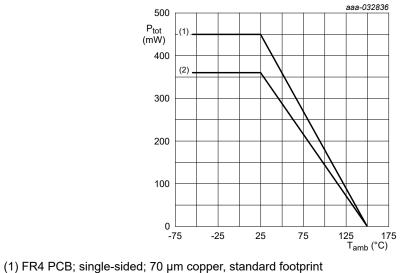
In accordance with the Absolute Maximum Rating System (IEC 60134).

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-45	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-200	mA
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	360	mW
			[2]	-	450	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided; 35 µm copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided; 70 µm copper; tin-plated and standard footprint.



(2) FR4 PCB; single-sided; 35 μm copper, standard footprint

Fig. 1. Power derating curves DFN1412D-3 (SOT8009)

9. Thermal characteristics

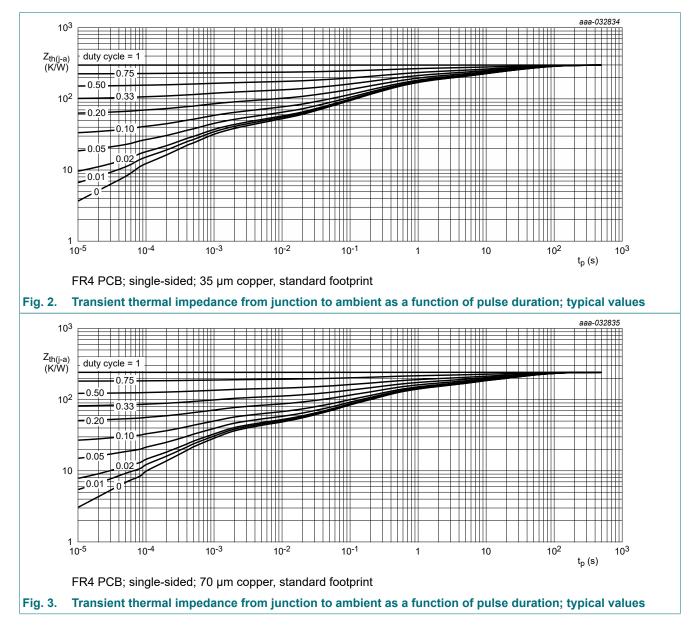
Table 7. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	348	K/W
			[2]	-	-	278	K/W

[1] Device mounted on an FR4 PCB; single-sided; 35 µm copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided; 70 µm copper; tin-plated and standard footprint.



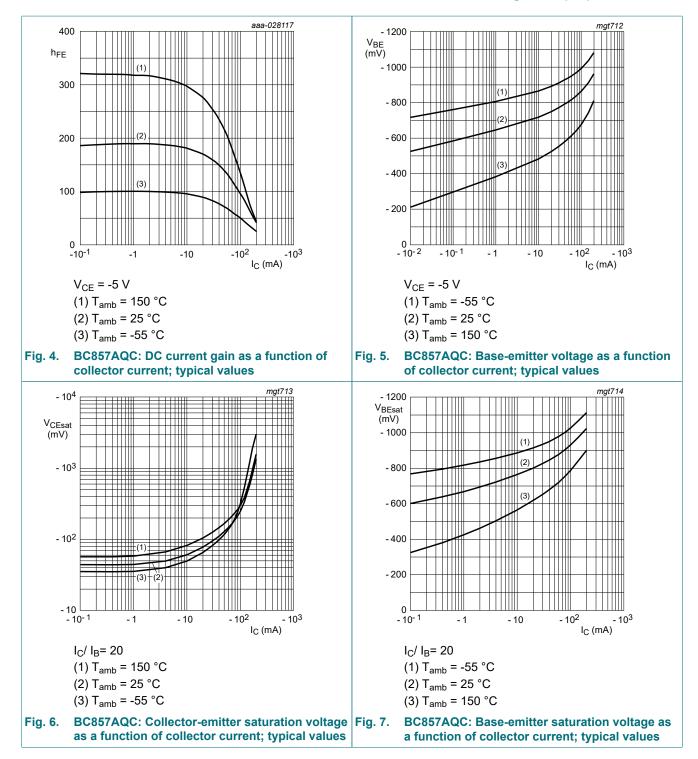
10. Characteristics

Table 8. Characteristics

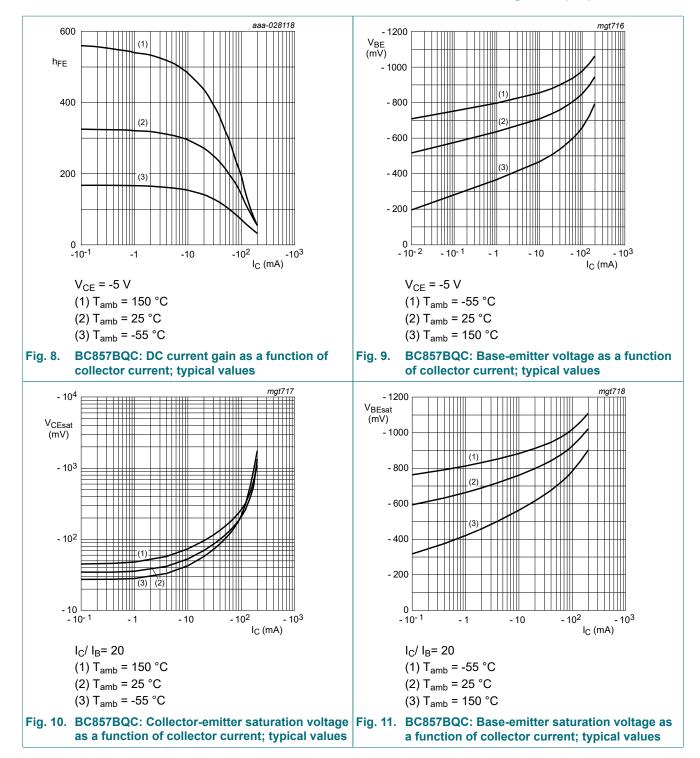
 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-50	-	-	V
V _{(BR)CES}	collector-emitter peak voltage	I _C = -2 mA; I _E = 0 A		-45	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = -100 μA; I _C = 0 A			-	-	V
I _{CBO}	collector-base cut-off V_{CB} = -30 V; I _E = 0 A			-	-	-15	nA
	current	V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C		-	-	-5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$			-	-100	nA
h _{FE}	DC current gain				_		
BC857AQC BC857BQC	$V_{CE} = -5 \text{ V}; \text{ I}_{C} = -2 \text{ mA}$		125	-	250		
			220	-	475		
	BC857CQC			420	-	800	
V _{CEsat}	collector-emitter	I _C = -10 mA; I _B = -0.5 mA		-	-	-300	mV
	saturation voltage	I _C = -100 mA; I _B = -5 mA	[1]	-	-	-650	mV
V _{BE}	base-emitter voltage	V _{CE} = -5 V ; I _C = -2 mA	[2]	-600	-	-750	mV
		V _{CE} = -5 V ; I _C = -10 mA	[2]	-	-	-820	mV
V _{BEsat}	base-emitter saturation	I _C = -10 mA ; I _B = -0.5 mA		-	-700	-	mV
	voltage	I _C = -100 mA ; I _B = -5 mA	[1]	-	-850	-	mV
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz		100	-	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz		-	2	-	pF
C _e	emitter capacitance	V _{EB} = -0.5 V; I _C = i _c = 0 A; f = 1 MHz		-	10	-	pF
NF	noise figure	V_{CE} = -5 V; I _C = -200 μA; R _S = 2 kΩ; f = 1 kHz; B = 200 Hz		-	-	10	dB

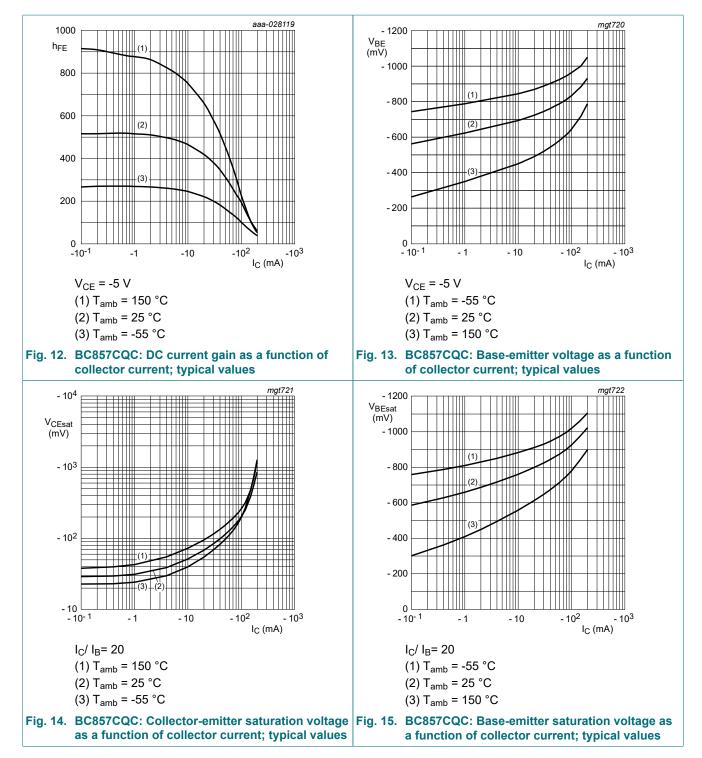
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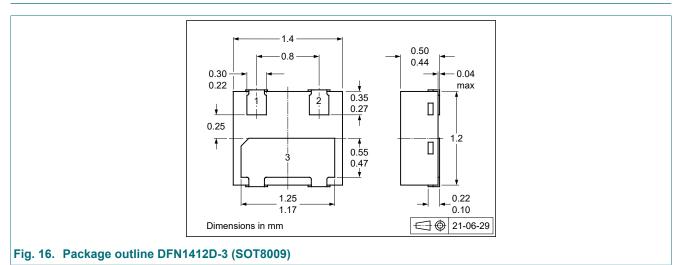
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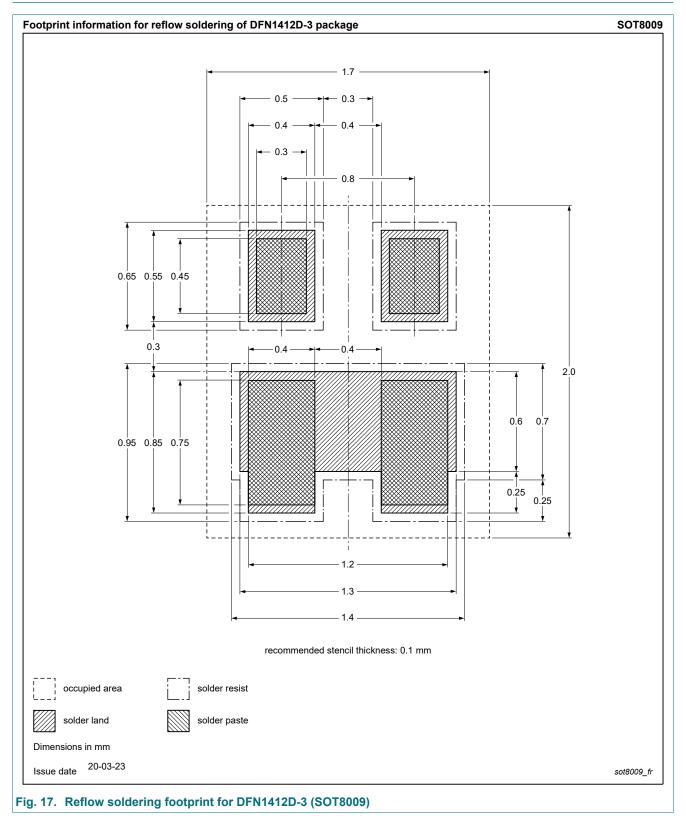


11. Package outline



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12. Soldering



13. Revision history

Table 9. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC857XQC_SER v.1	20211027	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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