**Product data sheet** 

## 1. General description

NPN medium power transistors in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package

### 2. Features and benefits

- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- Three current gain selections
- · High power dissipation capability

## 3. Applications

- Linear voltage regulators
- MOSFET drivers
- · Low-side switches
- Battery-driven devices
- · Power management
- Amplifiers

### 4. Quick reference data

### Table 1. Quick reference data

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base		-	-	80	V
I <sub>C</sub>	collector current			-	-	1	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	2	A
h <sub>FE</sub>	E DC current gain						
	BCX56	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA	[1]	63	-	250	
	BCX56-10		[1]	63	-	160	
	BCX56-16		[1]	100	-	250	

[1] pulsed;  $t_p \le 300 \,\mu s$ ;  $\delta \le 0.02$ 



### 80 V, 1 A NPN medium power transistors

# 5. Pinning information

### Table 2. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		C
2	С	collector		в—
3	В	base		19
			3 2 1	sym042

## 6. Ordering information

### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
BCX56	SC-62	plastic, surface-mounted package; 3 leads;	SOT89			
BCX56-10		1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body				
BCX56-16						

## 7. Marking

#### Table 4. Marking

Type number	Marking code					
BCX56	ВН					
BCX56-10	ВК					
BCX56-16	BL					

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### 80 V, 1 A NPN medium power transistors

## 8. Limiting values

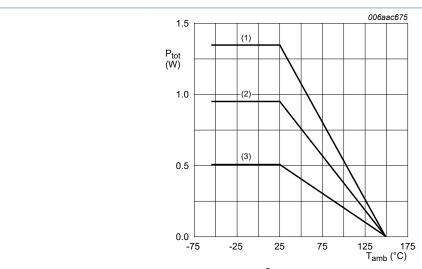
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	100	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	1	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	2	Α
l <sub>B</sub>	base current			-	0.3	Α
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	0.3	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.50	W
			[2]	-	0.95	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>. Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



- (1) FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>
- (2) FR4 PCB, mounting pad for collector 1 cm<sup>2</sup>
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves SOT89

#### 80 V, 1 A NPN medium power transistors

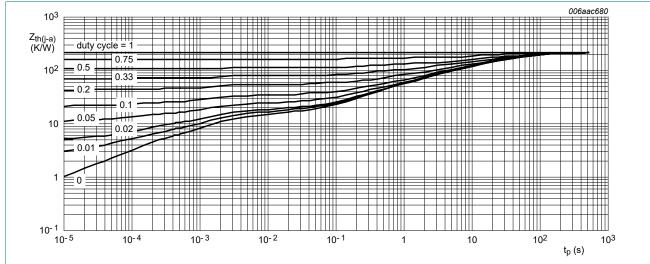
## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

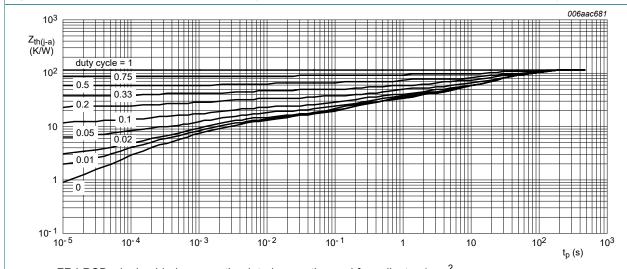
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
			[2]			132	K/W
			[3]			93	K/W
R <sub>(j-sp)</sub>	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm<sup>2</sup>.



FR4 PCB; single-sided copper; tin-plated and standard footprint

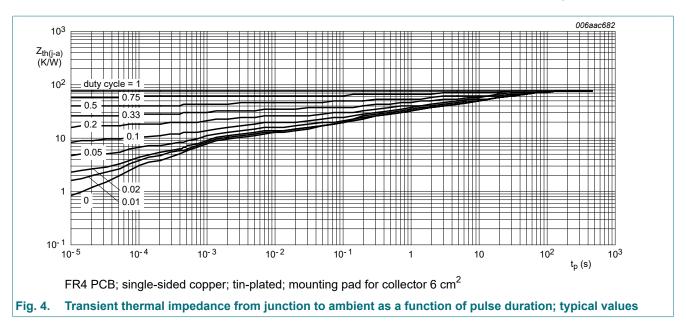
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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## 10. Characteristics

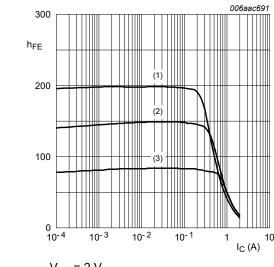
#### **Table 7. Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		100	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = 2 mA; I <sub>B</sub> = 0 A		80	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μA; I <sub>C</sub> = 0 A		5	-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A		-	-	100	nA
	cut-off current	$V_{CB} = 30 \text{ V}; I_{E} = 0 \text{ A}; T_{j} = 150 \text{ °C}$		-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A		-	-	100	nA
h <sub>FE</sub>	DC current gain		'		'	'	
	BCX56	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA		63	-	250	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA		40	-	-	
	BCX56-10	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA		63	-	160	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA		40	-	-	
	BCX56-16	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA	[1]	63	-	-	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA		100	-	250	
		V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA		40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA	[1]	-	-	0.5	V
$V_{BE}$	base-emitter voltage	V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA	[1]	-	-	1	V
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	6	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz		100	180	-	MHz

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

### 80 V, 1 A NPN medium power transistors



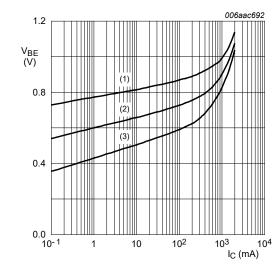
$$V_{CE} = 2 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

DC current gain as a function of collector Fig. 5. current; typical values



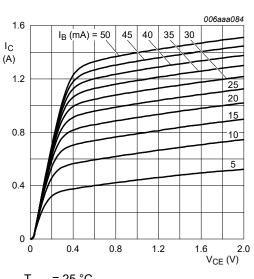
$$V_{CE} = 2 V$$

(1) 
$$T_{amb} = -55$$
 °C

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb}$$
 = 100 °C

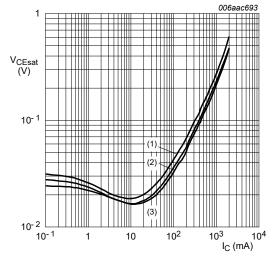
Fig. 7. Base-emitter voltage as a function of collector current; typical values



 $T_{amb}$  = 25 °C

Collector current as a function of collector-Fig. 6. emitter voltage; typical values





 $I_{\rm C}/I_{\rm B} = 10$ 

(1) 
$$T_{amb}$$
 = 100 °C

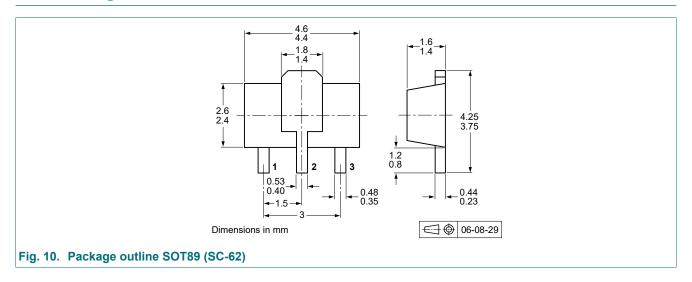
(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Fig. 9.

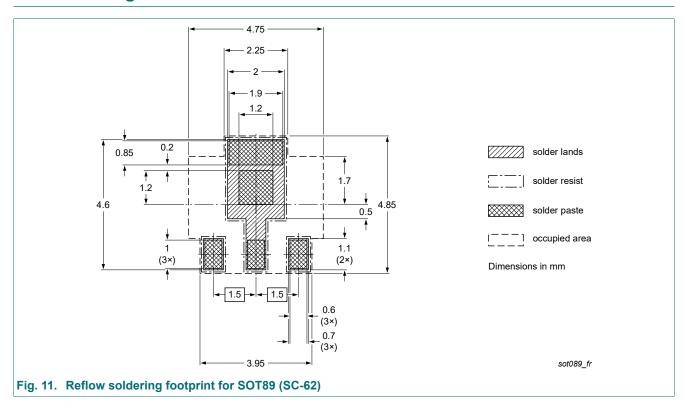
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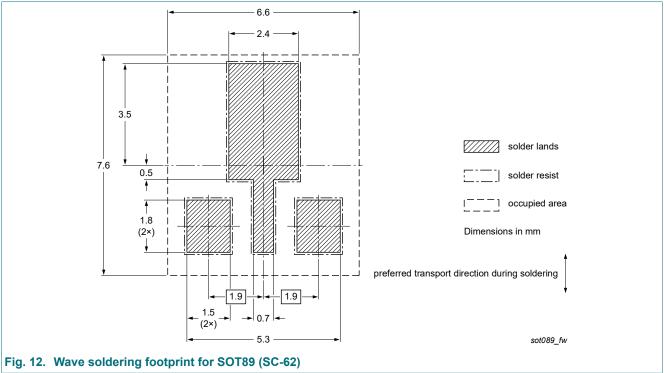
# 11. Package outline



## 80 V, 1 A NPN medium power transistors

## 12. Soldering





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# 13. Revision history

## Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes					
BCX56_SER v.13	20231026	Product data sheet	-	BCX56_SER v.12					
Modifications:	Typos corrected	Typos corrected in "Applications" and "Ordering information"							
BCX56_SER v.12	20230623	Product data sheet	-	BCX56_SER v.11					
BCX56_SER v.11	20230401	Product data sheet	-	BCX56_SER v.10					
BCX56_SER v.10	20220624	Product data sheet	-	BCP56_BCX56_BC56PA v.9					
BCP56_BCX56_BC56PA v.9	20111025	Product data sheet	-	BC639_BCP56_BCX56 v.8					
BC639_BCP56_BCX56 v.8	20070622	Product data sheet	-	BC639_BCP56_BCX56 v.7					
BC639_BCP56_BCX56 v.7	20050308	Product data sheet		BC639_BCP56_BCX56 v.6					
BC639_BCP56_BCX56 v.6	20050303	Product data sheet	CPCN2004050 29	BC635_637_639 v.4 BCP54_55_56 v.5 BCX54_55_56 v.4					
BC635_637_639 v.4	20011010	Product specification	-	BC635_637_639 v.3					
BCX54_55_56 v.5	20030206	Product specification	-	BCX54_55_56 v.4					
BCX54_55_56 v.4	20011010	Product specification	-	BCX54_55_56 v.3					

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#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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