**Product data sheet** 

## 1. General description

NPN high-voltage transistor in a SOT223 Surface-Mounted Device (SMD) plastic package.

PNP complement: BF723

### 2. Features and benefits

- Low feedback capacitance
- AEC-Q101 qualified

## 3. Applications

· General purpose high voltage circuits

### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	250	V
I <sub>C</sub>	collector current		-	-	100	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 20 V; I <sub>C</sub> = 25 mA; T <sub>amb</sub> = 25 °C	50	-	-	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		
3	E	emitter		B —
4	С	collector	<b>□</b> 1 <b>□</b> 2 <b>□</b> 3	Ė
			SC-73 (SOT223)	sym123

# 6. Ordering information

**Table 3. Ordering information** 

Type number	Package				
	Name	Description	Version		
BF722		plastic, surface-mounted package with increased heatsink; 4 leads; 2.3 mm pitch; 6.5 mm x 3.5 mm x 1.65 mm body	<u>SOT223</u>		



NPN high voltage transistor

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
BF722	BF722

# 8. Limiting values

### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	250	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	250	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current			-	200	mA
I <sub>BM</sub>	peak base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.2	W
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>.

### 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	106	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[1]	-	-	25	K/W

<sup>[1]</sup> Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>.

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### 10. Characteristics

**Table 7. Characteristics** 

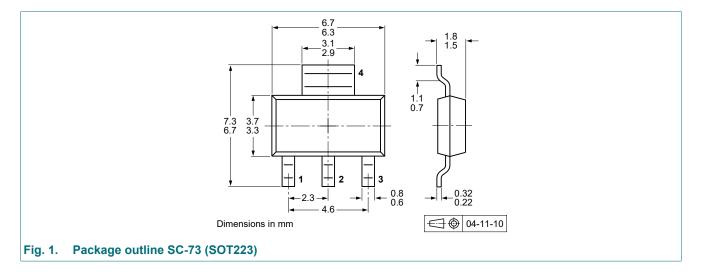
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 200 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	10	nA
	current	V <sub>CB</sub> = 200 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 20 V; I <sub>C</sub> = 25 mA; T <sub>amb</sub> = 25 °C	50	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 30 \text{ mA}; I_B = 5 \text{ mA}; T_{amb} = 25 ^{\circ}C$	-	-	0.6	V
C <sub>re</sub>	feedback capacitance	$V_{CB} = 30 \text{ V}; I_C = 0 \text{ A}; i_c = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 ^{\circ}\text{C}$	-	-	1.6	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	60	-	-	MHz

## 11. Test information

### **Quality information**

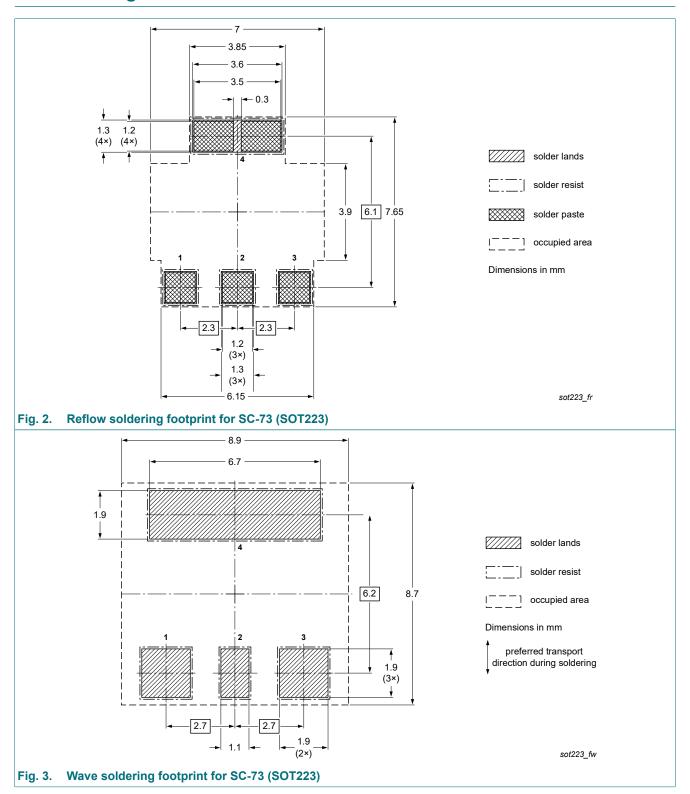
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline



### NPN high voltage transistor

# 13. Soldering



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# 14. Revision history

#### **Table 8. Revision history**

	- J					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BF722 v.3	20230628	Product data sheet	-	BF720_722 v.2		
Modifications:	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Family data sheet splitted to single type data sheets.</li> </ul>					
BF720_722 v.2	19990421	Product data sheet	-	BF720_722 v.1		
BF720_722 v.1	19961205	Product specification	-	-		

#### NPN high voltage transistor

### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BF722

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