**Product data sheet** 

# 1. General description

NPN high-voltage transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: BF823-Q

## 2. Features and benefits

- Low current (max. 50 mA)
- · High voltage (max. 300 V)
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

· Telephony and professional communication equipment

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter	-	-	250	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	250	V
Ic	collector current		-	-	50	mA
h <sub>FE</sub>	DC current gain	$V_{CE} = 20 \text{ V}; I_{C} = 25 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	50	-	-	
f <sub>T</sub>	transition frequency	$V_{CE}$ = 10 V; $I_{C}$ = 10 mA; f = 100 MHz; $T_{amb}$ = 25 °C	60	-	-	MHz

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	С
2	Е	emitter		j
3	С	collector		В
			1 2	É
			SOT23	sym021



### NPN high voltage transistor

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package	ckage				
	Name	Description	Version			
BF822-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
BF822-Q	1X%

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	250	V
$V_{CEO}$	collector-emitter voltage	open base		-	250	V
$V_{EBO}$	emitter-base voltage	open collector		-	5	V
I <sub>C</sub>	collector current			-	50	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	100	mA
I <sub>BM</sub>	peak base current			-	50	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Transistor mounted on an FR4 printed-circuit board.

## 9. Thermal characteristics

### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Transistor mounted on an FR4 printed-circuit board.

## NPN high voltage transistor

## 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 200 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	10	nA
	current	V <sub>CB</sub> = 200 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 20 V; I <sub>C</sub> = 25 mA; T <sub>amb</sub> = 25 °C	50	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 30 \text{ mA}; I_B = 5 \text{ mA}; T_{amb} = 25 ^{\circ}C$	-	-	600	mV
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	60	-	-	MHz
C <sub>re</sub>	feedback capacitance	$i_c$ = 0 A; $V_{CB}$ = 30 V; f = 1 MHz; $I_C$ = 0 A; $T_{amb}$ = 25 °C	-	-	1.6	pF

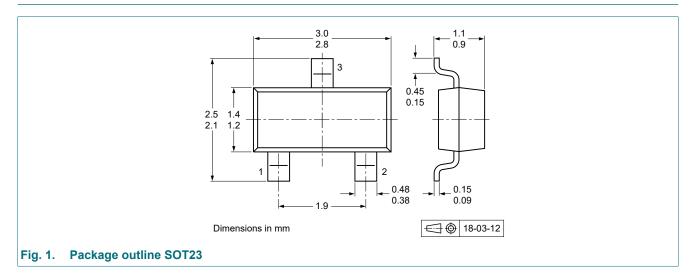
## 11. Test information

## **Quality information**

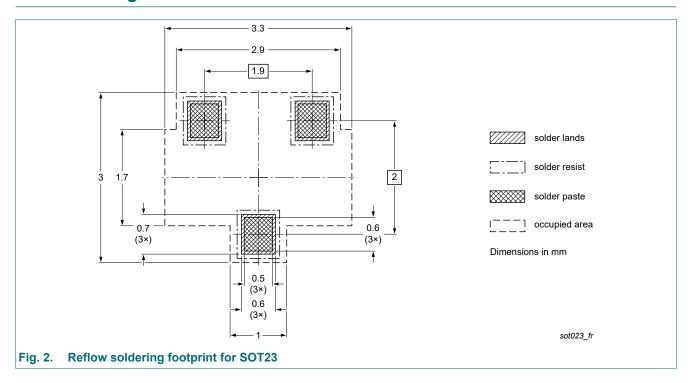
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## NPN high voltage transistor

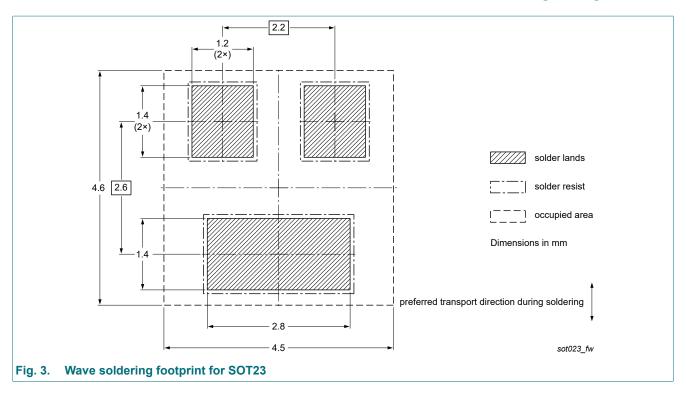
# 12. Package outline



# 13. Soldering



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# 14. Revision history

#### **Table 8. Revision history**

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
BF822-Q v.2	20221213	Product data sheet	-	BF822-Q v.1				
Modifications:	PNP complement is adju	PNP complement is adjusted in General description.						
BF822-Q v.1	20211020	Product data sheet	-	-				

### NPN high voltage transistor

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Date of release: 13 December 2022

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