

N-channel 60 V, 59 mΩ standard level MOSFET in LFPAK56 7 May 2013 Product data sheet

#### 1. General description

Standard level N-channel MOSFET in an LFPAK56 (Power SO8) package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

### 2. Features and benefits

- Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True standard level gate with  $V_{GS(th)}$  rating of greater than 1 V at 175 °C

### 3. Applications

- 12 V Automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

### 4. Quick reference data

Table 1. Quick reference data								
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C		-	-	60	V	
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>		-	-	17	А	
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>		-	-	37	W	
Static character	Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 5 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>		-	38.2	59	mΩ	
Dynamic characteristics								
Q <sub>GD</sub>	gate-drain charge	I <sub>D</sub> = 5 A; V <sub>DS</sub> = 48 V; V <sub>GS</sub> = 10 V; Fig. 13; Fig. 14		-	2.8	-	nC	

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### 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source	mb	D
2	S	source		
3	S	source	a	G-UFT4
4	G	gate	មុប្បូប្	mbb076 S
mb	D	mounting base; connected to drain	1 2 3 4 LFPAK56; Power- SO8 (SOT669)	

### 6. Ordering information

Table 3.       Ordering information							
Type number	Package						
	Name	Description	Version				
BUK7Y59-60E	LFPAK56; Power-SO8	Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads	SOT669				

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
BUK7Y59-60E	75960E

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

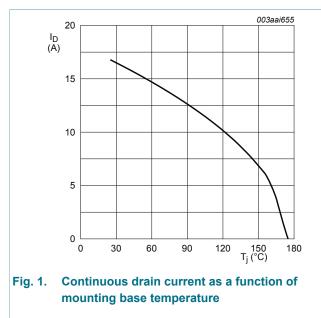
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C	-	60	V
V <sub>DGR</sub>	drain-gate voltage	R <sub>GS</sub> = 20 kΩ	-	60	V
V <sub>GS</sub>	gate-source voltage	T <sub>j</sub> ≤ 175 °C; DC	-20	20	V
I <sub>D</sub>	drain current	T <sub>mb</sub> = 25 °C; V <sub>GS</sub> = 10 V; <u>Fig. 1</u>	-	17	А
		T <sub>mb</sub> = 100 °C; V <sub>GS</sub> = 10 V; <u>Fig. 1</u>	-	11.9	А
I <sub>DM</sub>	peak drain current	$T_{mb}$ = 25 °C; pulsed; $t_p \le 10 \ \mu s$ ; Fig. 4	-	67	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>	-	37	W
T <sub>stg</sub>	storage temperature		-55	175	°C

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Symbol	Parameter	Conditions		Min	Мах	Unit
Tj	junction temperature			-55	175	°C
Source-drain diode						
I <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C		-	17	А
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^\circ C$		-	67	А
Avalanche	Avalanche ruggedness					
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$ I_D = 17 \text{ A}; V_{sup} \le 60 \text{ V}; \text{ R}_{GS} = 50 \Omega; V_{GS} = 10 \text{ V}; \text{ T}_{j(init)} = 25 \text{ °C}; \text{ unclamped}; Fig. 3 $	[1][2]	-	8.8	mJ

[1] Single-pulse avalanche rating limited by maximum junction temperature of 175  $^\circ$ C.

[2] Refer to application note AN10273 for further information.



 $V_{GS} \ge 10V$ 

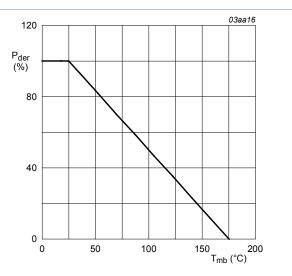
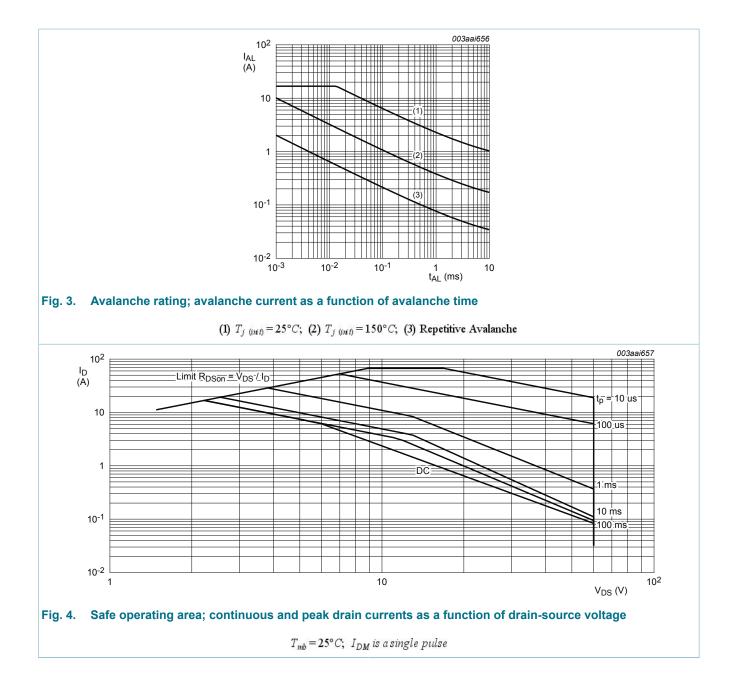


Fig. 2. Normalized total power dissipation as a function of mounting base temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

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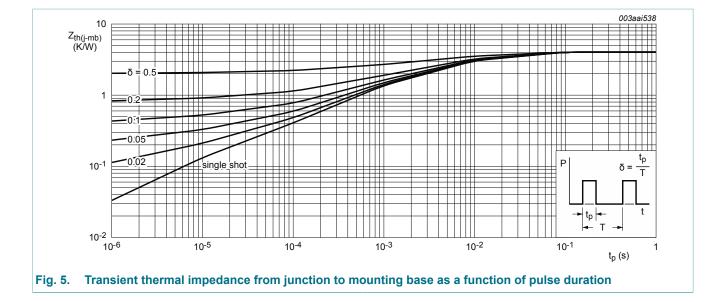
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### 9. Thermal characteristics

Table 6. The	rmal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	-	4.03	K/W

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### **10. Characteristics**

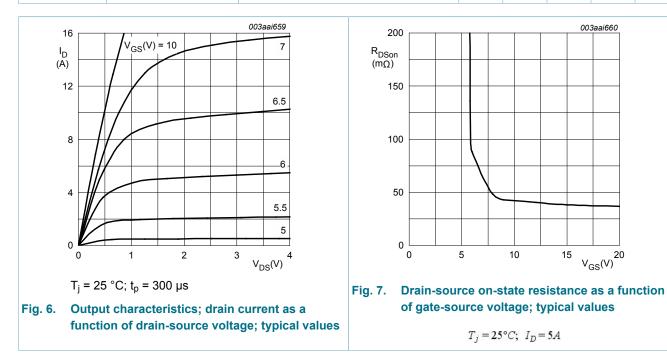
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V <sub>(BR)DSS</sub>	drain-source	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	60	-	-	V
breakdown	breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = -55 °C	54	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C};$ Fig. 9; Fig. 10	2.4	3	4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C};$ Fig. 9	-	-	4.5	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C};$ Fig. 9	1	-	-	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 60 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	0.01	1	μA
		V <sub>DS</sub> = 60 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C	-	-	500	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	2	100	nA
		$V_{GS}$ = -20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	2	100	nA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 5 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	38.2	59	mΩ
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 5 A; T <sub>j</sub> = 175 °C; Fig. 11; Fig. 12	-	-	132	mΩ
Dynamic ch	aracteristics			_		
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 5 A; V <sub>DS</sub> = 48 V; V <sub>GS</sub> = 10 V;	-	7.8	-	nC
Q <sub>GS</sub>	gate-source charge	Fig. 13; Fig. 14	-	1.7	-	nC
Q <sub>GD</sub>	gate-drain charge		-	2.8	-	nC

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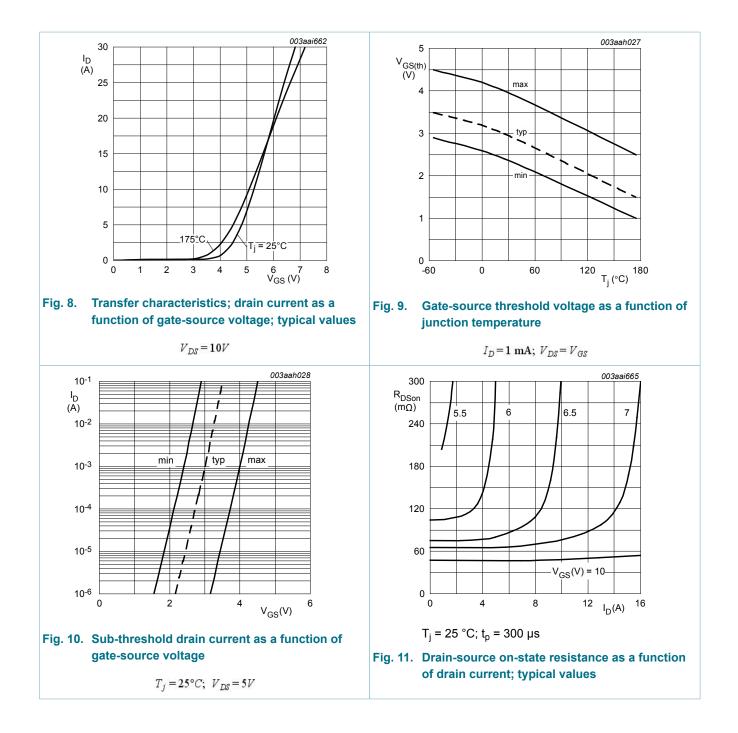
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
C <sub>iss</sub>	input capacitance	$V_{GS}$ = 0 V; $V_{DS}$ = 25 V; f = 1 MHz;		-	371	494	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C; <u>Fig. 15</u>		-	69	82	pF
C <sub>rss</sub>	reverse transfer capacitance			-	53	73	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 45 V; R <sub>L</sub> = 5 Ω; V <sub>GS</sub> = 10 V; R <sub>G(ext)</sub> = 5 Ω		-	3.8	-	ns
t <sub>r</sub>	rise time			-	5.5	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	6.5	-	ns
t <sub>f</sub>	fall time			-	5.2	-	ns
Source-drain diode							
V <sub>SD</sub>	source-drain voltage	$I_{S} = 5 \text{ A}; V_{GS} = 0 \text{ V}; \text{ T}_{j} = 25 \text{ °C}; \text{ Fig. 16}$		-	0.85	1.2	V
t <sub>rr</sub>	reverse recovery time	$I_{\rm S}$ = 5 A; dI <sub>S</sub> /dt = -100 A/µs; V <sub>GS</sub> = 0 V;		-	14.5	-	ns
Q <sub>r</sub>	recovered charge	V <sub>DS</sub> = 25 V		-	10.3	-	nC



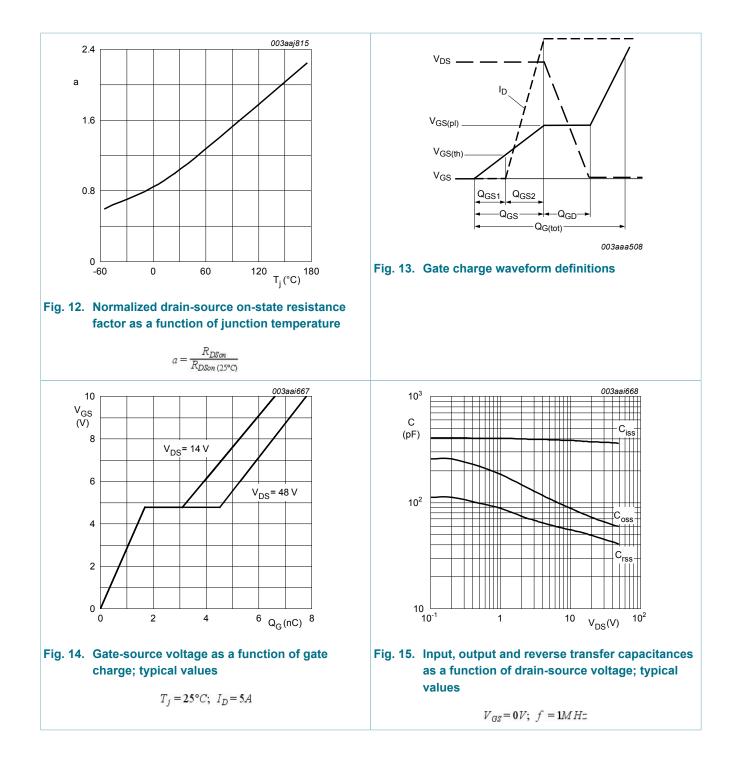
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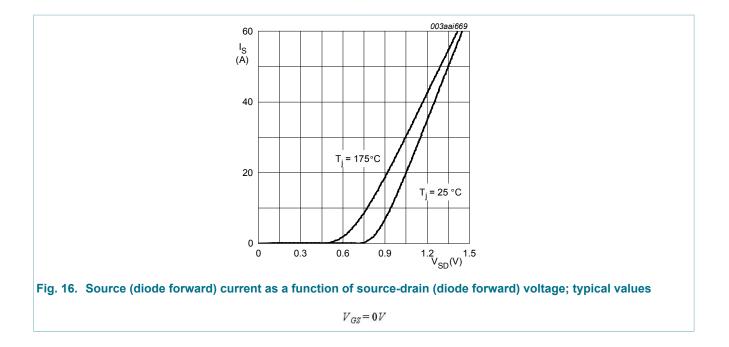


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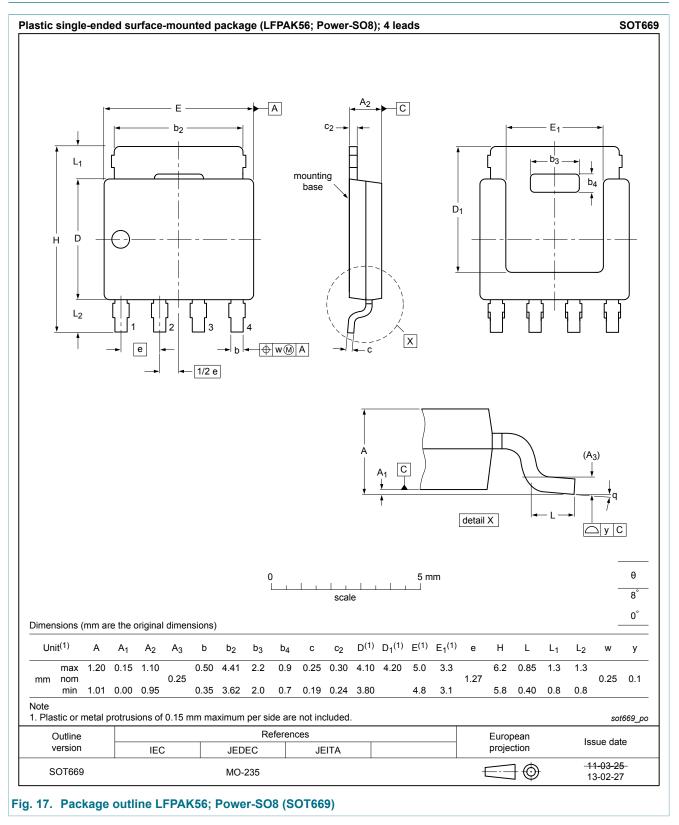
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#### N-channel 60 V, 59 mΩ standard level MOSFET in LFPAK56

### **11. Package outline**



BUK7Y59-60E

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#### N-channel 60 V, 59 mΩ standard level MOSFET in LFPAK56

#### **12. Legal information**

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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