

N-channel 60 V, 15 mΩ logic level MOSFET in LFPAK33

19 September 2016

Product data sheet

### 1. General description

Logic level N-channel MOSFET in an LFPAK33 (Power33) package using TrenchMOS technology. This product has been designed and qualified to AEC Q101 standard for use in high performance automotive applications.

### 2. Features and benefits

- Q101 compliant
- Repetitive avalanche rated
- Suitable for thermally demanding environments due to 175 °C rating
- True logic level gate with V<sub>GS(th)</sub> rating of greater than 0.5 V at 175 °C

### 3. Applications

- 12 V automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

### 4. Quick reference data

Table 1. Qui	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C	-	-	60	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 5 V; T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>	-	-	47	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>	-	-	75	W
Static charact	eristics					
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 5 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	12.5	15	mΩ
Dynamic char	acteristics					
Q <sub>GD</sub>	gate-drain charge	$I_D$ = 10 A; $V_{DS}$ = 48 V; $V_{GS}$ = 5 V; $T_j$ = 25 °C; Fig. 13; Fig. 14	-	6.4	-	nC

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### 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	Source		D
2	S	Source		
3	S	Source	$\bigcirc$	G-UT 4
4	G	Gate		mbb076 S
mb	D	Mounting base; connected to drain	LFPAK33 (SOT1210)	

# 6. Ordering information

Table 3. Ordering int	formation				
Type number	Package				
	Name	Description	Version		
BUK9M15-60E	LFPAK33	Plastic single ended surface mounted package (LFPAK33); 8 leads	SOT1210		

# 7. Marking

Table 4. Marking codes	
Type number	Marking code
BUK9M15-60E	91560E

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

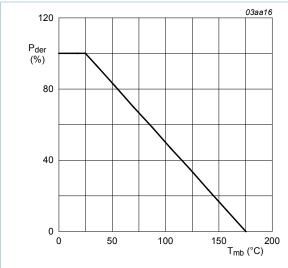
Symbol	Parameter	Conditions		Min	Мах	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		-	60	V
V <sub>DGR</sub>	drain-gate voltage	R <sub>GS</sub> = 20 kΩ		-	60	V
V <sub>GS</sub>	gate-source voltage	DC; T <sub>j</sub> ≤ 175 °C		-10	10	V
		Pulsed; T <sub>j</sub> ≤ 175 °C	[1][2]	-15	15	V
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>		-	75	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 5 V; T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>		-	47	А
		V <sub>GS</sub> = 5 V; T <sub>mb</sub> = 100 °C; <u>Fig. 2</u>		-	33	А
I <sub>DM</sub>	peak drain current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^\circ C$ ; Fig. 3		-	188	А

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#### N-channel 60 V, 15 mΩ logic level MOSFET in LFPAK33

Symbol	Parameter	Conditions		Min	Max	Unit
T <sub>stg</sub>	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drai	in diode					
I <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C		-	47	А
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^\circ C$		-	188	А
Avalanche r	ruggedness					
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$\label{eq:ID} \begin{array}{l} I_D = 47 \text{ A}; \ V_{sup} \leq 60; \ R_{GS} = 50 \ \Omega; \\ V_{GS} = 5 \ V; \ T_{j(init)} = 25 \ ^\circ\text{C}; \ unclamped; \\ \hline \ Fig. \ 4 \end{array}$	[3][4]	-	39	mJ

- Accumulated pulse duration up to 50 hours delivers zero defect ppm. Significantly longer life times are achieved by lowering  $\rm T_{j}$  and or  $\rm V_{GS}$ [1]
- [2]
- Single-pulse avalanche rating limited by maximum junction temperature of 175 °C. [3]
- Refer to application note AN10273 for further information. [4]





$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

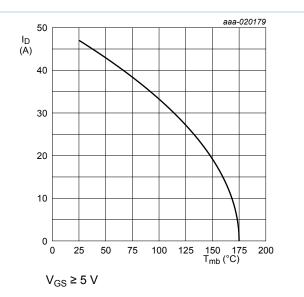
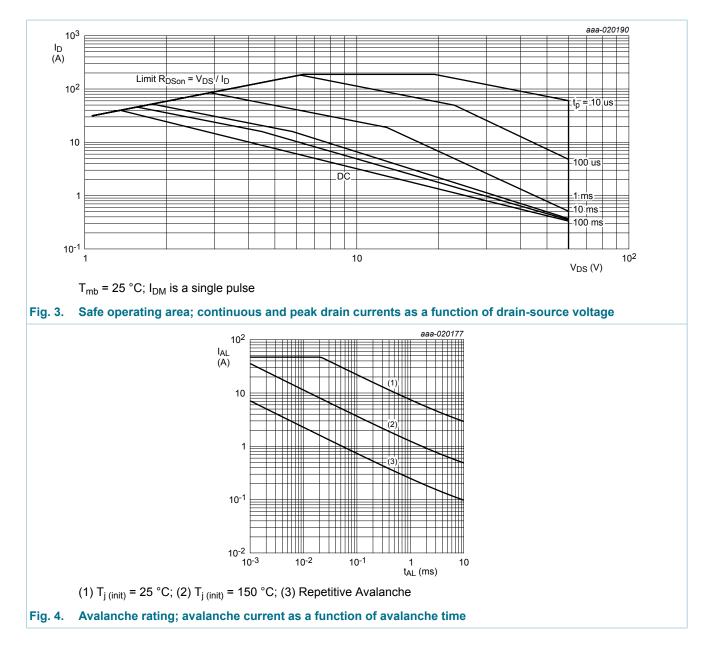


Fig. 2. Continuous drain current as a function of mounting base temperature

$$I_D = 47 \times \sqrt{\frac{175^{\circ}C - T_{mb}}{150^{\circ}C}} \text{ for } T_{mb} \ge 25^{\circ}C$$

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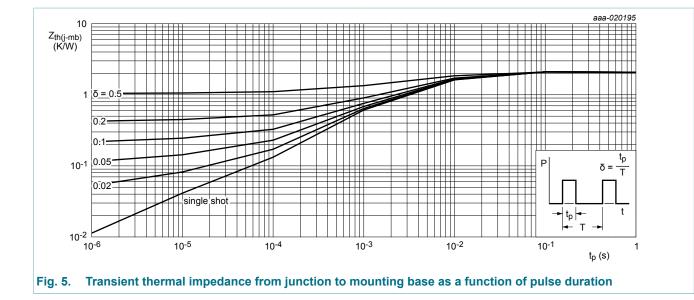


# 9. Thermal characteristics

Table 6. The	rmal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	1.82	2	K/W

# BUK9M15-60E

#### N-channel 60 V, 15 m $\Omega$ logic level MOSFET in LFPAK33



### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	l	I			
(BR)000	drain-source	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C	60	-	-	V
	breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = -55 °C	54	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C; Fig. 9; Fig. 10	1.4	1.7	2.1	V
		$I_D$ = 1 mA; $V_{DS}$ = $V_{GS}$ ; $T_j$ = -55 °C; Fig. 10	-	-	2.45	V
	I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 175 °C; Fig. 10	0.5	-	-	V	
I <sub>DSS</sub> drain leakage curre	drain leakage current	$V_{DS}$ = 60 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	0.01	1	μA
	urain leakage cuffent	V <sub>DS</sub> = 60 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C	-	-	500	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 10 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	2	100	nA
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	2	100	nA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 5 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>	-	12.5	15	mΩ
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 25 °C; Fig. 11	-	11	13	mΩ
		V <sub>GS</sub> = 5 V; I <sub>D</sub> = 10 A; T <sub>j</sub> = 175 °C; Fig. 12	-	-	34	mΩ
Dynamic cł	naracteristics	· · · · · ·	I	1	1	
Q <sub>G(tot)</sub>	total gate charge	$I_D$ = 10 A; $V_{DS}$ = 48 V; $V_{GS}$ = 5 V;	-	17	-	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C; <u>Fig. 13; Fig. 14</u>	-	4.1	-	nC

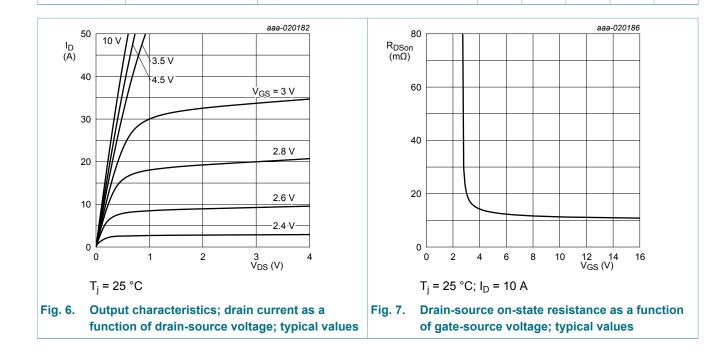
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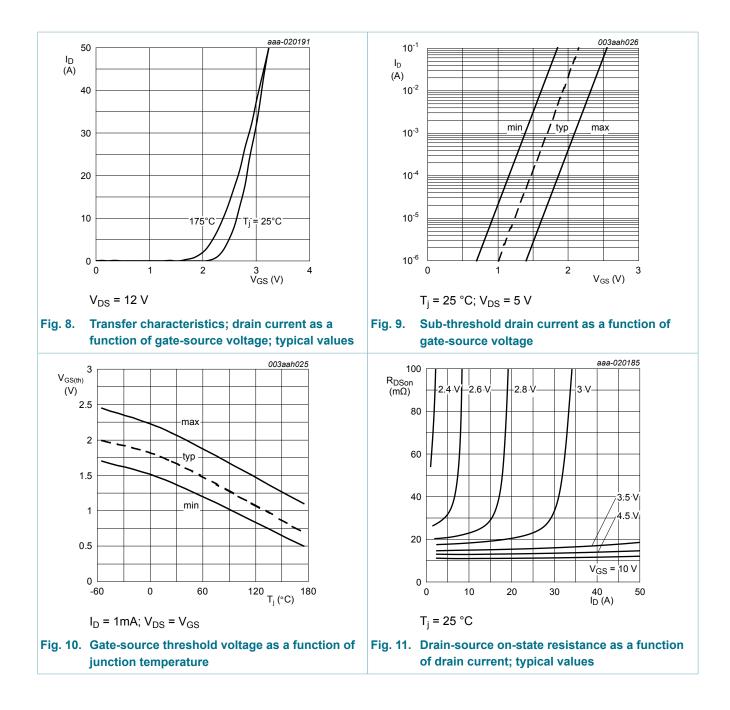
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Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Q <sub>GD</sub>	gate-drain charge		-	6.4	-	nC
C <sub>iss</sub>	input capacitance	$V_{DS}$ = 25 V; $V_{GS}$ = 0 V; f = 1 MHz;	-	1677	2230	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C; <u>Fig. 15</u>	-	160	192	pF
C <sub>rss</sub>	reverse transfer capacitance	V <sub>DS</sub> = 45 V; R <sub>L</sub> = 4.3 Ω; V <sub>GS</sub> = 5 V;	-	85	116	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 45 V; $R_L$ = 4.3 Ω; $V_{GS}$ = 5 V; $R_{G(ext)}$ = 5 Ω; $T_j$ = 25 °C	-	10.3	-	ns
t <sub>r</sub>	rise time		-	16.9	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	24	-	ns
t <sub>f</sub>	fall time		-	15.4	-	ns
Source-dra	in diode					
V <sub>SD</sub>	source-drain voltage	$I_{S}$ = 10 A; $V_{GS}$ = 0 V; $T_{j}$ = 25 °C; <u>Fig. 16</u>	-	0.81	1.2	V
t <sub>rr</sub>	reverse recovery time	$I_{\rm S}$ = 10 A; dI <sub>S</sub> /dt = -100 A/µs; V <sub>GS</sub> = 0 V;	-	19.3	-	ns
Q <sub>r</sub>	recovered charge	V <sub>DS</sub> = 25 V; T <sub>j</sub> = 25 °C	-	17.3	-	nC



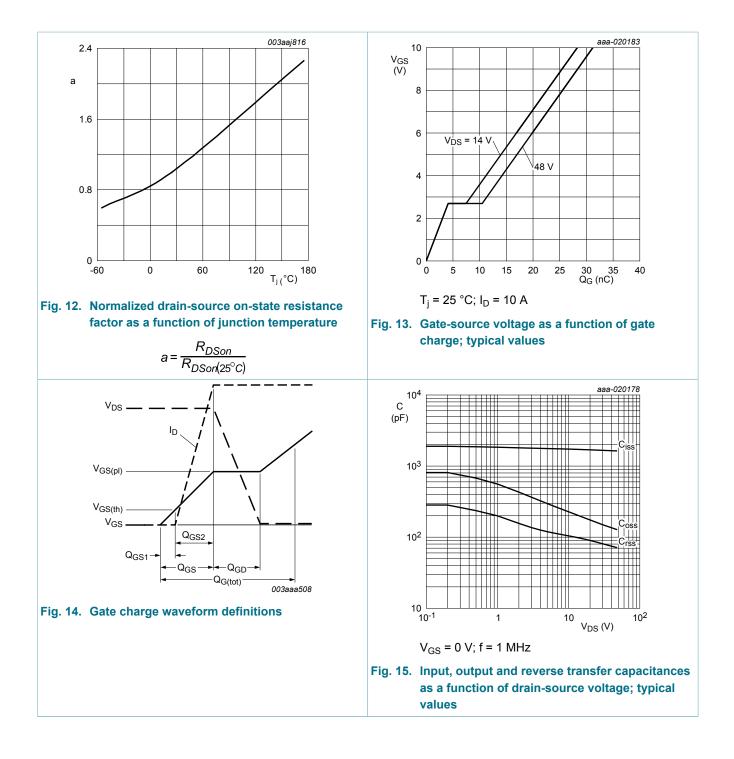
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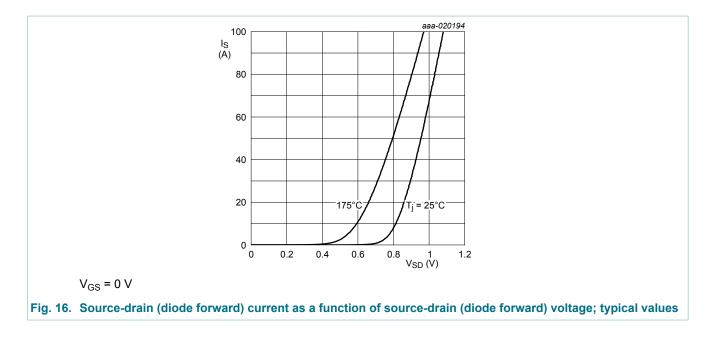
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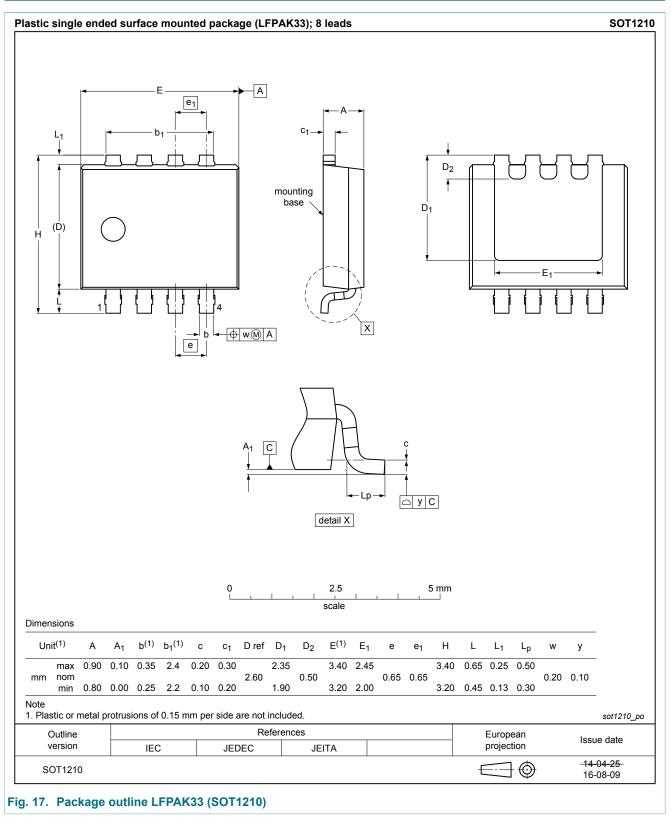


# **11. Application information**

For guidance on how to use and understand this datasheet, please refer to application note <u>AN11158</u> "Understanding power MOSFET datasheet parameters".

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### 12. Package outline



BUK9M15-60E

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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#### N-channel 60 V, 15 m $\Omega$ logic level MOSFET in LFPAK33

### 14. Contents

1	General description	1
2	Features and benefits	1
3	Applications	1
4	Quick reference data	1
5	Pinning information	2
6	Ordering information	2
7	Marking	2
8	Limiting values	2
9	Thermal characteristics	4
10	Characteristics	5
11	Application information	9
12	Package outline	10
13	Legal information	11
13.1		
13.1	Data sheet status	
13.1 13.2	Definitions	11
		11

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