40 V, 200 mA PNP switching transistor

27 May 2024

Product data sheet

1. General description

PNP switching transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

NPN complement: MMBT3904-Q

2. Features and benefits

- Collector current capability I_C = -200 mA
- Collector-emitter voltage V_{CEO} = -40 V
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

· General switching and amplification

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-40	V
I _C	collector current		-	-	-200	mA
h _{FE}	DC current gain	V_{CE} = -1 V; I_{C} = -10 mA; T_{amb} = 25 °C	100	-	300	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	Е	emitter		C
3	С	collector		В—
				E sym132
			SOT23	3911132



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6. Ordering information

Table 3. Ordering information

Type number	Package	kage				
	Name	Description	Version			
MMBT3906-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
MMBT3906-Q	7B%

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-40	V
V _{CEO}	collector-emitter voltage	open base		-	-40	V
V_{EBO}	emitter-base voltage	open collector		-	-6	V
I _C	collector current			-	-200	mA
I _{CM}	peak collector current			-	-200	mA
I _{BM}	peak base current			-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

^[1] Device mounted on an FR4 , PCB; single-sided copper; tin-plated and standard footprint.

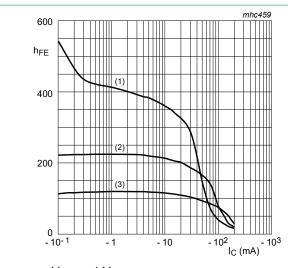
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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = -30 \text{ V}; I_{E} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-50	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = -6 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-50	nA
h _{FE}	DC current gain	V _{CE} = -1 V; I _C = -0.1 mA; T _{amb} = 25 °C	60	-	-	
		V_{CE} = -1 V; I_{C} = -1 mA; T_{amb} = 25 °C	80	-	-	
		V _{CE} = -1 V; I _C = -10 mA; T _{amb} = 25 °C	100	-	300	
		V _{CE} = -1 V; I _C = -50 mA; T _{amb} = 25 °C	60	-	-	
		V _{CE} = -1 V; I _C = -100 mA; T _{amb} = 25 °C	30	-	-	
V _{CEsat}	collector-emitter	I_C = -10 mA; I_B = -1 mA; T_{amb} = 25 °C	-	-	-250	mV
	saturation voltage	I _C = -50 mA; I _B = -5 mA; T _{amb} = 25 °C	-	-	-400	mV
V _{BEsat}	base-emitter saturation voltage	I _C = -10 mA; I _B = -1 mA; T _{amb} = 25 °C	-	-	-850	V
		I_C = -50 mA; I_B = -5 mA; T_{amb} = 25 °C	-	-	-950	V
C _c	collector capacitance	$V_{CB} = -5 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$	-	-	4.5	pF
C _e	emitter capacitance	V_{EB} = -500 mV; I_{C} = 0 A; i_{c} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	-	10	pF
f _T	transition frequency	V_{CE} = -20 V; I_{C} = -10 mA; f = 100 MHz; T_{amb} = 25 °C	250	-	-	MHz
NF	noise figure	V_{CE} = -5 V; I_{C} = -100 μA; R_{S} = 1 kΩ; f = 10 Hz to 15.7 kHz	-	-	4	dB
Switching t	imes (between 10 % and 90) % levels)	'			
t _d	delay time	I _{Bon} = -1 mA; I _{Boff} = 1 mA; I _{Con} = −10	-	-	35	ns
t _r	rise time	mA; T _{amb} = 25 °C	-	-	35	ns
t _s	storage time		-	-	225	ns
t _f	fall time		-	-	75	ns

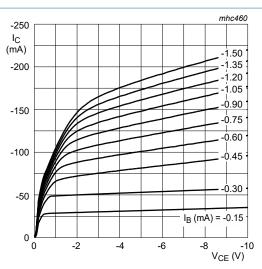
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$$(1) I_{amb} = 150^{\circ}$$

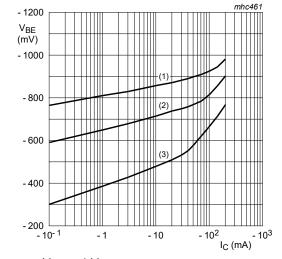
(2)
$$T_{amb} = 25 \, ^{\circ}C$$

Fig. 1. DC current gain as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 2. Collector current as a function of collectoremitter voltage; typical values



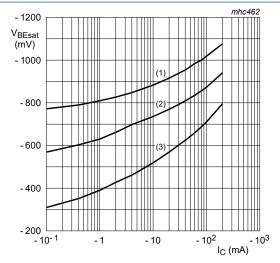
 V_{CE} = -1 V

$$(1) T_{amb} = -55 °C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 3. Base-emitter voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B}=10$$

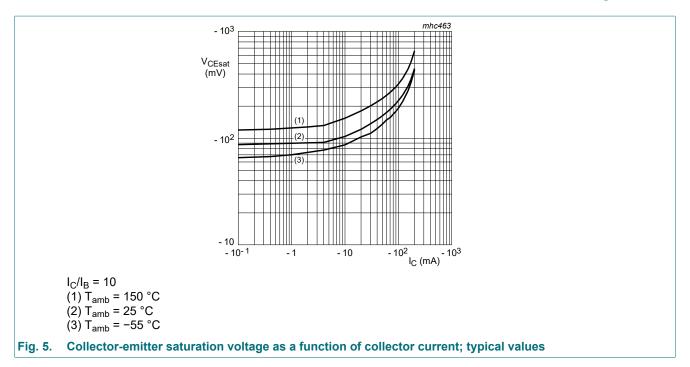
(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

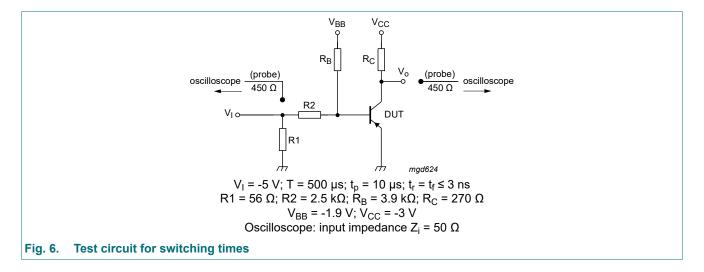
(3) $T_{amb} = 150 \, ^{\circ}C$

Fig. 4. Base-emitter saturation voltage as a function of collector current; typical values

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11. Test information

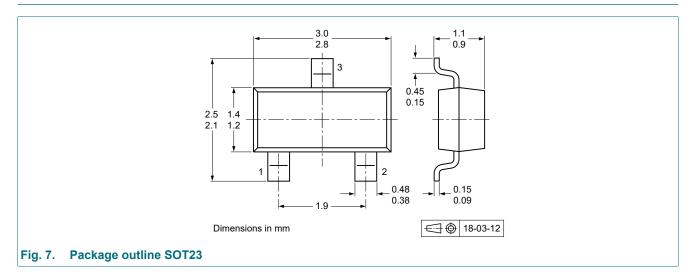


Quality information

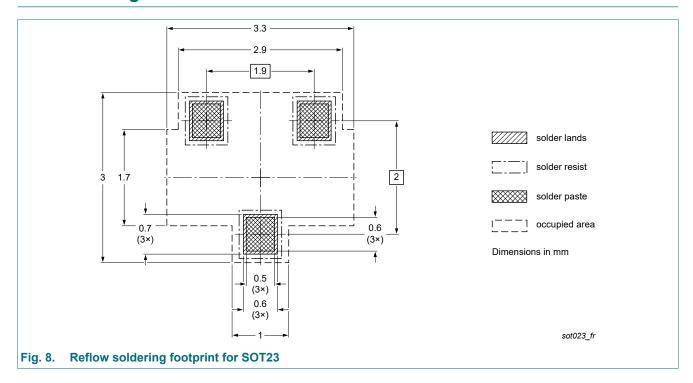
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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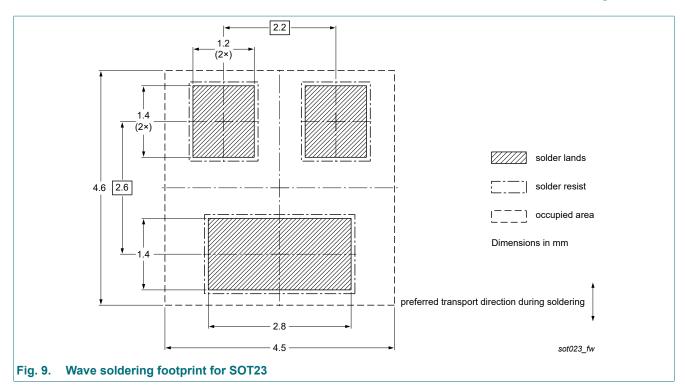
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBT3906-Q v.1	YYYYMMDD	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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