1. General description

ESD protection device in an ultra small DFN1412D-3 (SOT8009) leadless Surface-Mounted Device (SMD) plastic package with side wettable flanks, designed to protect two lines from the damage caused by ElectroStatic Discharge (ESD) and other transients.

2. Features and benefits

- Reverse stand-off voltage: V_{RWM} = 24 V
- Low clamping voltage: V_{CL} = 33 V at I_{PP} = 2.6 A
- ESD protection up to 23 kV (IEC 61000-4-2)
- ESD protection up to 23 kV (ISO 10605)
- Low capacitance: C_d = 8 pF
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Computers and peripherals
- Audio and video equipment
- · Cellular handsets and accessories
- · Automotive electronic control units
- Portable electronics

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	24	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1] [2]	-	-	2.6	А
V _{CL}	clamping voltage	I_{PPM} = 2.6 A; t_p = 8/20 μ s; T_{amb} = 25 °C	[2] [3]	-	33	42	V

- [1] According to IEC 61000-4-5
- [2] Measured from pin 1 or 2 to pin 3
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	الم يوسي إل	
2	K2	cathode (diode 2)	3	,, [14.52]
3	СС	common cathode	Transparent top view DFN1412D-3 (SOT8009)	K1 CC K2 006aaa155

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
MMBZ27VBQC-Q		plastic, leadless ultra small outline package with sidewettable flanks (SWF); 3 terminals; 0.8 mm pitch; 1.4 mm x 1.2 mm x 0.48 mm body	SOT8009		

7. Marking

Table 4. Marking codes

Type number	Marking code
MMBZ27VBQC-Q	Q5

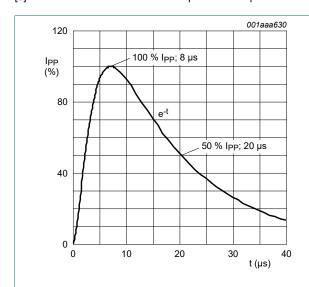
8. Limiting values

Table 5. Limiting values

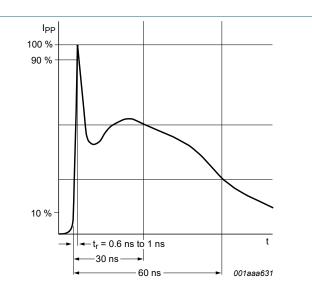
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I _{PPM}	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	2.6	Α
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	um ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[3] [2]	-	23	kV
	voltage	ISO10605; contact discharge; C = 330 pF, R = 330 Ω	[3] [2]	-	20	kV
		ISO10605; contact discharge; C = 150 pF, R = 330Ω	[3] [2]	-	23	kV

- According to IEC 61000-4-5 Measured from pin 1 or 2 to pin 3
- Device stressed with ten non-repetitive ESD pulses



8/20 µs pulse waveform according to Fig. 1. IEC 61000-4-5



ESD pulse waveform according to Fig. 2. IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	24	V
V _{BR}	breakdown voltage	I _R = 10 mA; T _{amb} = 25 °C	[1]	25.5	-	35.5	V
I _{RM}	reverse leakage current	V _{RWM} = 24 V; T _{amb} = 25 °C	[1]	-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	6	8	pF
V _{CL}	clamping voltage	I_{PPM} = 2.6 A; t_p = 8/20 µs; T_{amb} = 25 °C	[1] [2]	-	33	42	V
		I_{PP} = 16 A; t_p = 100 ns; T_{amb} = 25 °C	[1] [3]	-	33	-	V

- [1] Measured from pin 1 or 2 to pin 3
- [2] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5
- [3] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

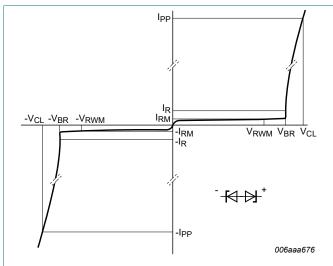


Fig. 3. V-I characteristics for a bidirectional ESD protection diode

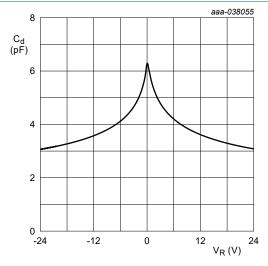


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

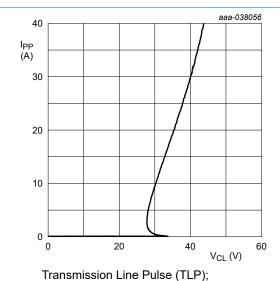
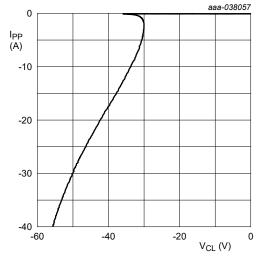


Fig. 5. Positive clamping voltage (TLP); typical values



Transmission Line Pulse (TLP); $t_p = 100 \text{ ns}$

Fig. 6. Negative clamping voltage (TLP); typical values

 $t_{\rm p} = 100 \; \rm ns$

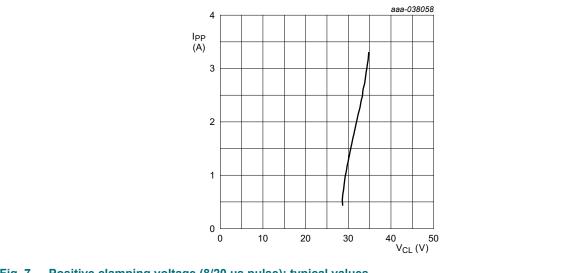
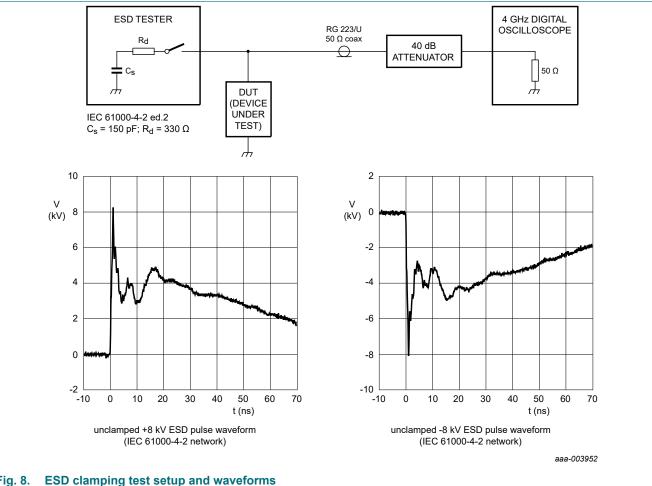


Fig. 7. Positive clamping voltage (8/20 µs pulse); typical values



5/12

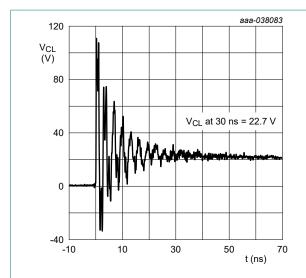


Fig. 9. Clamped +8 kV pulse waveform (IEC61000-4-2 network)

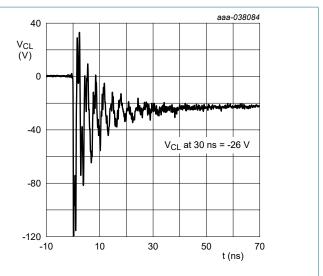
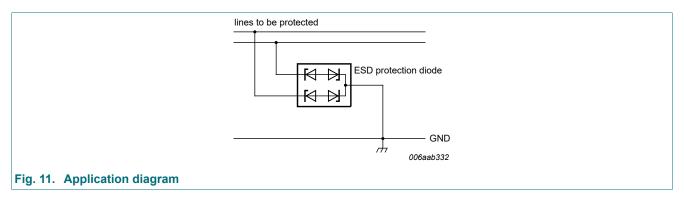


Fig. 10. Clamped -8 kV pulse waveform (IEC61000-4-2 network)

10. Application information

The device is designed for the protection of two lines from the damage caused by ESD and surge pulses.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

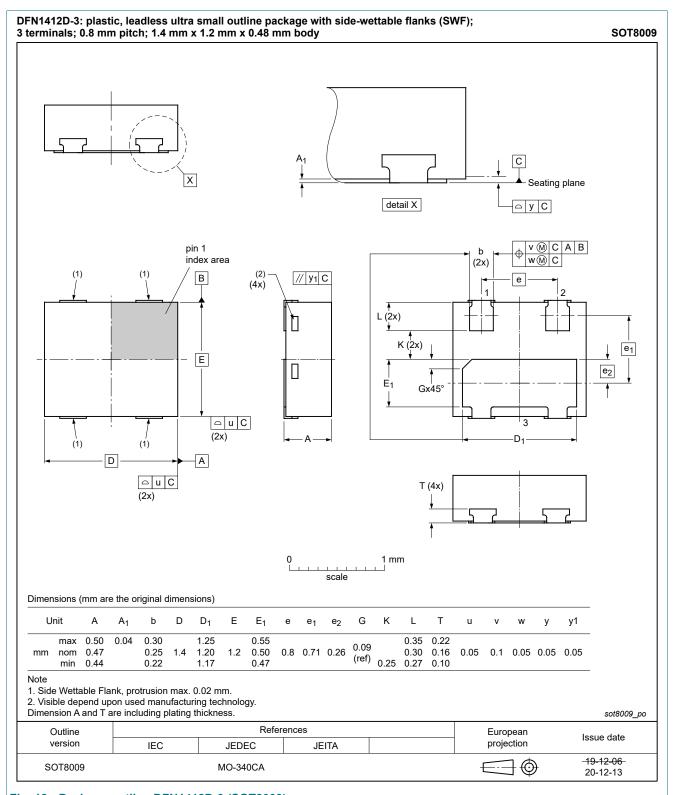
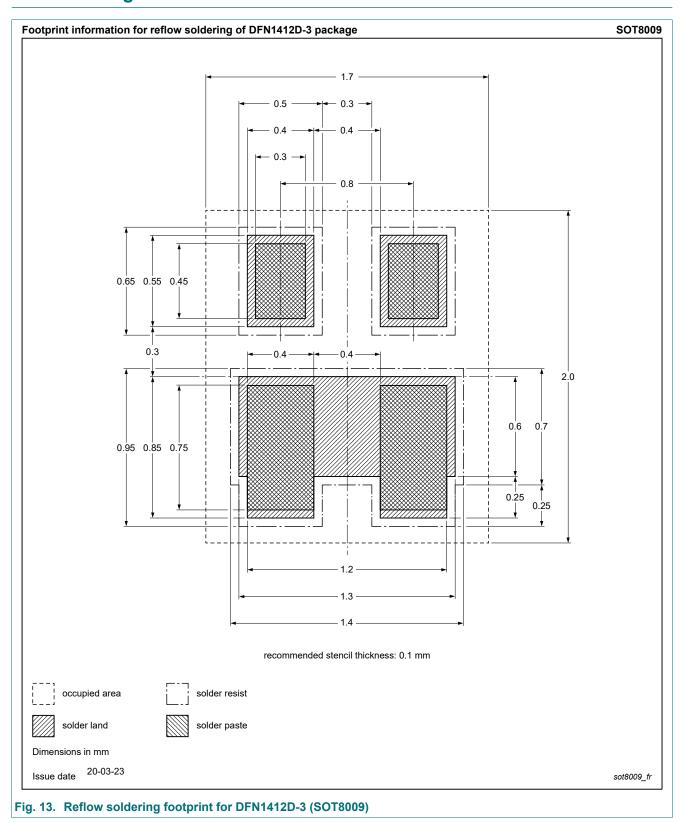


Fig. 12. Package outline DFN1412D-3 (SOT8009)

8 / 12

13. Soldering



14. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBZ27VBQC-Q v.1	20240404	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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Contents

1.	General description	1
2.	Features and benefits	1
3.	Applications	1
4.	Quick reference data	1
5.	Pinning information	2
6.	Ordering information	2
7.	Marking	2
8.	Limiting values	3
9.	Characteristics	4
10.	Application information	7
	Test information	
12.	Package outline	8
	Soldering	
	. Revision history	
	Legal information	

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12 / 12

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