## 1. General description

ESD protection device in an ultra small DFN1110D-3 (SOT8015) leadless Surface-Mounted Device (SMD) plastic package with side wettable flanks, designed to protect two lines from the damage caused by ElectroStatic discharge (ESD) and other transients.

### 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 27 V
- Low clamping voltage: V<sub>CL</sub> = 33 V at I<sub>PP</sub> = 2.5 A
- ESD protection up to 20 kV (IEC 61000-4-2)
- ESD protection up to 20 kV (ISO 10605)
- Low capacitance: C<sub>d</sub> = 8 pF
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- Computers and peripherals
- Audio and video equipment
- · Cellular handsets and accessories
- Automotive electronic control units
- Portable electronics

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	-	2.5	А
V <sub>CL</sub>	clamping voltage	$I_{PPM}$ = 2.5 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1] [2]	-	33	44	V

- [1] Measured from pin 1 or 2 to pin 3
- [2] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5



# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	3	[ <u>-</u> ]
3	СС	common cathode	Transparent top view DFN1110D-3 (SOT8015)	K1 CC CC K2 006aaa155

# 6. Ordering information

**Table 3. Ordering information** 

Type number	Package				
	Name	Description	Version		
MMBZ33VBQB-Q	DFN1110D-3	plastic, leadless extremely thin small outline package with side-wettable flanks (SWF); 3 terminals; 0.65 mm pitch; 1.1 mm x 1 mm x 0.48 mm body	SOT8015		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
MMBZ33VBQB-Q	QH

# 8. Limiting values

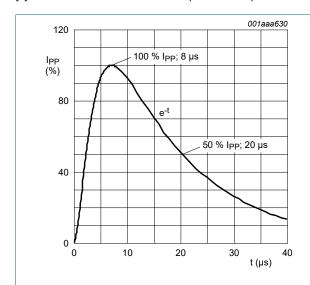
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

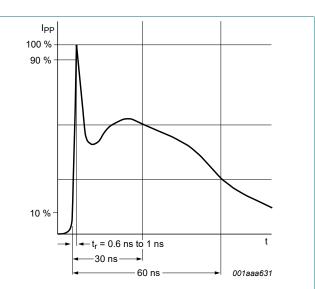
Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	2.5	Α
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximi	um ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[3] [1]	-	20	kV
	voltage	ISO10605; contact discharge; C = 330 pF, R = 330 $\Omega$	[3] [1]	-	17	kV
		ISO10605; contact discharge; C = 150 pF, R = $330\Omega$	[3] [1]	-	20	kV

- Measured from pin 1 or 2 to pin 3

  Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5
- Device stressed with ten non-repetitive ESD pulses



8/20 µs pulse waveform according to Fig. 1. IEC 61000-4-5



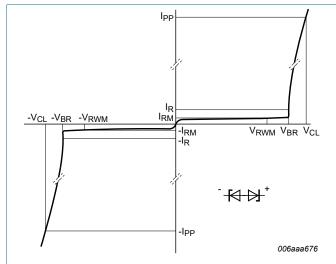
ESD pulse waveform according to Fig. 2. IEC 61000-4-2

### 9. Characteristics

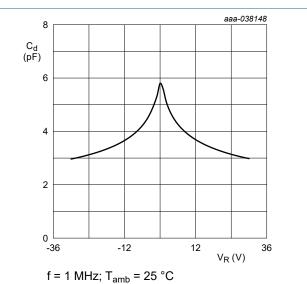
**Table 6. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	27	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C	[1]	28	-	38	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 27 V; T <sub>amb</sub> = 25 °C	[1]	-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	[1]	-	6	8	pF
V <sub>CL</sub>	clamping voltage	$I_{PPM}$ = 2.5 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[1] [2]	-	33	44	V
		$I_{PP}$ = 16 A; $t_p$ = 100 ns; $T_{amb}$ = 25 °C	[1] [3]	-	44	-	V

- Measured from pin 1 or 2 to pin 3
- Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5 Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008 [3]



V-I characteristics for a bidirectional ESD protection diode



Capacitance as a function of reverse voltage; typical values

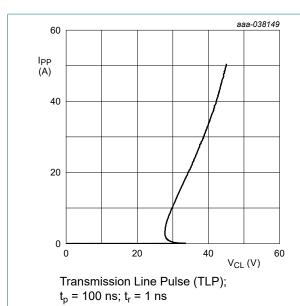
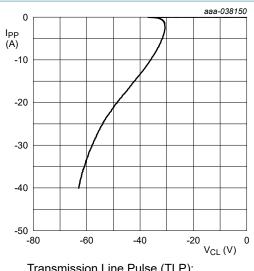
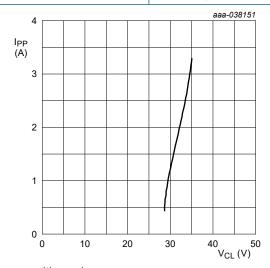


Fig. 5. Dynamic resistance with positive clamping; typical values



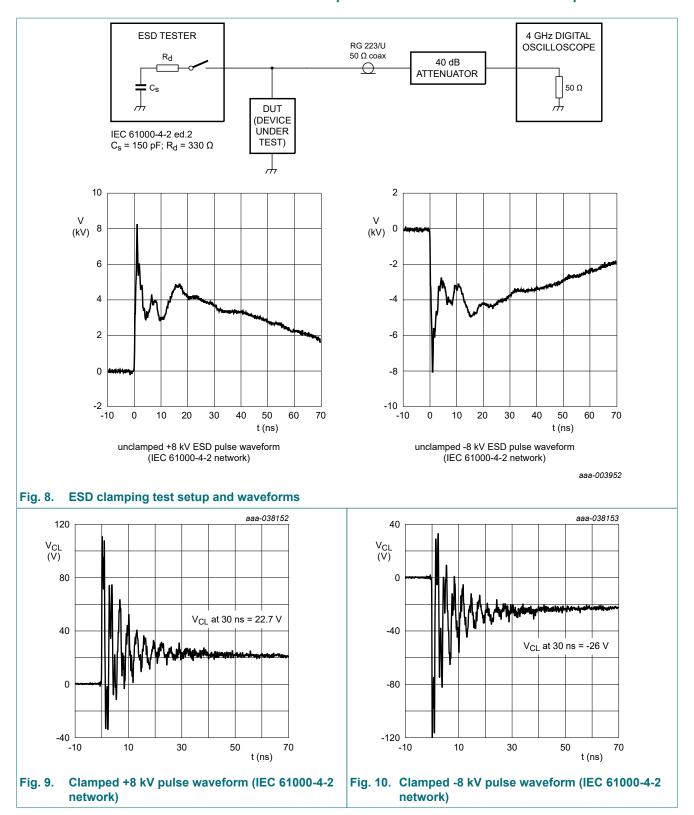
Transmission Line Pulse (TLP);  $t_p = 100 \text{ ns}$ ;  $t_r = 1 \text{ ns}$ 

Fig. 6. Dynamic resistance with negative clamping; typical values



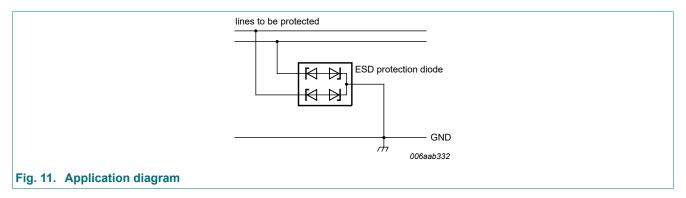
IEC 61000-4-5;  $t_p$  = 8/20  $\mu$ s; positive pulse

Fig. 7. Dynamic resistance with positive clamping; typical values



# 10. Application information

The device is designed for the protection of two lines from the damage caused by ESD and surge pulses.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

#### 11. Test information

#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline

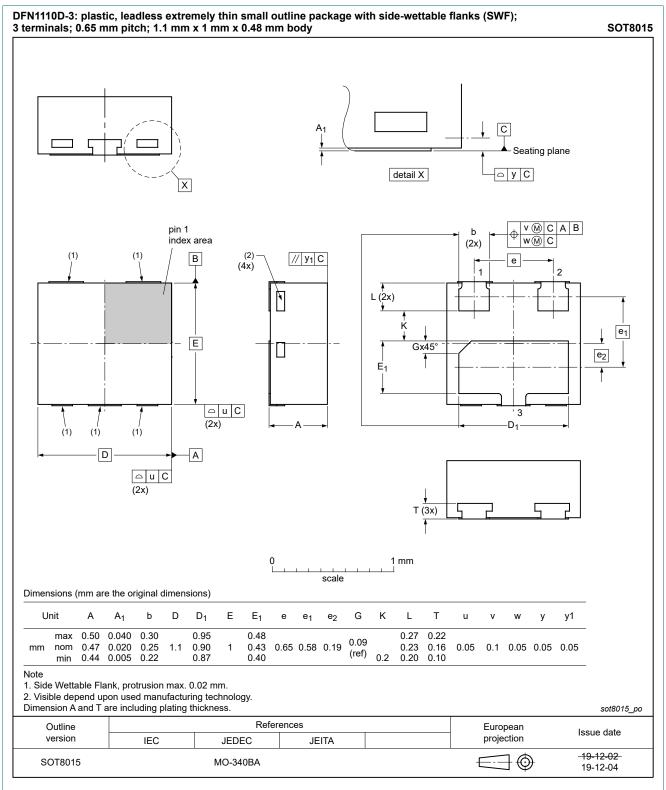
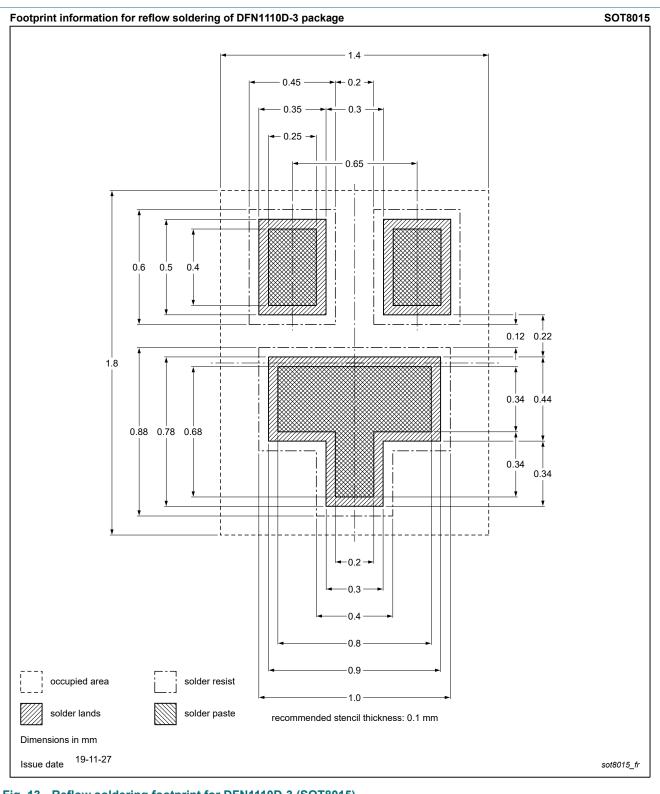


Fig. 12. Package outline DFN1110D-3 (SOT8015)

# 13. Soldering



# 14. Revision history

#### Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBZ33VBQB-Q v.1	20240404	Product data sheet	-	-

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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