

MMBZ6V2A-T

Low capacitance unidirectional double ESD protection diode 4 December 2023 Product data sheet

1. General description

Unidirectional double ElectroStatic Discharge (ESD) protection diode in a common anode configuration, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package. The device is designed for ESD and transient overvoltage protection of up to two signal lines.

2. Features and benefits

- Unidirectional ESD protection of two lines
- Bidirectional ESD protection of one line
- Reverse stand-off voltage: V_{RWM} = 3 V
- Low clamping voltage: V_{CL} = 12 V typ. at I_{PP} = 8.8 A
- ESD protection up to 30 kV (IEC 61000-4-2)
- Ultra low leakage current: I_{RM} < 1 nA

3. Applications

- Computers and peripherals
- · Audio and video equipment
- · Cellular handsets and accessories
- Electronic control units
- Portable electronics

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3	V
I _{PPM}	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1]	-	-	8.8	A
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	88	106	pF

[1] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	<u>3</u>	3
2	K2	cathode (diode 2)		
3	CA	common anode	SOT23	1 2 006aaa154

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
MMBZ6V2A-T		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
MMBZ6V2A-T	%Q3

[1] % = placeholder for manufacturing site code

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8. Limiting values

Table 5. Limiting values

In accordance with the Aboluste Maximum Rating System (IEC 60134)

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 10/1000 µs	[1]	-	17	W
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[2]	-	8.8	Α
		t _p = 10/1000 μs	[1]	-	2	Α
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximui	m ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[3]	-	30	kV
	voltage	IEC 61000-4-2; air discharge	[3]	-	30	kV

- [1] In accordance with IEC 61643-321 (10/1000 µs current waveform).
- Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [3] Device stressed with ten non-repetitive ESD pulses.

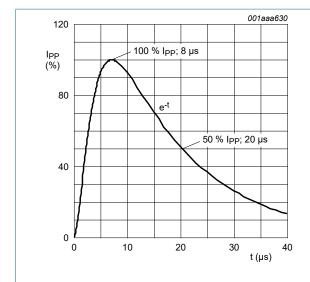


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

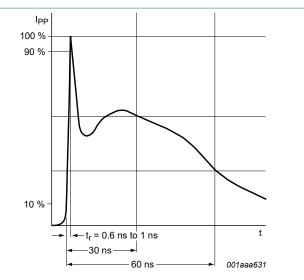


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	417	-	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[2]	-	100	-	K/W

- [1] Device mounted on an FR4PCB, single-sided copper, tin-plated and standard footprint.
- [2] Soldering points at pin 1 and 2.

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _F	forward voltage	I _F = 1 mA; T _{amb} = 25 °C		-	0.7	-	V
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3	V
V_{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		5.7	6.2	6.7	V
I _{RM}	reverse leakage current	V _{RWM} = 3 V; T _{amb} = 25 °C		-	1	200	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	88	106	pF
V _{CL}	clamping voltage	I _{PPM} = 8.8 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1]	-	12	-	V
S _Z	temperature coefficient	I _Z = 1 mA		-	2.2	-	mV/K

[1] Device stressed with $8/20~\mu s$ exponential decay waveform according to IEC 61000-4-5.

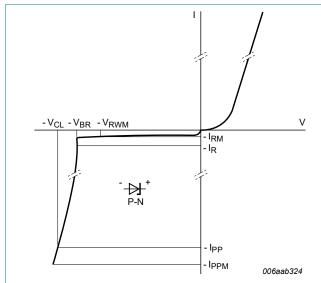


Fig. 3. V-I characteristics for a unidirectional ESD protection diode

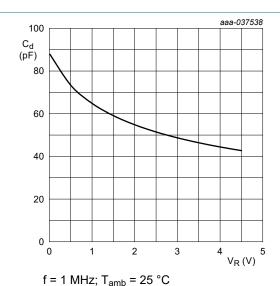


Fig. 4. Diode capacitance as a function of reverse voltage; typical values

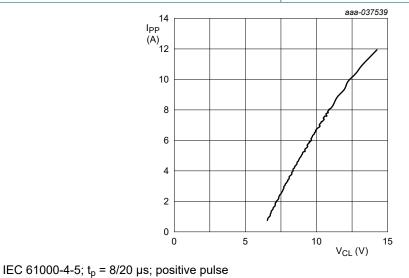
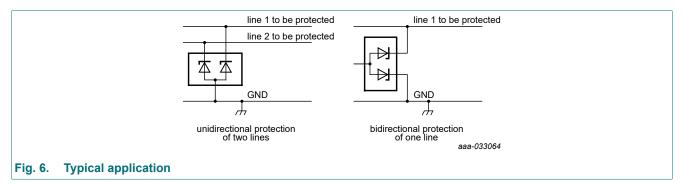


Fig. 5. Dynamic resistance with positive clamping; typical values

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11. Application information

The device is designed for the protection of two lines from the damage caused by ESD and surge pulses.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- **3.** Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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12. Package outline

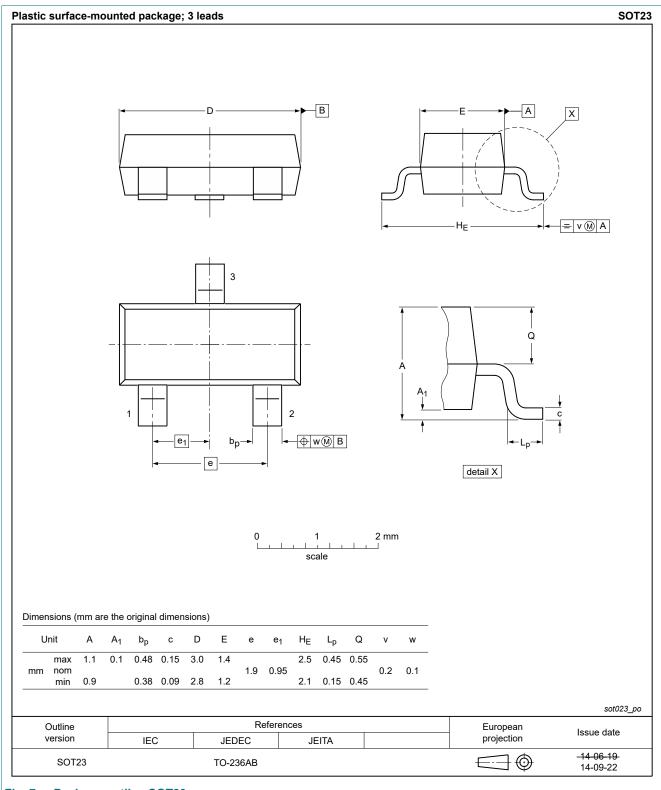
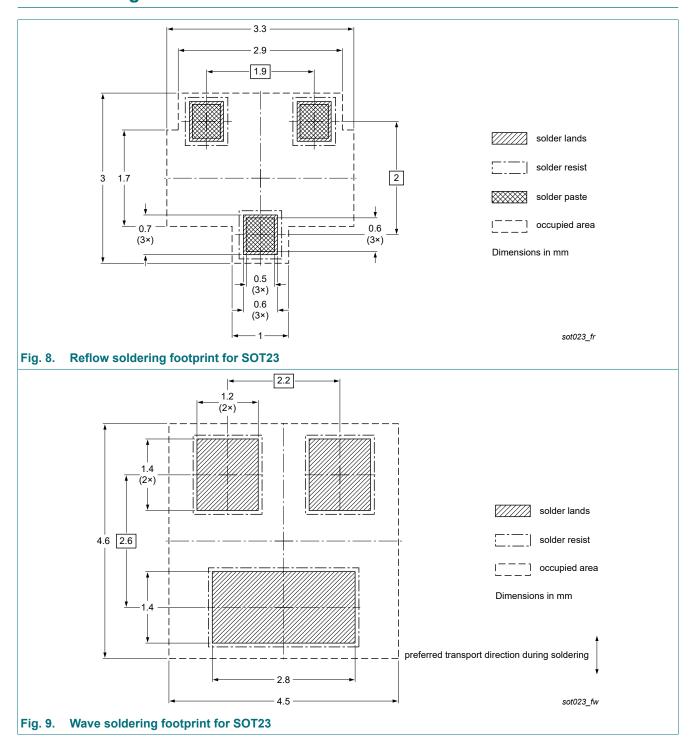


Fig. 7. Package outline SOT23

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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
MMBZ6V2A-T v.1	20231204	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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