## 1. General description

NPN high-voltage low  $V_{CEsat}$  transistor in a SOT89 (SC-62) medium power and flat lead Surface-Mounted Device (SMD) plastic package.

PNP complement: PBHV9040X

#### 2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High collector current gain h<sub>FE</sub> at high I<sub>C</sub>
- Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

- · LED driver for LED chain module
- LCD backlighting
- · Automotive motor management
- · Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CESM</sub>	collector-emitter peak voltage	V <sub>BE</sub> = 0 V	-	-	500	V
$V_{CEO}$	collector-emitter voltage	open base	-	-	400	V
I <sub>C</sub>	collector current		-	-	0.5	Α
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 10 V; $I_{C}$ = 50 mA; $T_{amb}$ = 25 °C	100	200	-	



### 500 V, 0.5 A NPN high-voltage low VCEsat transistor

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		С
2	С	collector		в
3	В	base	3 2 1 SOT89	E sym042

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PBHV8540X-Q	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89			

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PBHV8540X-Q	%4D

[1] % = placeholder for manufacturing site code

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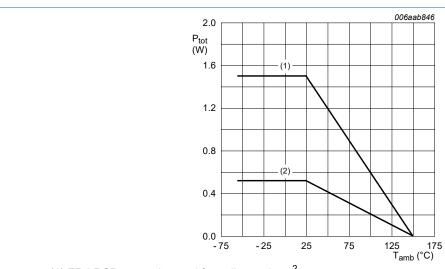
# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	500	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	400	V
$V_{CESM}$	collector-emitter peak voltage	V <sub>BE</sub> = 0 V		-	500	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	0.5	Α
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	1	Α
I <sub>BM</sub>	peak base current			-	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	0.52	W
			[2]	-	1.5	W
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.



- (1) FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>
- (2) FR4 PCB, standard footprint

Fig. 1. Power derating curves

#### 500 V, 0.5 A NPN high-voltage low VCEsat transistor

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1]	-	-	240	K/W
junction to ambient	junction to ambient		[2]	-	-	83	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	20	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

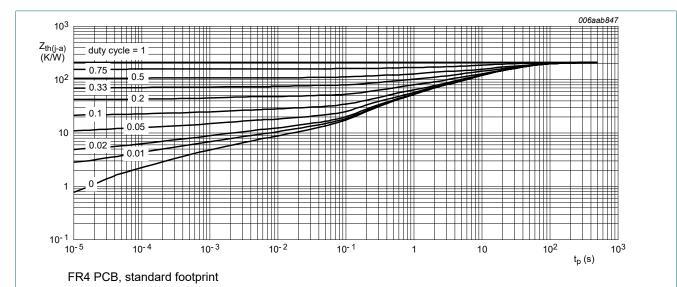


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

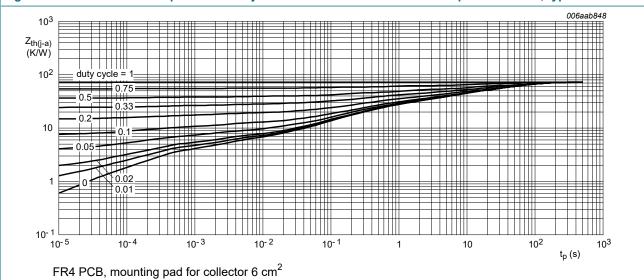


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

## 500 V, 0.5 A NPN high-voltage low VCEsat transistor

# 10. Characteristics

#### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 320 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
	current	V <sub>CB</sub> = 320 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	10	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = 320 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; I <sub>C</sub> = 50 mA; T <sub>amb</sub> = 25 °C	100	200	-	
		$V_{CE}$ = 10 V; $I_{C}$ = 100 mA; $t_{p}$ ≤ 300 μs; $\delta$ ≤ 0.02; $T_{amb}$ = 25 °C; pulsed	80	150	-	
		$V_{CE}$ = 10 V; $I_{C}$ = 300 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	10	20	-	
V <sub>CEsat</sub>	collector-emitter	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 10 mA; T <sub>amb</sub> = 25 °C	-	100	200	mV
	saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 20 mA; T <sub>amb</sub> = 25 °C	-	60	90	mV
		I <sub>C</sub> = 300 mA; I <sub>B</sub> = 60 mA; T <sub>amb</sub> = 25 °C	-	135	250	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C$ = 300 mA; $I_B$ = 60 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	0.91	1.1	V
t <sub>d</sub>	delay time	V <sub>CC</sub> = 6 V; I <sub>C</sub> = 0.5 A; I <sub>Bon</sub> = 0.1 A;	-	50	-	ns
t <sub>r</sub>	rise time	I <sub>Boff</sub> = -0.1 A; T <sub>amb</sub> = 25 °C	-	6200	-	ns
t <sub>on</sub>	turn-on time		-	6250	-	ns
t <sub>s</sub>	storage time		-	800	-	ns
t <sub>f</sub>	fall time		-	2200	-	ns
t <sub>off</sub>	turn-off time		-	3000	-	ns
f <sub>T</sub>	transition frequency	$V_{CE}$ = 10 V; $I_{C}$ = 100 mA; f = 100 MHz; $T_{amb}$ = 25 °C	-	30	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 20 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 ^{\circ}\text{C}$	-	4	-	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ f = 1 MHz; $T_{amb} = 25 \text{ °C}$	-	165	-	pF

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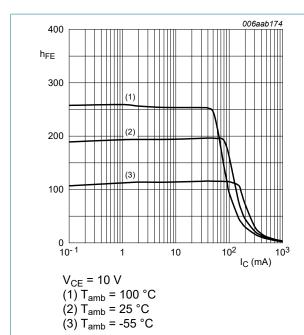


Fig. 4. DC current gain as a function of collector current; typical values

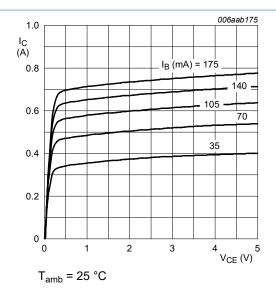


Fig. 5. Collector current as a function of collectoremitter voltage; typical values

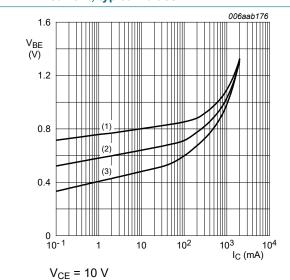
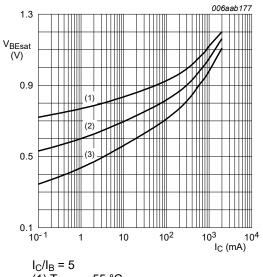


Fig. 6. Base-emitter voltage as a function of collector current; typical values

(1)  $T_{amb} = -55 \,^{\circ}C$ (2)  $T_{amb} = 25 \,^{\circ}C$ 

(3)  $T_{amb} = 100 \, ^{\circ}C$ 



 $I_{C}/I_{B} = 5$ (1)  $T_{amb} = -55 \,^{\circ}C$ (2)  $T_{amb} = 25 \,^{\circ}C$ (3)  $T_{amb} = 100 \,^{\circ}C$ 

Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

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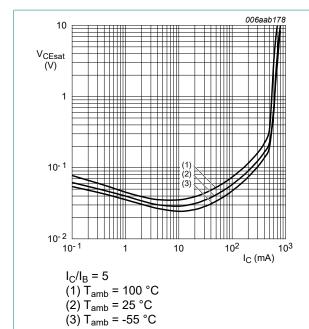


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

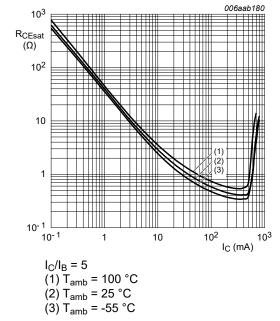


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

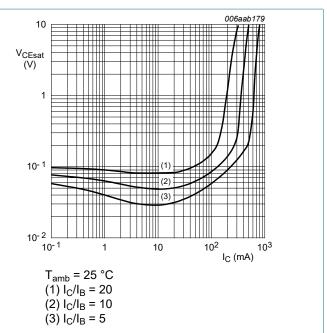


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

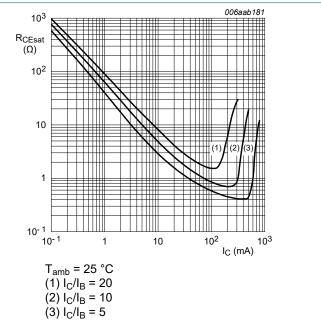
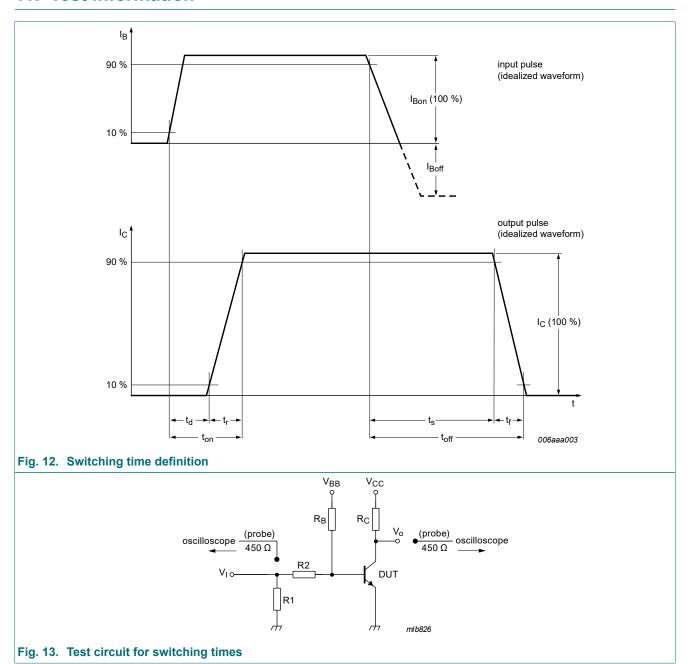


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

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## 11. Test information

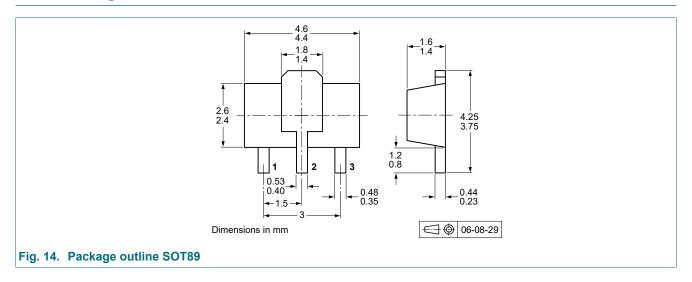


### **Quality information**

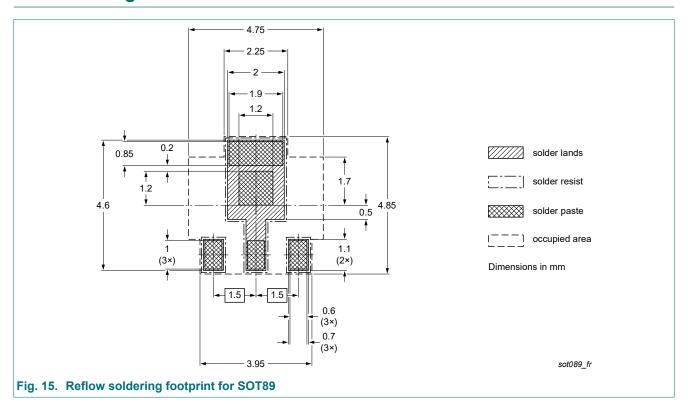
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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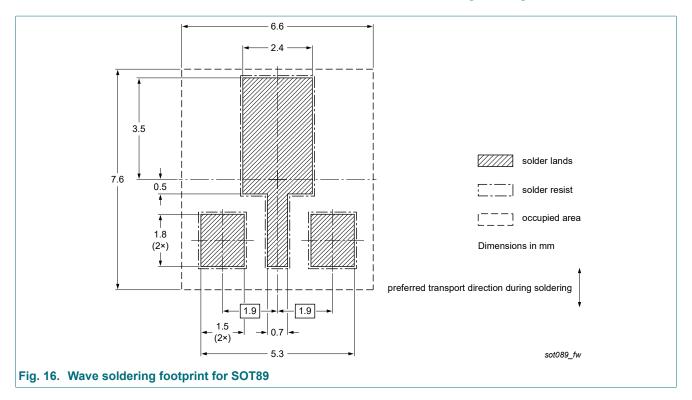
# 12. Package outline



## 13. Soldering



### 500 V, 0.5 A NPN high-voltage low VCEsat transistor



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# 14. Revision history

#### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBHV8540X-Q v.1	20230712	Product data sheet	-	-

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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