

PBHV9040X

500 V, 0.25 A PNP high-voltage low VCEsat transistor

17 July 2023

Product data sheet

1. General description

PNP high-voltage low V_{CEsat} transistor in a SOT89 (SC-62) medium power and flat lead Surface-Mounted Device (SMD) plastic package.

NPN complement: PBHV8540X

2. Features and benefits

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- + High collector current capability ${\rm I}_{\rm C}$ and ${\rm I}_{\rm CM}$
- High collector current gain (h_{FE}) at high I_C
- AEC-Q101 qualified

3. Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- LCD backlighting
- High Intensity Discharge (HID) front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch mode power supply

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V	-	-	-500	V
V _{CEO}	collector-emitter voltage	open base	-	-	-400	V
I _C	collector current		-	-	-0.25	А
h _{FE}	DC current gain	V_{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C	100	200	-	



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter		С
2	С	collector		в
3	В	base		
			SOT89	sym079

6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PBHV9040X	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	<u>SOT89</u>		

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBHV9040X	%4E

[1] % = placeholder for manufacturing site code

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8. Limiting values

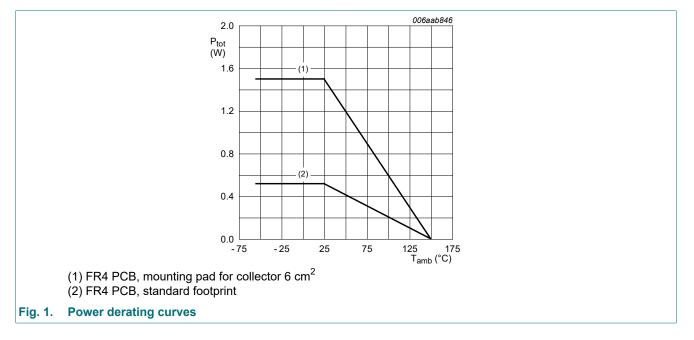
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-500	V
V _{CEO}	collector-emitter voltage	open base		-	-400	V
V _{CESM}	collector-emitter peak voltage	V _{BE} = 0 V		-	-500	V
V _{EBO}	emitter-base voltage	open collector		-	-6	V
l _C	collector current			-	-0.25	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-0.5	А
I _{BM}	peak base current			-	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.52	W
			[2]	-	1.5	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

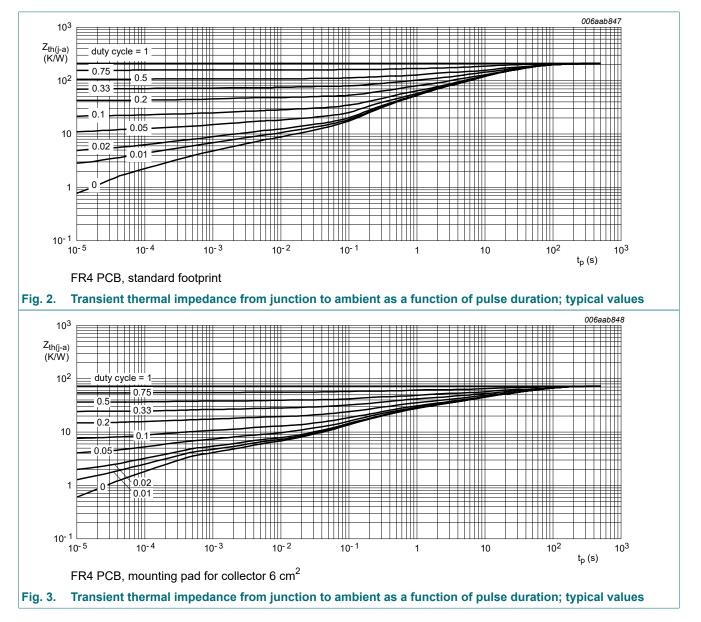


9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	R _{th(j-a)} thermal resistance from junction to ambient	in free air	[1]	-	-	240	K/W
			[2]	-	-	83	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	20	K/W

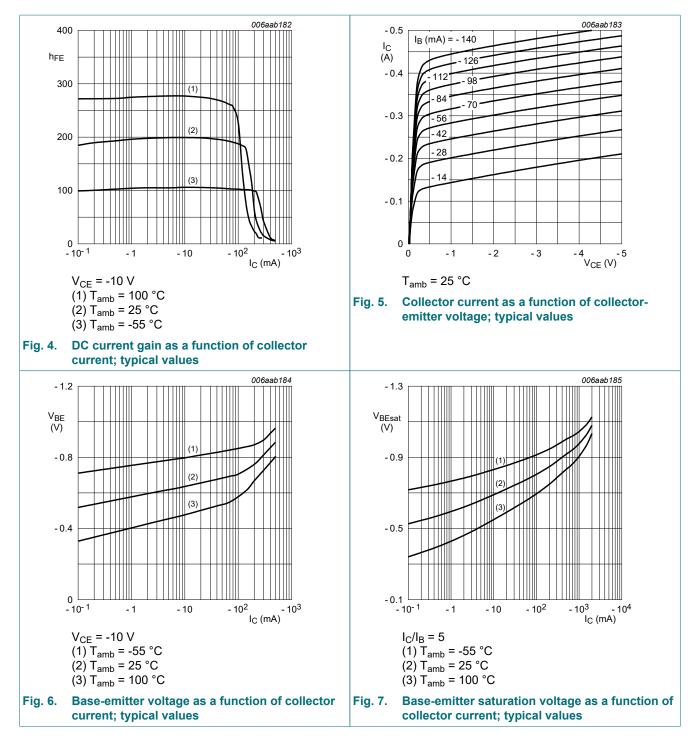
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -320 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V _{CB} = -320 V; I _E = 0 A; T _j = 150 °C	-	-	-10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
I _{CES}	collector-emitter cut-off current	V _{CE} = -320 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V _{CE} = -10 V; I _C = -50 mA; T _{amb} = 25 °C	100	200	-	
		V _{CE} = -10 V; I _C = -100 mA; T _{amb} = 25 °C	80	200	-	
		V_{CE} = -10 V; I _C = -250 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	10	25	-	
V _{CEsat}	collector-emitter saturation voltage	I_{C} = -100 mA; I_{B} = -20 mA; T_{amb} = 25 °C	-	-110	-200	mV
V _{BEsat}	base-emitter saturation voltage	I_{C} = -100 mA; I_{B} = -20 mA; pulsed; $t_{p} \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-1	-1.1	V
t _d	delay time	V _{CC} = -2 V; I _C = -0.15 A; I _{Bon} = -0.03 A;	-	9	-	ns
t _r	rise time	I _{Boff} = 0.03 A; T _{amb} = 25 °C	-	1810	-	ns
t _{on}	turn-on time	-	-	1819	-	ns
t _s	storage time	-	-	715	-	ns
t _f	fall time	-	-	1085	-	ns
t _{off}	turn-off time		-	1800	-	ns
f _T	transition frequency	V _{CE} = -10 V; I _C = -10 mA; f = 100 MHz; T _{amb} = 25 °C	-	55	-	MHz
C _c	collector capacitance	V _{CB} = -20 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	7	-	pF
C _e	emitter capacitance	V _{EB} = -0.5 V; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	150	-	pF

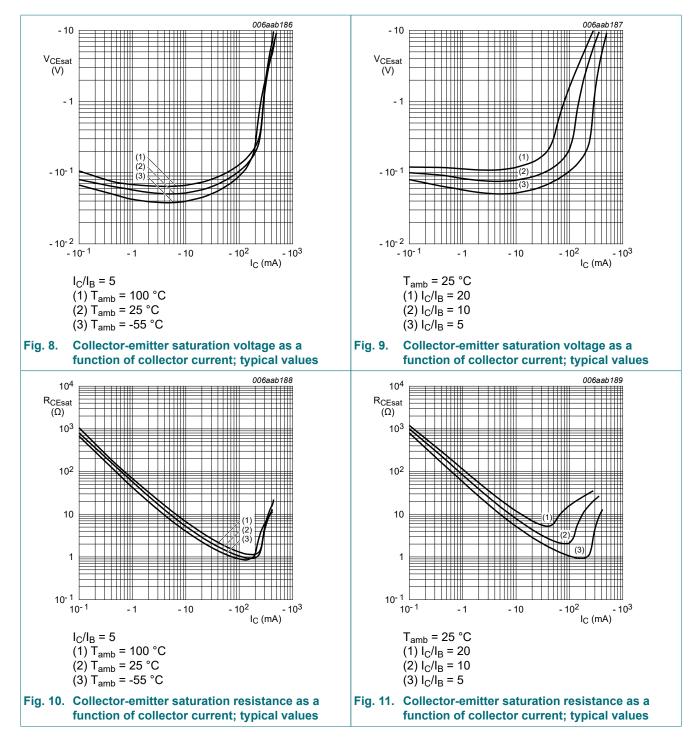


Product data sheet

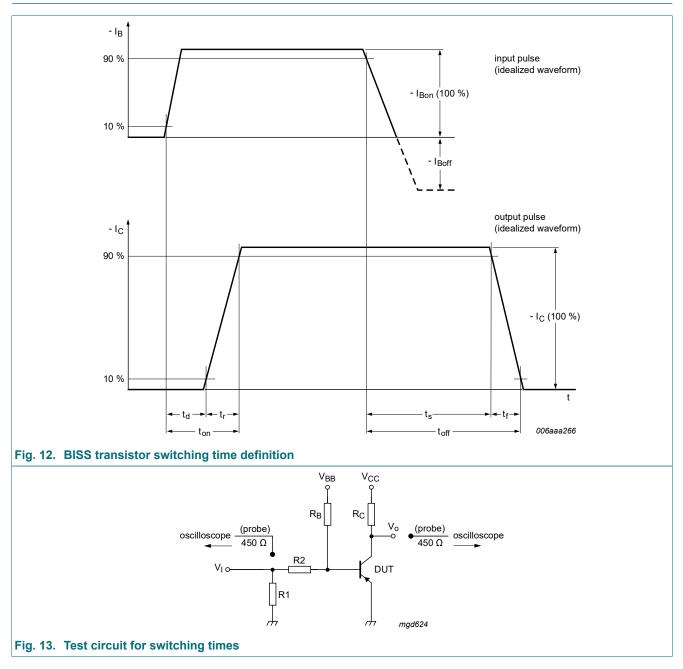
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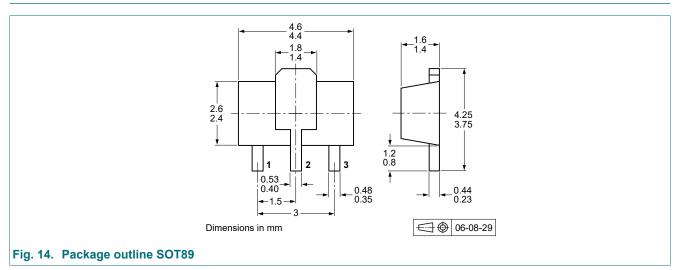
11. Test information



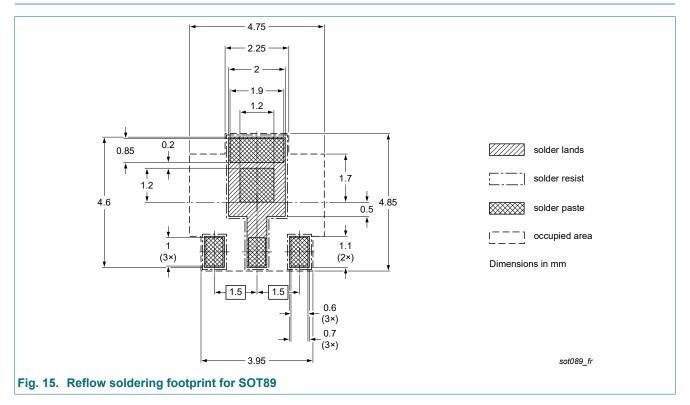
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

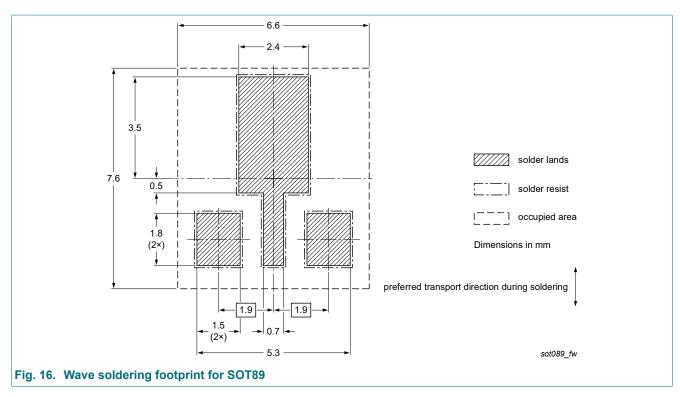


13. Soldering



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14. Revision history

Table 8. Revision hi	istory							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PBHV9040X v.2	20230717	Product data sheet	-	PBHV9040X v.1				
Modifications:	Nexperia.	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
PBHV9040X v.1	20131209	Product data sheet	-	-				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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