



PBSS4021PX-Q

20 V, 6.2 A PNP low V_{CEsat} transistor

5 January 2024

Product data sheet

1. General description

PNP low V_{CEsat} transistor in a medium power and flat lead SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4021NX-Q

2. Features and benefits

- Very low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- High energy efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- Power switches (e.g. motors, fans)

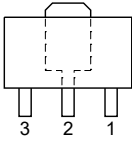
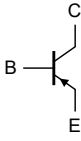
4. Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--------------------|---|--|-----|-----|------|------|
| V _{CEO} | collector-emitter voltage | open base | - | - | -20 | V |
| I _C | collector current | | - | - | -6.2 | A |
| I _{CM} | peak collector current | single pulse; t _p ≤ 1 ms | - | - | -15 | A |
| R _{CEsat} | collector-emitter saturation resistance | I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C | - | 23 | 38 | mΩ |

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--|--|
| 1 | E | emitter |  SOT89 |  006aaa231 |
| 2 | C | collector | | |
| 3 | B | base | | |

6. Ordering information

Table 3. Ordering information

| Type number | Package | | |
|------------------------------|---------|--|-----------------------|
| | Name | Description | Version |
| PBSS4021PX-Q | SOT89 | plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body | SOT89 |

7. Marking

Table 4. Marking codes

| Type number | Marking code[1] |
|--------------|-----------------|
| PBSS4021PX-Q | %6E |

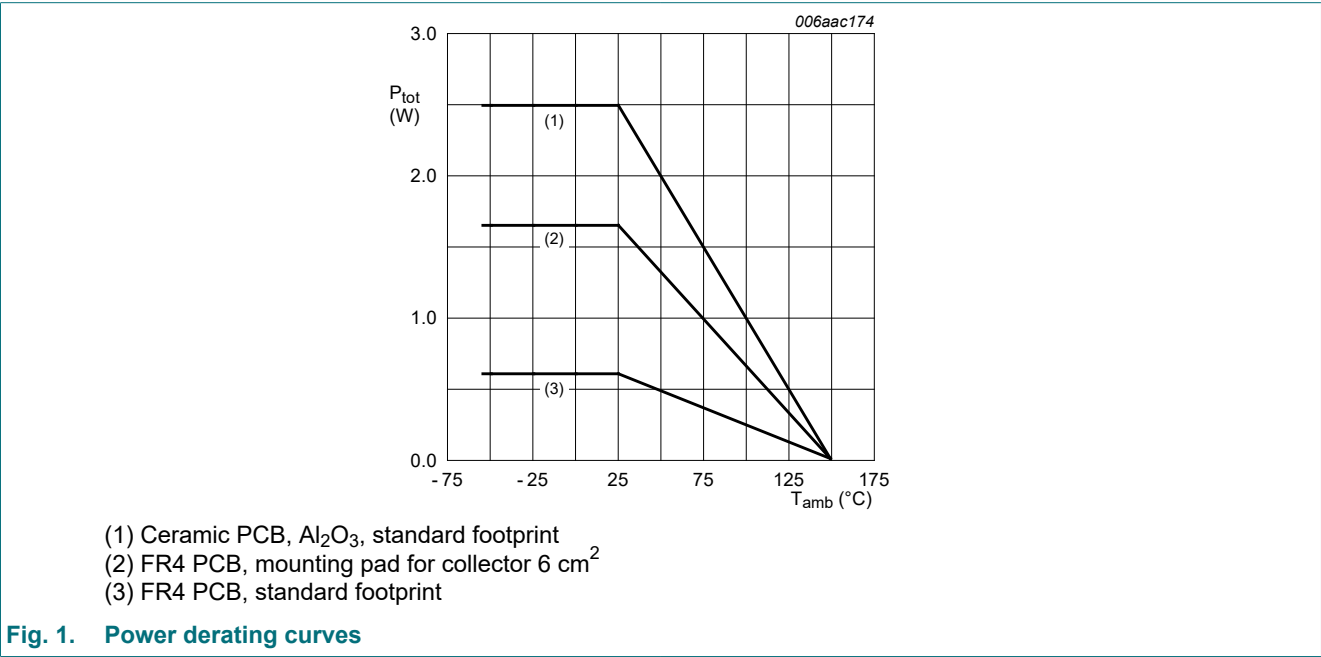
[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|---------------------------|-------------------------------------|-----|-----|------|------|
| V _{CBO} | collector-base voltage | open emitter | | - | -20 | V |
| V _{CEO} | collector-emitter voltage | open base | | - | -20 | V |
| V _{EBO} | emitter-base voltage | open collector | | - | -5 | V |
| I _C | collector current | | | - | -6.2 | A |
| I _{CM} | peak collector current | single pulse; t _p ≤ 1 ms | | - | -15 | A |
| I _B | base current | | | - | -1 | A |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | [1] | - | 600 | mW |
| | | | [2] | - | 1650 | mW |
| | | | [3] | - | 2500 | mW |
| T _j | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -55 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
[3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.



9. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|----------------|--|-------------|-----|-----|-----|-----|------|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] | - | - | 210 | K/W |
| | | | [2] | - | - | 75 | K/W |
| | | | [3] | - | - | 50 | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | | - | - | 20 | K/W |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

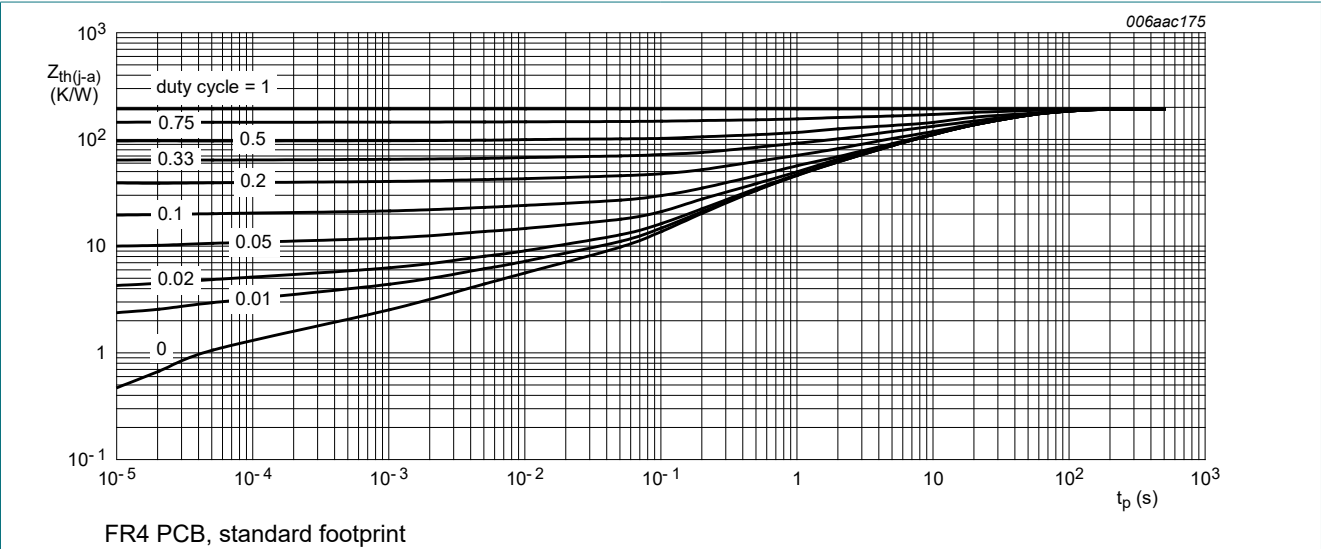


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

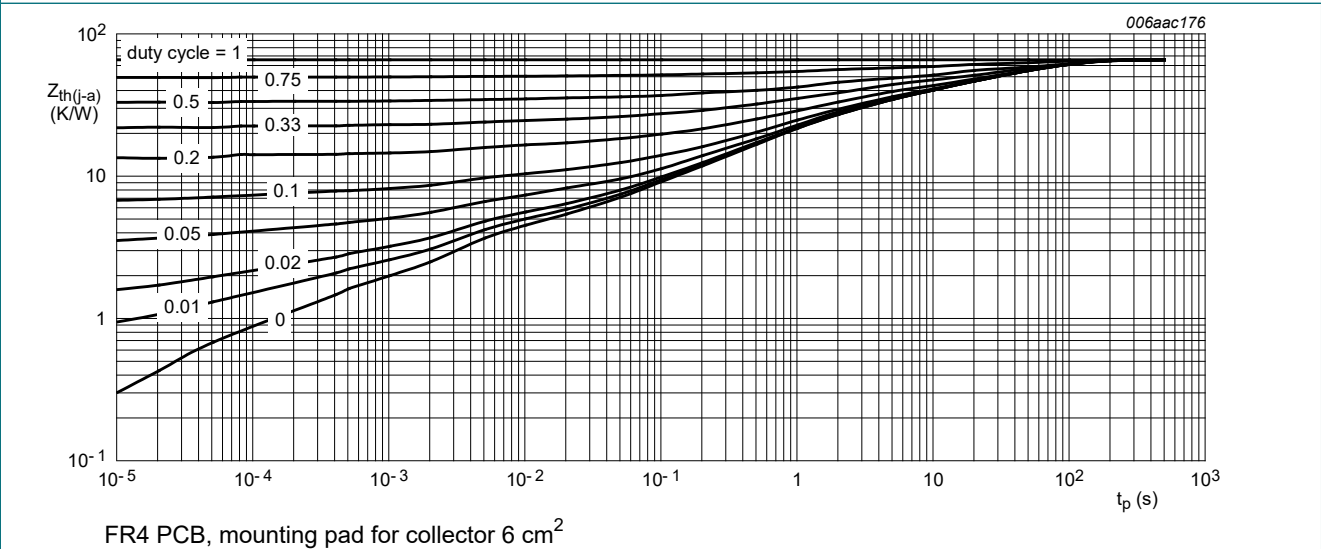
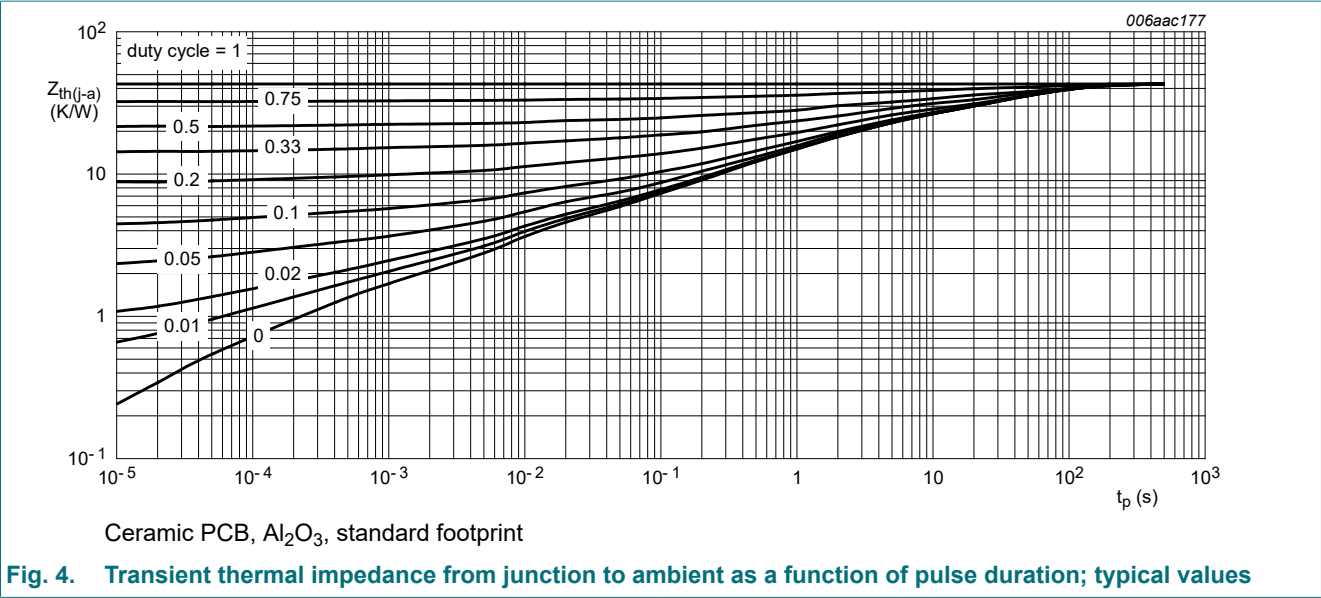


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|--------------------|---|--|--|-----|-------|-------|------|
| I _{CBO} | collector-base cut-off current | V _{CB} = -20 V; I _E = 0 A; T _{amb} = 25 °C | | - | - | -100 | nA |
| | | V _{CB} = -20 V; I _E = 0 A; T _j = 150 °C | | - | - | -50 | µA |
| I _{EBO} | emitter-base cut-off current | V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C | | - | - | -100 | nA |
| I _{CES} | collector-emitter cut-off current | V _{CE} = -16 V; V _{BE} = 0 V; T _{amb} = 25 °C | | - | - | -100 | nA |
| h _{FE} | DC current gain | V _{CE} = -2 V; I _C = -500 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | 250 | 400 | - | |
| | | V _{CE} = -2 V; I _C = -1 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | 250 | 400 | - | |
| | | V _{CE} = -2 V; I _C = -2 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | 200 | 300 | - | |
| | | V _{CE} = -2 V; I _C = -4 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | 150 | 200 | - | |
| | | V _{CE} = -2 V; I _C = -7 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | 80 | 140 | - | |
| V _{CEsat} | collector-emitter saturation voltage | I _C = -1 A; I _B = -50 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -34 | -50 | mV |
| | | I _C = -1 A; I _B = -10 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -60 | -90 | mV |
| | | I _C = -2 A; I _B = -40 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -72 | -110 | mV |
| | | I _C = -4 A; I _B = -200 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -105 | -160 | mV |
| | | I _C = -4 A; I _B = -40 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -180 | -270 | mV |
| | | I _C = -6.9 A; I _B = -345 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -175 | -265 | mV |
| R _{CEsat} | collector-emitter saturation resistance | I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | 23 | 38 | mΩ |
| V _{BEsat} | base-emitter saturation voltage | I _C = -1 A; I _B = -100 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -0.84 | -0.9 | V |
| | | I _C = -4 A; I _B = -400 mA; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -0.96 | -1.05 | V |
| V _{BEon} | base-emitter turn-on voltage | V _{CE} = -2 V; I _C = -2 A; pulsed; t _p ≤ 300 µs; δ ≤ 0.02; T _{amb} = 25 °C | | - | -0.75 | -0.85 | V |
| t _d | delay time | V _{CC} = -12.5 V; I _C = -1 A; I _{BoN} = -0.05 A; I _{Boff} = 0.05 A; T _{amb} = 25 °C | | - | 40 | - | ns |
| t _r | rise time | | | - | 55 | - | ns |
| t _{on} | turn-on time | | | - | 95 | - | ns |
| t _s | storage time | | | - | 340 | - | ns |
| t _f | fall time | | | - | 85 | - | ns |
| t _{off} | turn-off time | | | - | 425 | - | ns |
| f _T | transition frequency | V _{CE} = -10 V; I _C = -100 mA; f = 100 MHz; T _{amb} = 25 °C | | - | 105 | - | MHz |
| C _c | collector capacitance | V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C | | - | 95 | - | pF |

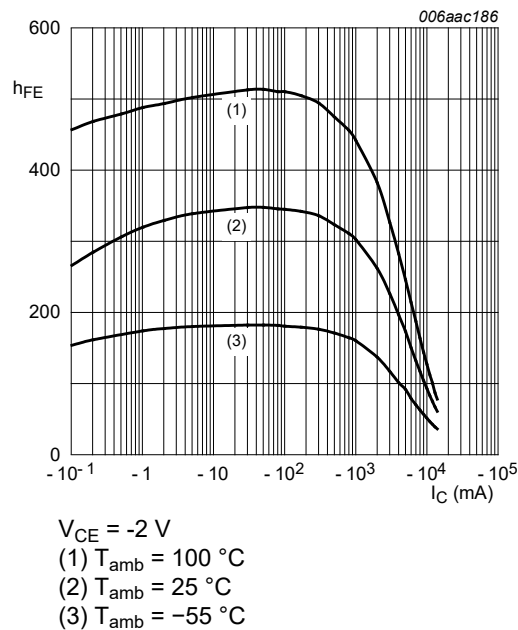


Fig. 5. DC current gain as a function of collector current; typical values

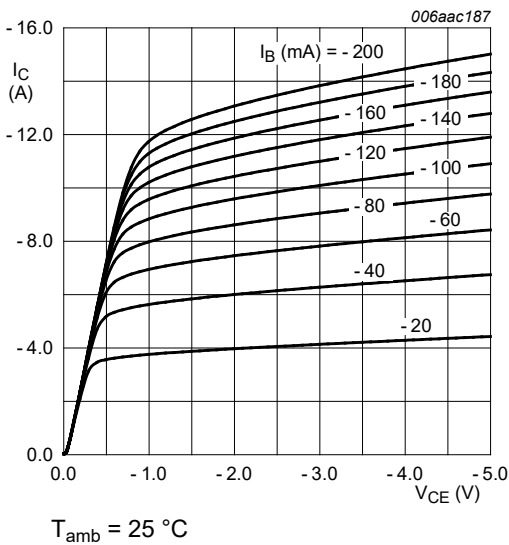


Fig. 6. Collector current as a function of collector-emitter voltage; typical values

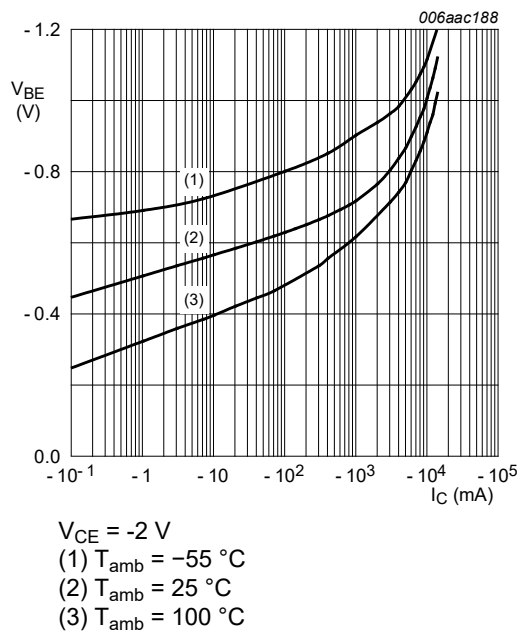


Fig. 7. Base-emitter voltage as a function of collector current; typical values

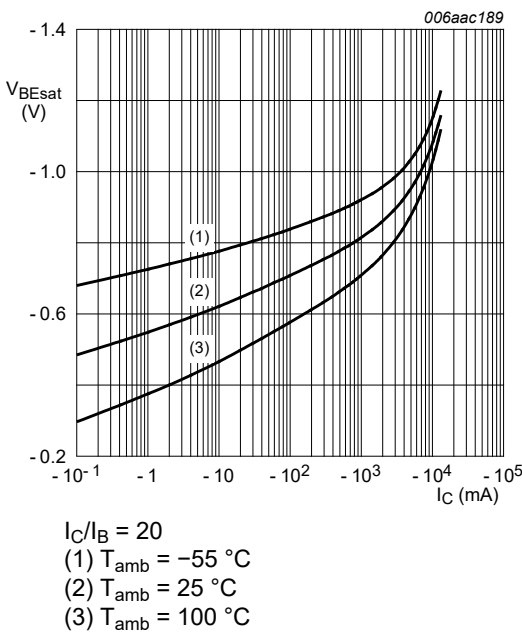


Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

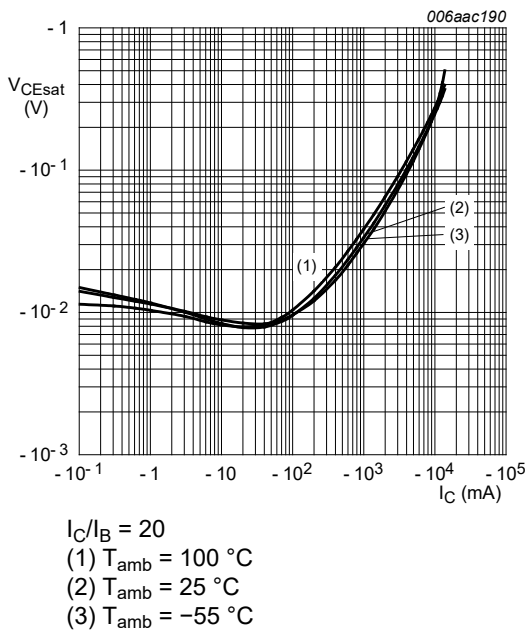


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

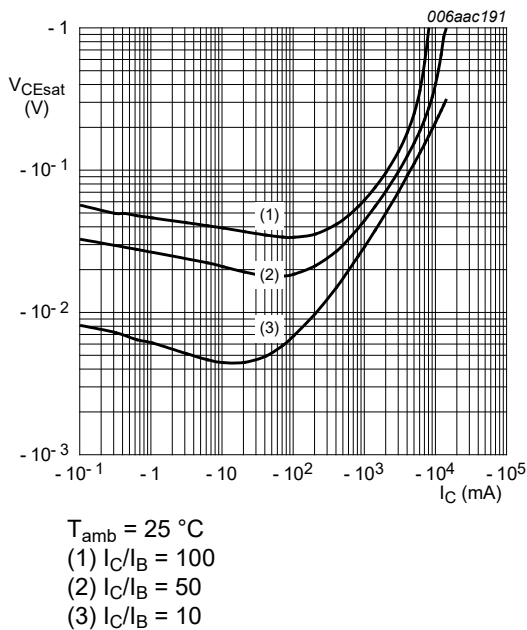


Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values

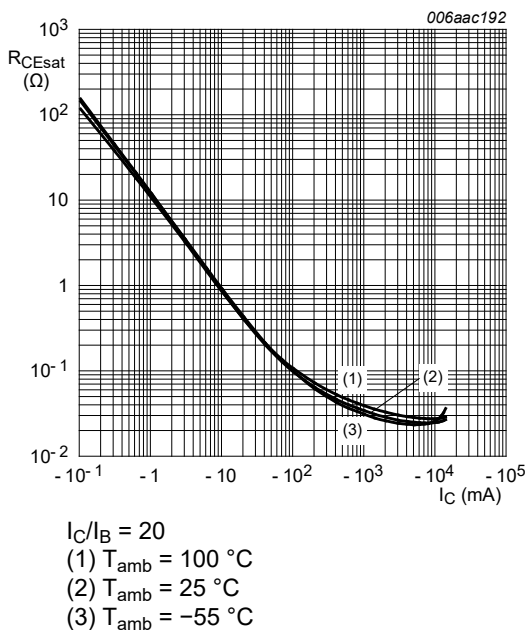


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

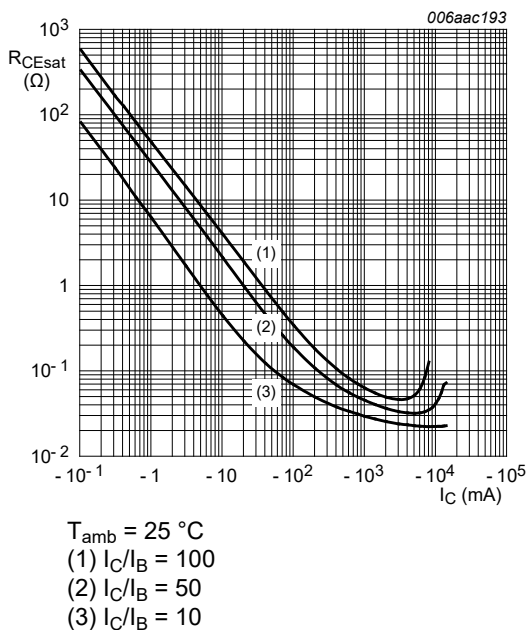


Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

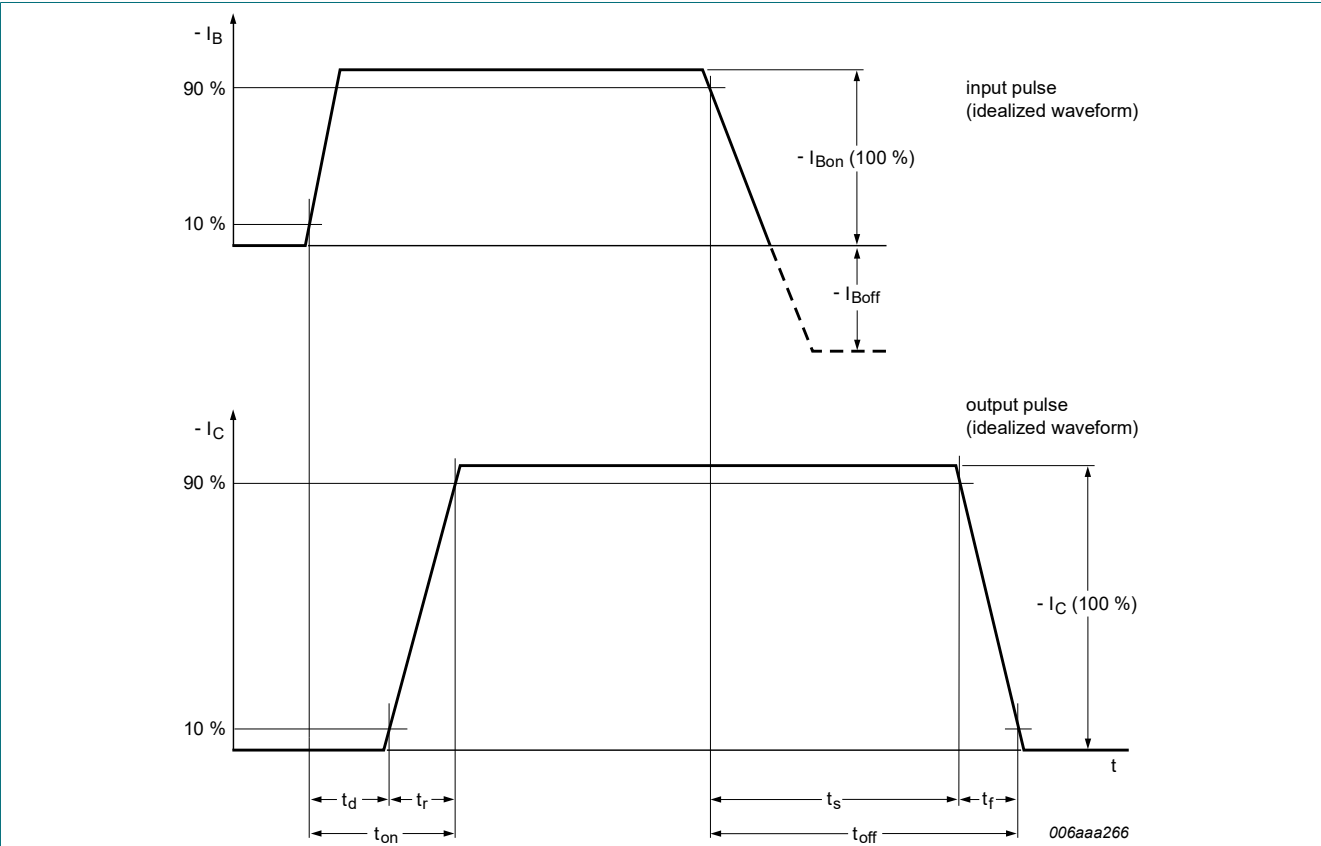


Fig. 13. Transistor switching time definition

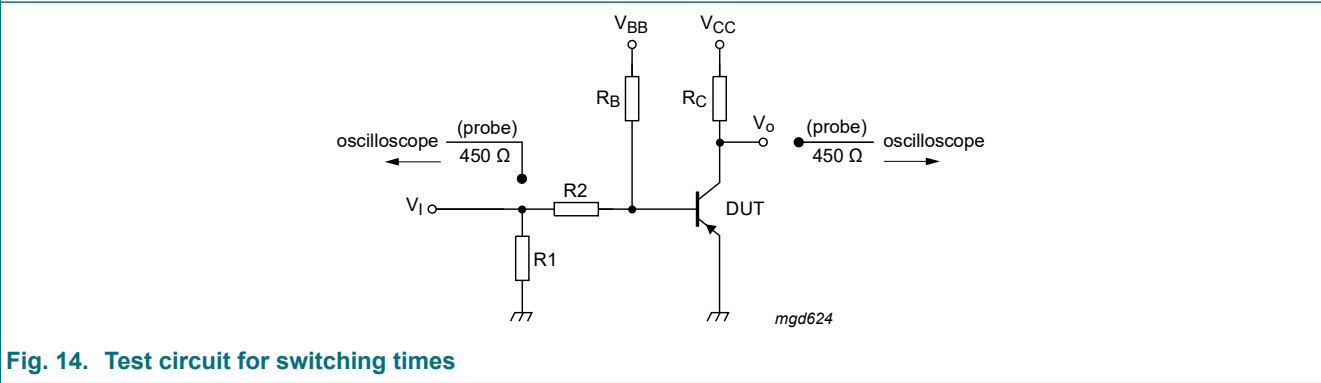
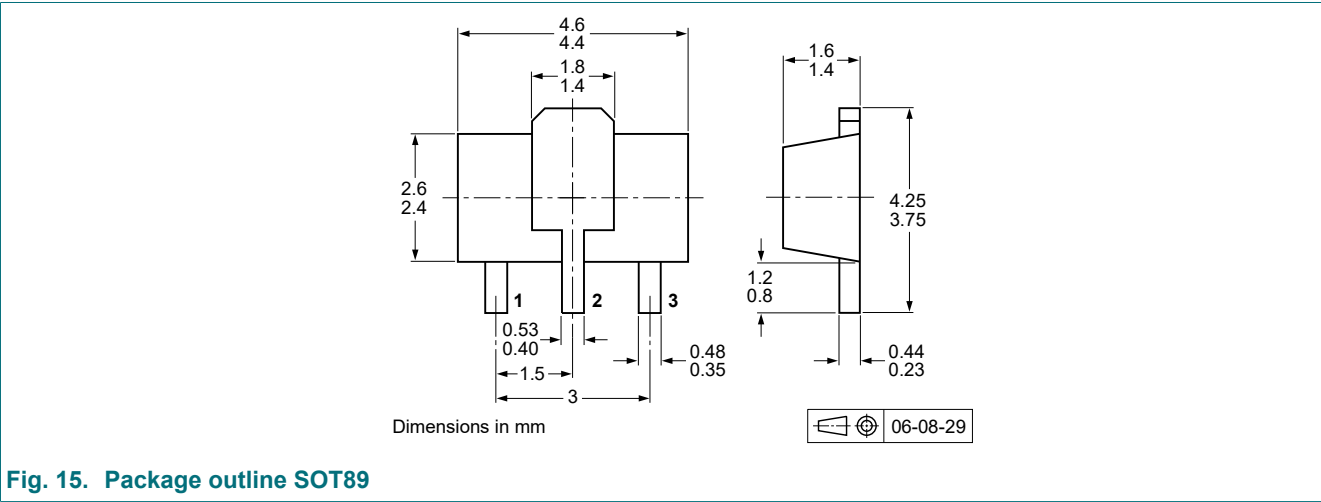


Fig. 14. Test circuit for switching times

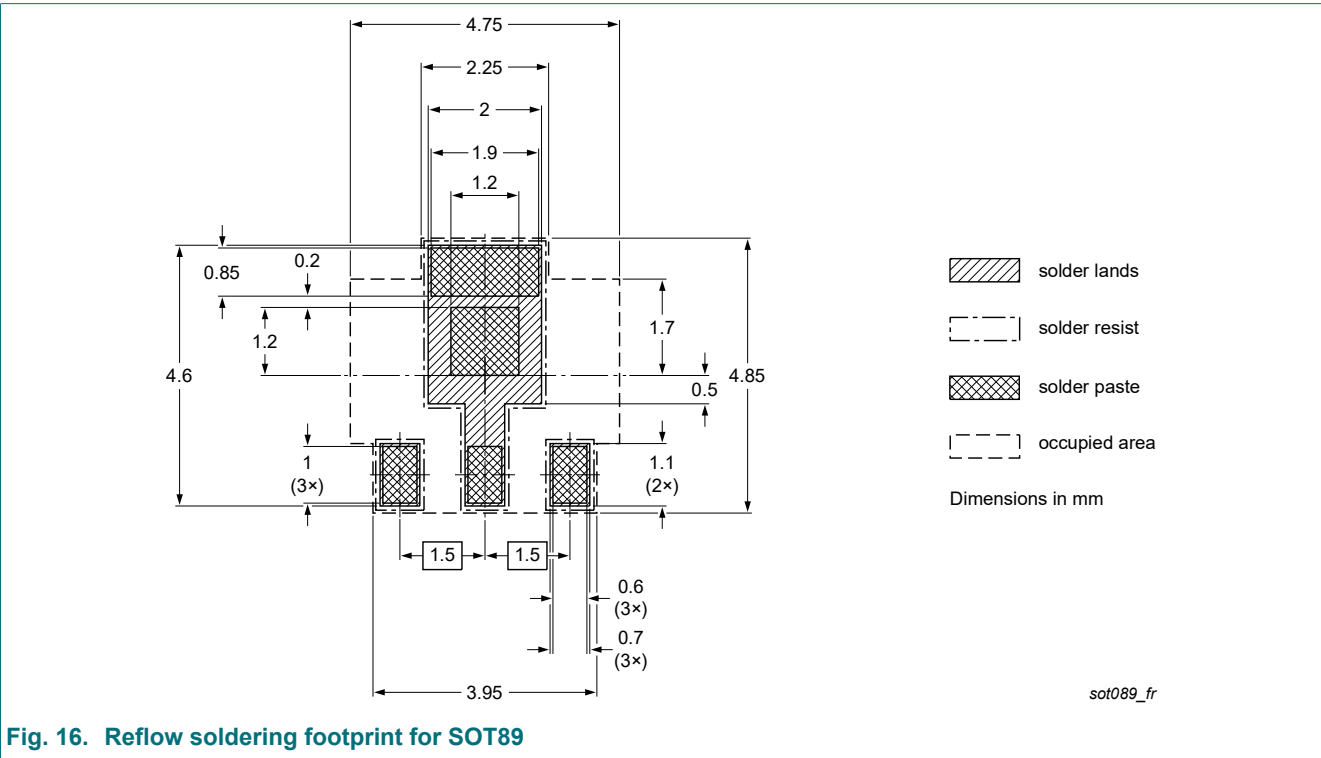
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|------------------|--------------|--------------------|---------------|------------|
| PBSS4021PX-Q v.1 | 20240105 | Product data sheet | - | - |

15. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
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