

PBSS4350D-Q

50 V low VCEsat NPN transistor

9 November 2023

**Product data sheet** 

## 1. General description

NPN low  $V_{CEsat}$  transistor in a SOT457 (SC-74) plastic package.

PNP complement: PBSS5350D-Q

## 2. Features and benefits

- Low collector-emitter saturation voltage
- High current capability
- Improved device reliability due to reduced heat generation
- Replacement for SOT89/SOT223 standard packaged transistors due to enhanced performance
- Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

- Supply line switching circuits
- Battery management applications
- DC/DC convertor applications
- Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers)

## 4. Quick reference data

Table 1. Qui	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	50	V
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	5	А
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C$ = 2 A; $I_B$ = 200 mA; pulsed; $t_p$ ≤ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	110	145	mΩ

# 5. Pinning information

Table 2	. Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	С	collector		
2	С	collector		С
3	В	base		в
4	E	emitter		Ì
5	С	collector	TSOP6 (SOT457)	sym014
6	С	collector		



# 6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PBSS4350D-Q	TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	<u>SOT457</u>			

### 7. Marking

Table 4. Marking codes					
Type number	Marking code				
PBSS4350D-Q	43				

## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	60	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	3	А
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	5	А
I <sub>BM</sub>	peak base current			-	1	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	600	mW
			[2]	-	750	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup> [1]

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>. [2]

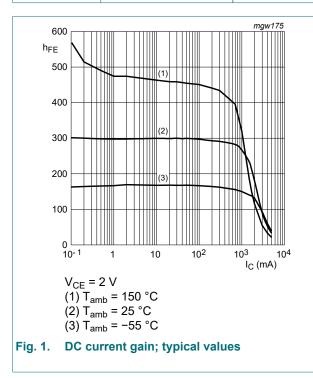
## 9. Thermal characteristics

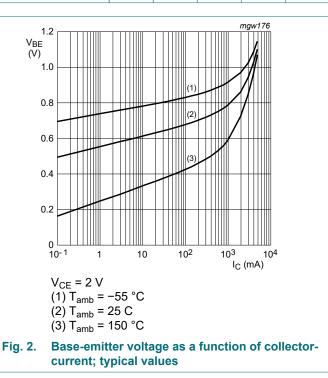
Table 6. Therma	al characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
ui(j-a)	thermal resistance from	in free air	[1]	-	-	208	K/W
	junction to ambient		[2]	-	-	160	K/W

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.
 Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.

# **10. Characteristics**

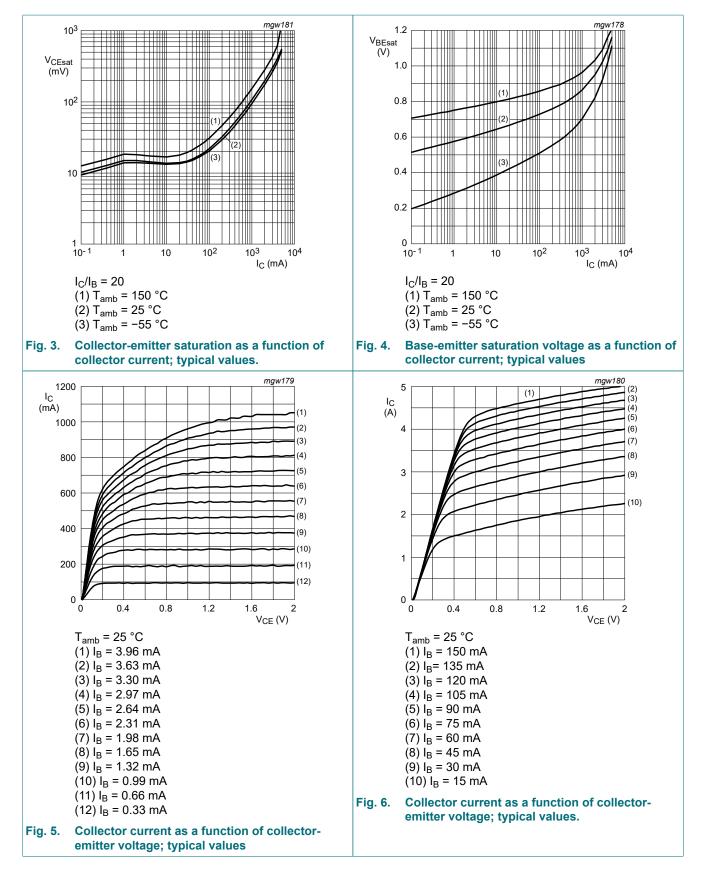
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	50	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C		200	-	-	
		$ \begin{array}{l} V_{CE} \texttt{= 2 V; } I_{C} \texttt{= 1 A; pulsed; } t_{p} \texttt{\leq 300 } \mu s; \\ \delta \texttt{\leq } 0.02;  T_{amb} \texttt{= 25 }^\circ C \end{array} $		200	-	-	
		$ \begin{array}{l} V_{CE} \texttt{= 2 V; } I_{C} \texttt{= 2 A; pulsed; } t_{p} \texttt{\leq } \texttt{300 } \mu s; \\ \delta \texttt{\leq } \texttt{0.02; } T_{amb} \texttt{= 25 °C} \end{array} $		100	-	-	
	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; T <sub>amb</sub> = 25 °C		-	-	90	mV
		I <sub>C</sub> = 1 A; I <sub>B</sub> = 50 mA; T <sub>amb</sub> = 25 °C		-	-	170	mV
		$I_{C}$ = 2 A; $I_{B}$ = 200 mA; pulsed; $t_{p} \le$		-	-	290	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$300 \ \mu s; \delta ≤ 0.02; T_{amb} = 25 \ °C$		-	110	145	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage			-	-	1.2	V
V <sub>BEon</sub>	base-emitter turn-on voltage	$ \begin{array}{l} V_{CE} \texttt{= 2 V; } I_{C} \texttt{= 1 A; pulsed; } t_{p} \texttt{\leq 300 } \mu s; \\ \delta \texttt{\leq } 0.02;  T_{amb} \texttt{= 25 }^\circ C \end{array} $		-	-	1.1	V
f <sub>T</sub>	transition frequency	$V_{CE}$ = 5 V; I <sub>C</sub> = 100 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C		100	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	-	30	pF





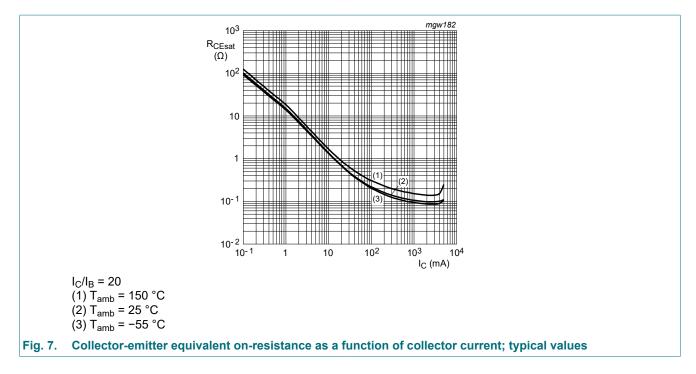
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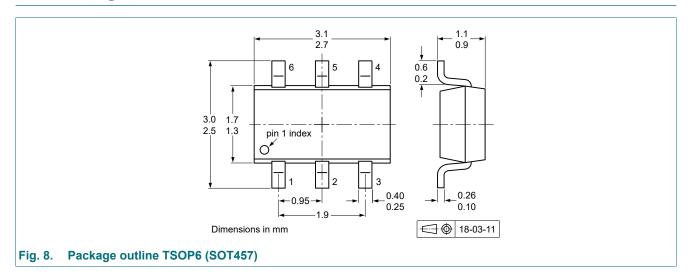


### 11. Test information

### **Quality information**

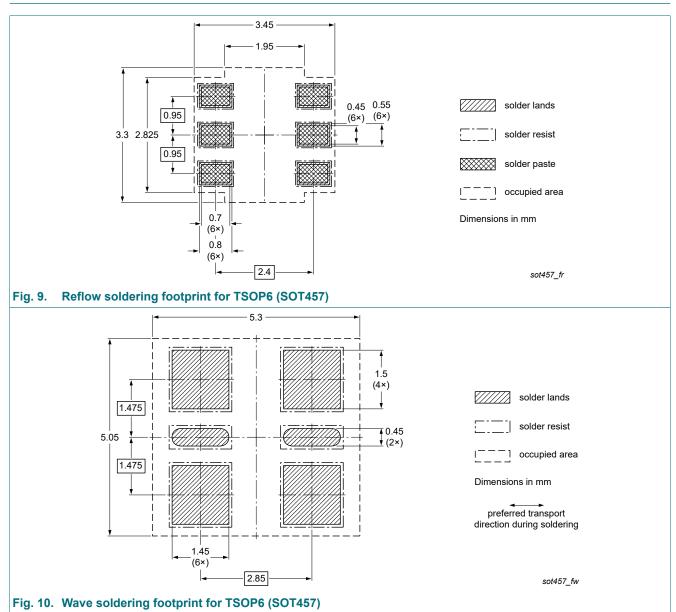
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### 12. Package outline



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# 13. Soldering



**Product data sheet** 

### **50 V low VCEsat NPN transistor**

# 14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS4350D-Q v.1	20231109	Product data sheet	-	-		

PBSS4350D-Q

# 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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