

PDTC123YT

NPN resistor-equipped transistor; R1 = 2.2 kΩ, R2 = 10 kΩ 17 April 2023 Product data sheet

1. General description

NPN Resistor-Equipped Transistor (RET) in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTA123YT

2. Features and benefits

- Built-in bias resistors
- Simplifies circuit design
- · Reduces component count
- Reduces pick and place costs
- AEC-Q101 qualified

3. Applications

- · General-purpose switching and amplification
- · Inverter and interface circuits
- · Circuit drivers

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		[1]	3.6	4.5	5.5	

^[1] See "Section 11: Test information" for resistor calculation and test conditions.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	
2	GND	ground (emitter)		R1
3	0	output (collector)	SOT23	GND Sym007



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6. Ordering information

Table 3. Ordering information

Type number	Package	ackage				
	Name	Description	Version			
PDTC123YT		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PDTC123YT	%AL

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

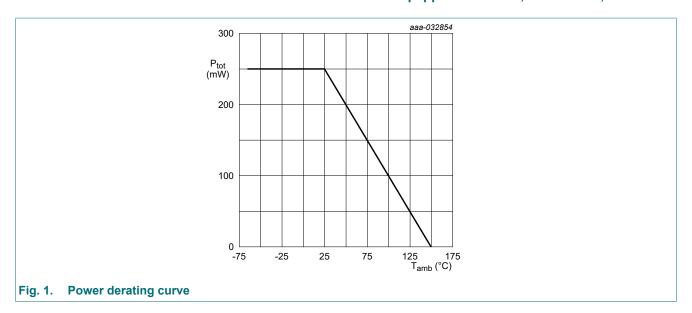
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V_{EBO}	emitter-base voltage	open collector		-	5	V
V _I	input voltage	positive		-	12	V
		negative		-	-5	V
Io	output current			-	100	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

 $^{[1] \}quad \text{Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, } 35~\mu\text{m copper, tin-plated and standard footprint.}$

NPN resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

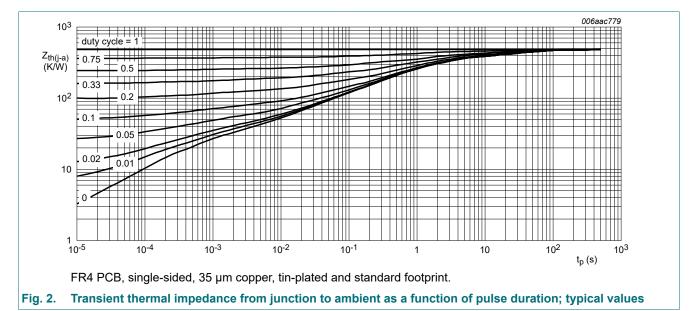


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.



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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 ^{\circ}\text{C}$		50	-	-	V
I _{CBO}	collector-base cut-off current	_{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	100	nA
I _{CEO}	collector-emitter cut-off	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	100	nA
	current	V _{CE} = 30 V; I _B = 0 A; T _j = 150 °C		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	700	μΑ
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 5 mA; T _{amb} = 25 °C		35	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	150	mV
V _{I(off)}	off-state input voltage	V _{CE} = 5 V; I _C = 100 μA; T _{amb} = 25 °C		-	0.75	0.3	V
V _{I(on)}	on-state input voltage	V_{CE} = 300 mV; I_{C} = 20 mA; T_{amb} = 25 °C		2.5	1.15	-	V
R1	bias resistor 1 (input)		[1]	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		[1]	3.6	4.5	5.5	
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$		-	-	2	pF

[1] See "Section 11: Test information" for resistor calculation and test conditions.

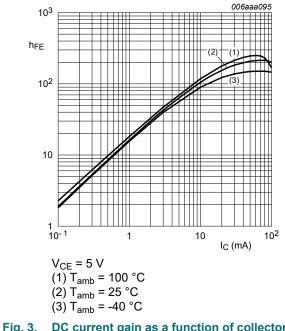


Fig. 3. DC current gain as a function of collector current; typical values

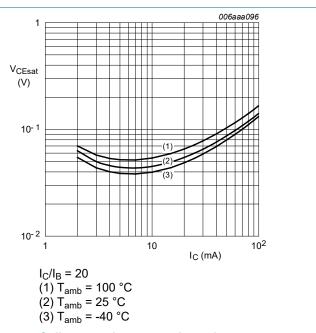
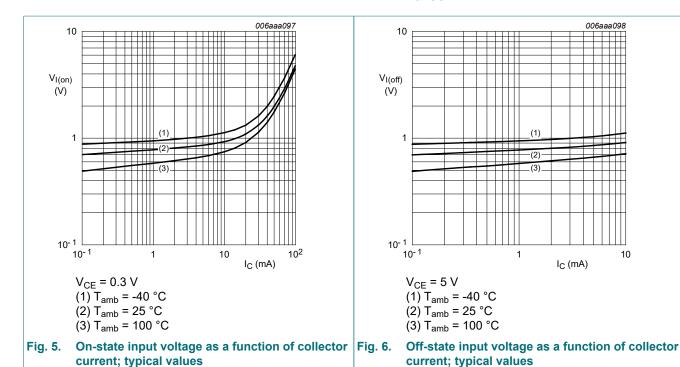


Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values

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11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

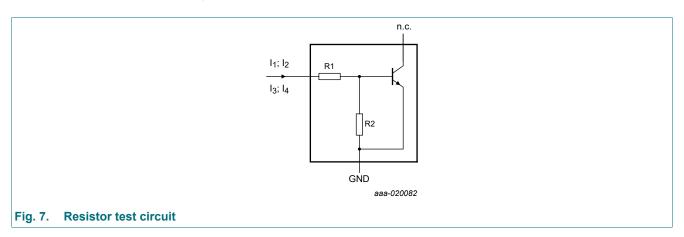
Resistor calculation

Calculation of bias resistor 1 (R1)

$$R_I = \frac{V(I_2) - V(I_I)}{I_2 - I_I}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$



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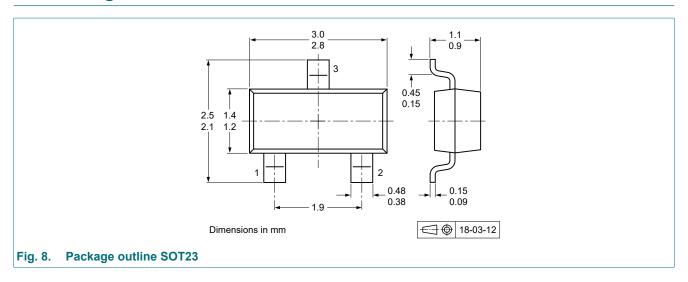
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Resistor test conditions

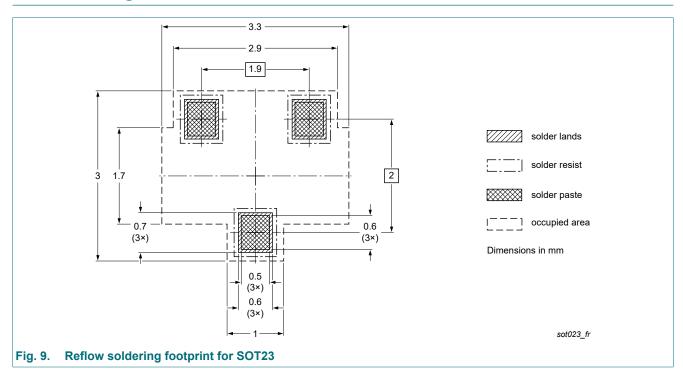
Table 8. Resistor test conditions

PDTC123YT	R1 (kΩ)	R2 (kΩ)	Test conditions	Test conditions				
			I ₁	l ₂	l ₃	14		
NPN	2.2	10	1300 µA	1500 µA	-350 μΑ	-450 μA		

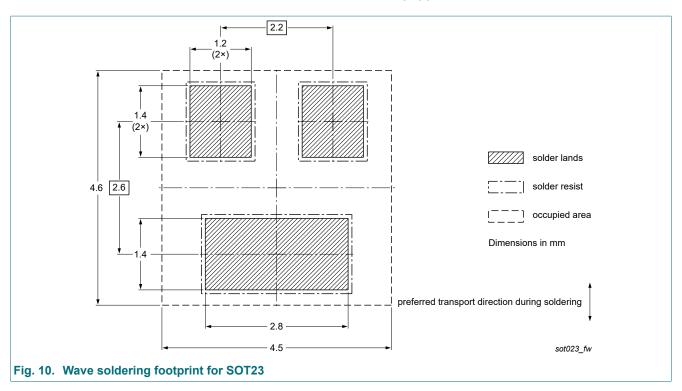
12. Package outline



13. Soldering



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NPN resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

14. Revision history

Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTC123YT v.5	20230417	Product data sheet	-	PDTC123Y_SER_4
Modifications:	Nexperia. Legal texts have Section "Packing	s data sheet has been rede been adapted to the new of information" removed. et splitted to single type dat	company name where a	
PDTC123Y_SER_4	20091116	Product data sheet	-	PDTC123Y_SER_3
PDTC123Y_SER_3	20050324	Product data sheet	-	PDTC123YT_2
PDTC123YT_2	20040510	Objective data sheet	-	PDTC123YT_1
PDTC123YT_1	20040406	Objective data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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