**Product data sheet** 

## 1. General description

NPN Resistor-Equipped Transistor (RET) in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package.

PNP complement: PDTB143EU

### 2. Features and benefits

- 500 mA output current capability
- Built-in bias resistors
- · Simplifies circuit design
- Reduces component count
- ± 10 % resistor ratio tolerance
- High temperature applications up to 175 °C
- AEC-Q101 qualified

## 3. Applications

- · Controlling IC inputs
- · Cost-saving alternative to BC817 series in digital applications
- · Switching loads

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	50	V
Io	output current		-	-	500	mA
R1	bias resistor 1 (input)		3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio		0.9	1	1.1	



50 V, 500 mA NPN resistor-equipped transistor

# 5. Pinning information

#### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	<u></u> 3	
2	GND	ground (emitter)		R <sub>1</sub>
3	0	output (collector)	SC-70 (SOT323)	GND R2

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PDTD143EU	SC-70	plastic, surface-mounted package; 3 leads; 1.3 mm pitch; 2 mm x 1.25 mm x 0.95 mm body	SOT323			

## 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
PDTD143EU	ZT%

[1] % = placeholder for manufacturing site code

#### 50 V, 500 mA NPN resistor-equipped transistor

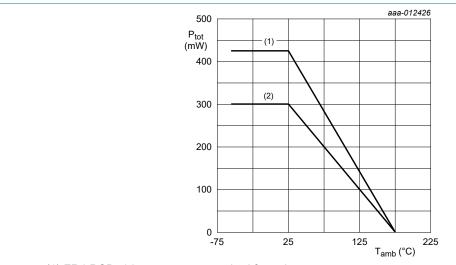
## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	10	V
VI	input voltage			-10	30	V
Io	output current			-	500	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
			[2]	-	425	mW
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-55	175	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.



- (1) FR4 PCB, 4-layer copper, standard footprint
- (2) FR4 PCB, single-sided copper, tin-plated and standard footprint

Fig. 1. Power derating curves

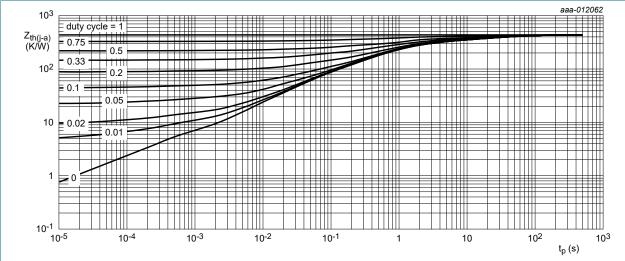
#### 50 V, 500 mA NPN resistor-equipped transistor

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

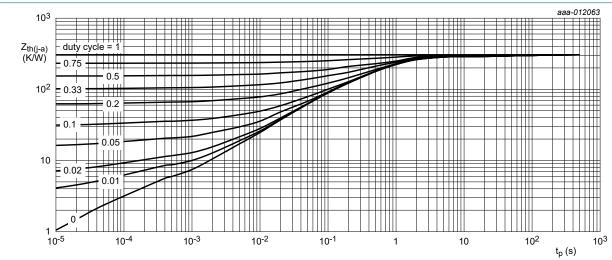
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
uig-a)	thermal resistance from	in free air	[1]	-	-	500	K/W
	junction to ambient		[2]	-	-	353	K/W

- [1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.



FR4 PCB, single-sided copper, tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT323/SC-70; typical values



FR4 PCB, 4-layer copper, tin-plated and standard footprint.

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT323/SC-70; typical values

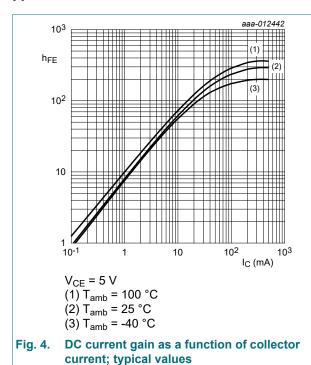
### 50 V, 500 mA NPN resistor-equipped transistor

### 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 40 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
I <sub>CEO</sub>	collector-emitter cut-off current	V <sub>CE</sub> = 50 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	0.5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	0.9	mA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; T <sub>amb</sub> = 25 °C		60	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 50 \text{ mA}; I_B = 2.5 \text{ mA}; T_{amb} = 25 \text{ °C}$		-	-	100	mV
V <sub>I(off)</sub>	off-state input voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 100 μA; T <sub>amb</sub> = 25 °C		0.6	0.9	1.5	V
V <sub>I(on)</sub>	on-state input voltage	$V_{CE} = 0.3 \text{ V}; I_{C} = 20 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		1	1.6	2.2	V
R1	bias resistor 1 (input)			3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio			0.9	1	1.1	
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$		-	7	-	pF
f <sub>T</sub>	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 50 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	[1]	-	225	-	MHz

#### [1] Characteristics of built-in transistor.



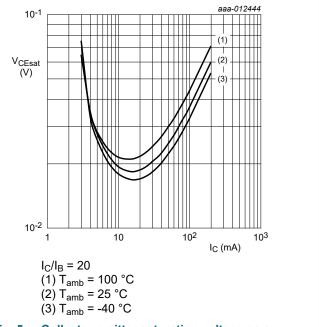
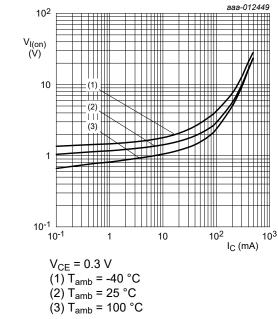
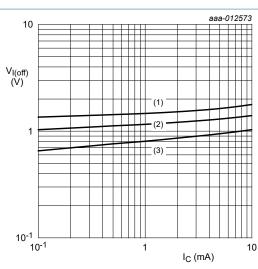


Fig. 5. Collector-emitter saturation voltage as a function of collector current; typical values

#### 50 V, 500 mA NPN resistor-equipped transistor





f = 1 MHz $T_{amb} = 25 \, ^{\circ}C$ 

V<sub>CE</sub> = 5 V (1) T<sub>amb</sub> = -40 °C (2) T<sub>amb</sub> = 25 °C (3) T<sub>amb</sub> = 100 °C

Fig. 6. On-state input voltage as a function of collector | Fig. 7. Off-state input voltage as a function of collector current; typical values current; typical values

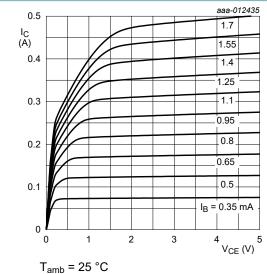


Fig. 8. Collector current as a function of collectoremitter voltage; typical values

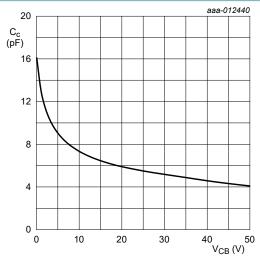
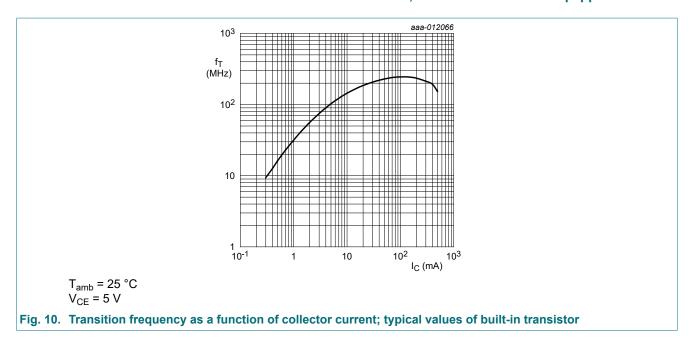


Fig. 9. Collector capacitance as a function of collectorbase voltage; typical values

#### 50 V, 500 mA NPN resistor-equipped transistor

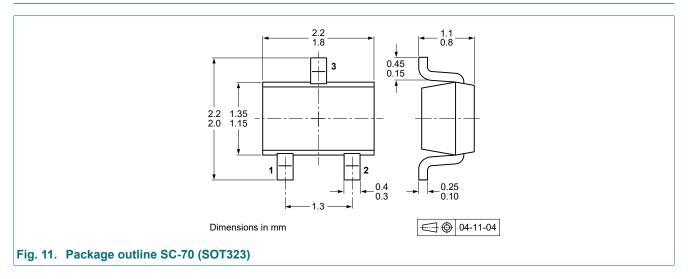


### 11. Test information

#### **Quality information**

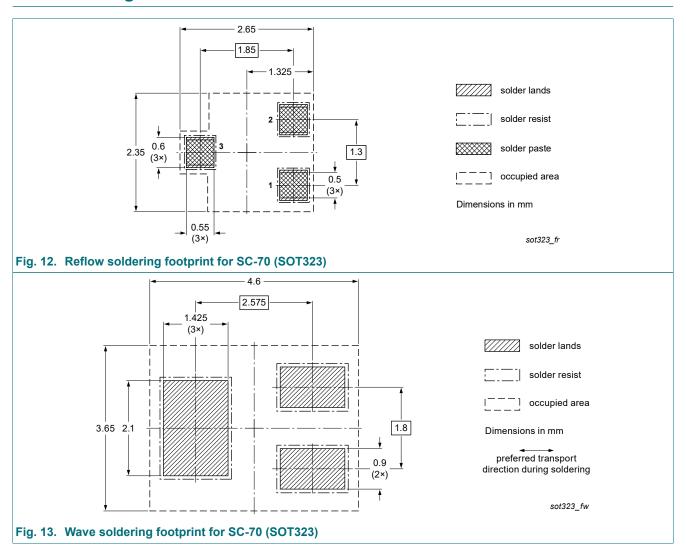
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline



#### 50 V, 500 mA NPN resistor-equipped transistor

## 13. Soldering



## 50 V, 500 mA NPN resistor-equipped transistor

# 14. Revision history

#### Table 8. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PDTD143EU v.2	20240417	Product data sheet	-	PDTD1XXXU_SER v.1				
Modifications:	Family data sheet reduced to single type data sheet.							
PDTD1XXXU_SER v.1	20140513	Product data sheet	-	-				

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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### 50 V, 500 mA NPN resistor-equipped transistor

## **Contents**

1
1
1
1
2
2
2
3
4
5
7
7
8
9
10

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11 / 11

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