

PEMH2

50 V, 100 mA NPN/NPN resistor-equipped double transistor; R1 = 47 k Ω , R2 = 47 k Ω

29 December 2022

Product data sheet

1. General description

NPN/NPN double Resistor-Equipped Transistor (RET) in an ultra small and flat lead SOT666 Surface-Mounted Device (SMD) plastic package.

NPN/PNP complement: PEMD12

PNP/PNP complement: PEMB2

2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs

3. Applications

- Low current peripheral driver
- Controlling IC inputs
- Replacement for general purpose transistors in digital applications

4. Quick reference data

Table 1. Quic	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transiste	or						
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
I _O	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	33	47	61	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	

[1] See "Test information" for resistor calculation and test conditions.



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	GND1	GND (emitter) TR1		O1 I2 GND2
2	11	input (base) TR1	6 5 4	
3	02	output (collector) TR2		
4	GND2	GND (emitter) TR2		
5	12	input (base) TR2	0	
6	01	output (collector) TR1		
			SOT666	
				GND1 I1 O2 sym063

6. Ordering information

Table 3. Ordering informationType number	Package					
	Name	Description	Version			
PEMH2	SOT666	plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>			

7. Marking

Ta	ble 4. Marking codes	
Ту	pe number	Marking code
PE	EMH2	Z2

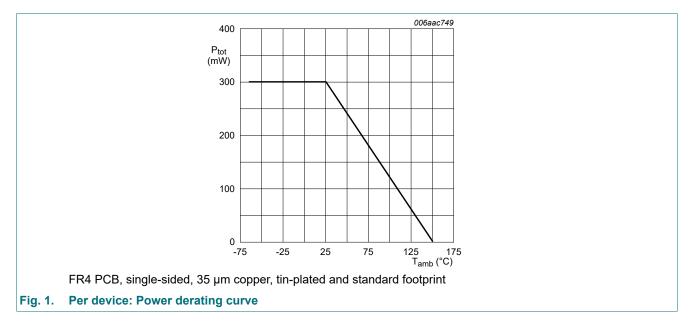
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transist	or	1	L			
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	10	V
VI	input voltage	positive		-	40	V
		negative		-	-10	V
I _O	output current			-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	200	mW
Per device		1	L			
P _{tot}	total power dissipation	T _{amb} = 25 °C	[1] [2]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.
Reflow soldering is the only recommended soldering method.



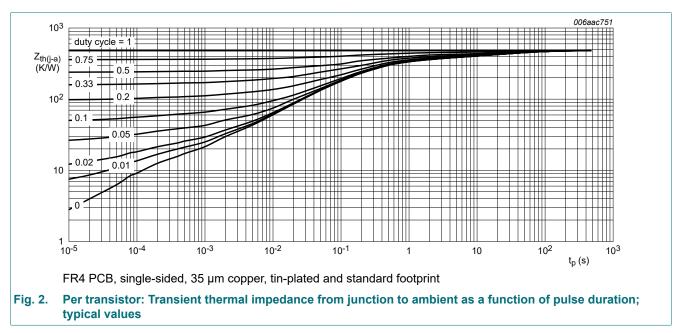
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transistor							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	625	K/W
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	417	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

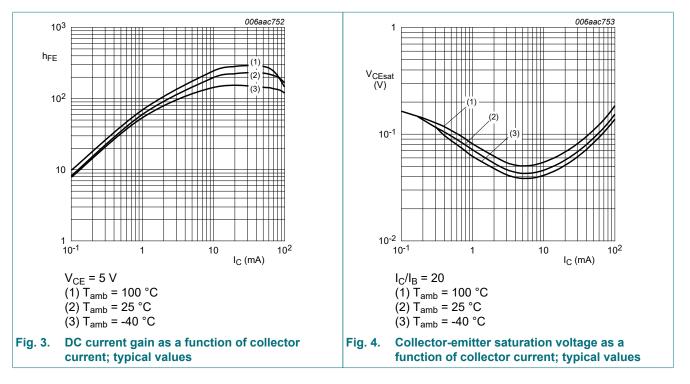


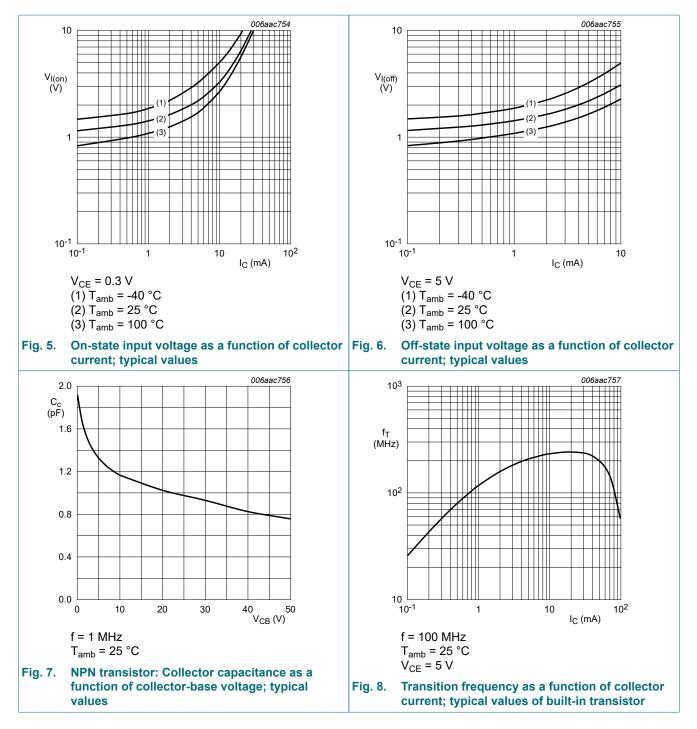
10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or	1					
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A; T _{amb} = 25 °C		50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 2 mA; I _B = 0 A; T _{amb} = 25 °C		50	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
I _{CEO} collector-emitter current	collector-emitter cut-off	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	1	μA
	current	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C; T _j = 150 °C		-	-	5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	90	μA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 5 mA; T _{amb} = 25 °C		80	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 0.5 mA; T _{amb} = 25 °C		-	-	150	mV
V _{I(off)}	off-state input voltage	V _{CE} = 5 V; I _C = 100 μA; T _{amb} = 25 °C		-	1.2	0.8	V
V _{I(on)}	on-state input voltage	V _{CE} = 0.3 V; I _C = 2 mA; T _{amb} = 25 °C		3	1.6	-	V
R1	bias resistor 1 (input)		[1]	33	47	61	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	-	2.5	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 10 mA; f = 100 MHz; T _{amb} = 25 °C	[2]	-	230	-	MHz

[1] See "Test information" for resistor calculation and test conditions.

[2] Characteristics of built-in transistor





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11. Test information

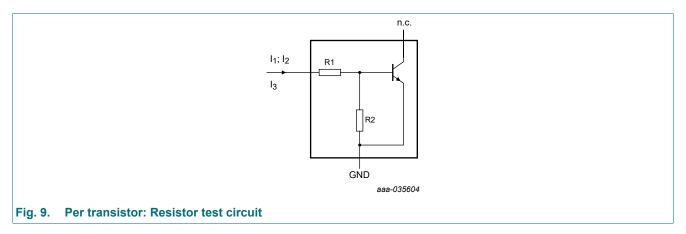
Resistor calculation

Calculation of bias resistor 1 (R1)

$$R_1 = \frac{V(I_2) - V(I_1)}{I_2 - I_1}$$

Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I3)}{R1 \cdot I3} - 1$$

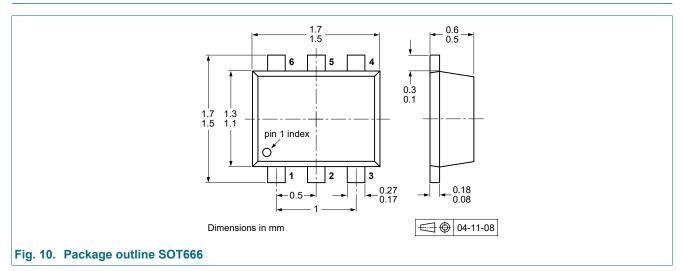


Resistor test conditions

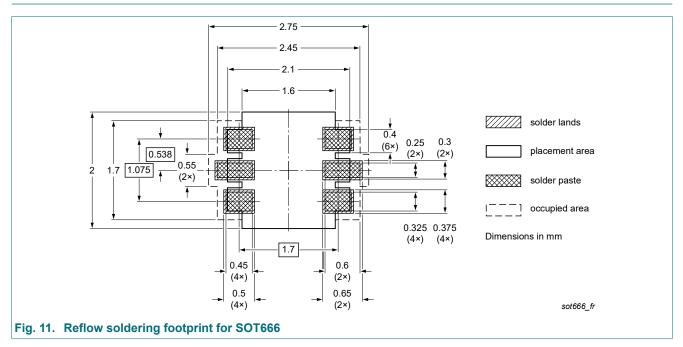
Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions		
			l ₁	l ₂	l ₃
PEMH2	47	47	60 µA	110 µA	-85 µA

12. Package outline



13. Soldering



Product data sheet

14. Revision history

Table 9. Revision histo	ory			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PEMH2 v.6	20221229	Product data sheet	-	PEMH2_PUMH2 v.5
Modifications:	Nexperia.Legal texts have beeFamily data sheet re	ata sheet has been redes en adapted to the new co educed to single type data to non-automotive qualifi is removed.	mpany name where app a sheet.	
PEMH2_PUMH2 v.5	20111205	Product data sheet	-	PEMH2_PUMH2 v.4
PEMH2_PUMH2 v.4	20111116	Product data sheet	-	PEMH2_PUMH2 v.3
PEMH2_PUMH2 v.3	20040414	Product data sheet	-	PEMH2_PUMH2 v.2
PEMH2_PUMH2 v.2	20031002	Product specification	-	PEMH2 v.1 PUMH2 v.1
PEMH2 v.1	20011022	Preliminary specification	-	-
PUMH2 v.1	19990803	Product specification	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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