

# PESD2IVN48T-Q

# ESD protection for in-vehicle networks

2 May 2022

**Product data sheet** 

## 1. General description

ESD protection device in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package, designed to protect two automotive in-vehicle network bus lines from the damage caused by electrostatic discharge (ESD) and other transients for 24V and 48 V board net.

## 2. Features and benefits

- Reverse stand-off voltage: V<sub>RWM</sub> = 48 V
- Low clamping voltage: V<sub>CL</sub>= 67 V at I<sub>PPM</sub> = 3.5 A
- Typical diode capacitance matching: ∆C<sub>d</sub>/C<sub>d</sub> = 0.5 %
- Low capacitance for improved signal integrity: C<sub>d typ</sub> = 7.1 pF
- ESD protection up to 30 kV (IEC 61000-4-2; ISO 10605; C = 330 pF, R = 330 Ω)
- Low leakage current: I<sub>RM</sub> < 1 nA</li>
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

ESD protection for in-vehicle network lines in automotive environments

- CAN/CAN-FD
- LIN
- FlexRay
- SENT

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	48	V
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1] [2]	-	-	3.5	А
V <sub>CL</sub>	clamping voltage	$I_{PPM} = 3.5 \text{ A}; t_p = 8/20  \mu\text{s}; T_{amb} = 25 ^{\circ}\text{C}$	[3] [2]	-	67	-	V

- [1] According to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.



# 5. Pinning information

## **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	3	
2	K2	cathode (diode 2)		к1
3	К	common cathode	1 2	CC K2 006aaa155
			SOT23	

# 6. Ordering information

## **Table 3. Ordering information**

Type number	er Package					
	Name	Description	Version			
PESD2IVN48T-Q		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

# 7. Marking

## Table 4. Marking codes

Type number	Marking code[1]
PESD2IVN48T-Q	T4%

[1] % = placeholder for manufacturing site code

2/11

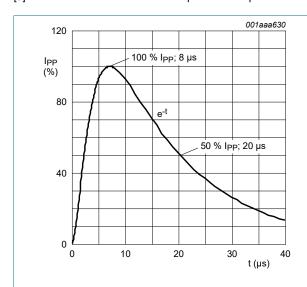
# 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	$t_p = 8/20 \ \mu s$	[1] [2]	-	3.5	Α
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximi	um ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[2] [3]	-	30	kV
	voltage	ISO 10605; contact discharge; C = 330 pF, R = 330 $\Omega$	[2] [3]	-	30	kV
		ISO 10605; contact discharge; C = 150 pF, R = 330 $\Omega$	[2] [3]	-	30	kV

- According to IEC 61000-4-5. Measured from pin 1 or 2 to pin 3.
- Device stressed with ten non-repetitive ESD pulses.



8/20 µs pulse waveform according to Fig. 1. IEC 61000-4-5

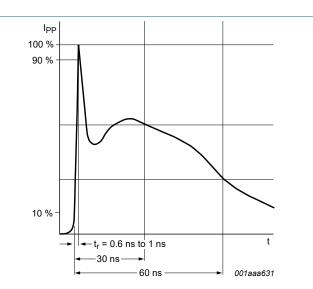


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

**Table 6. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	48	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 10 mA; T <sub>amb</sub> = 25 °C	[1]	56	64	76	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 48 V; T <sub>amb</sub> = 25 °C	[1]	-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	[1]	-	7.1	8.6	pF
$\Delta C_d/C_d$	diode capacitance		[2]	-	0.5	-	%
	matching	f = 1 MHz; V <sub>R</sub> = 2.5 V; T <sub>amb</sub> = 25 °C	[2]	-	0.5	-	%
V <sub>CL</sub>	clamping voltage	$I_{PP}$ = 1 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[3] [1]	-	61	-	V
		$I_{PPM}$ = 3.5 A; $t_p$ = 8/20 µs; $T_{amb}$ = 25 °C	[3] [1]	-	67	-	V
		$I_{PP}$ = 16 A; $t_p$ = 100ns; TLP; $T_{amb}$ = 25 °C	[4] [1]	-	64	-	V
R <sub>dyn</sub>	dynamic resistance	$I_R$ = 10 A; $t_p$ = 100ns; TLP; $T_{amb}$ = 25 °C	[4] [1]	-	0.55	-	Ω

- [1] Measured from pin 1 or 2 to pin 3.
- 2]  $\Delta C_d$  is the difference of the capacitance measured between pin 1 and pin 3 and the capacitance measured between pin 2 and pin 3.
- [3] Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [4] Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008

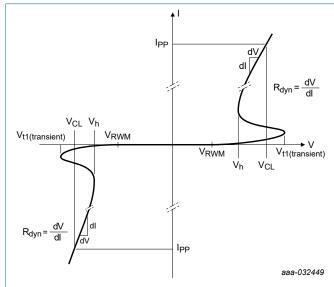


Fig. 3. Transient characteristics for a bidirectional ESD protection device

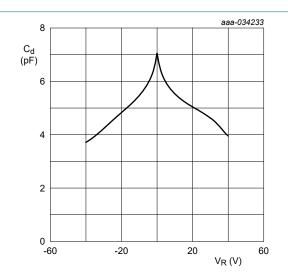


Fig. 4. Diode capacitance as a function of input voltage; typical values

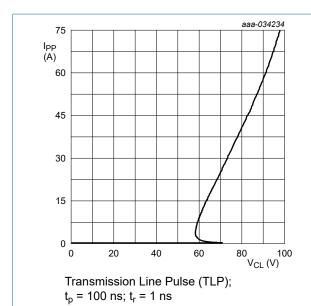


Fig. 5. Dynamic resistance with positive clamping; typical values

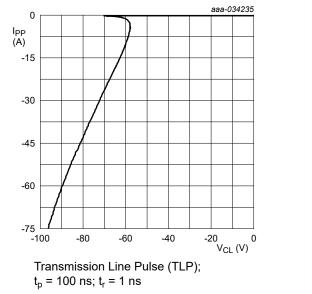
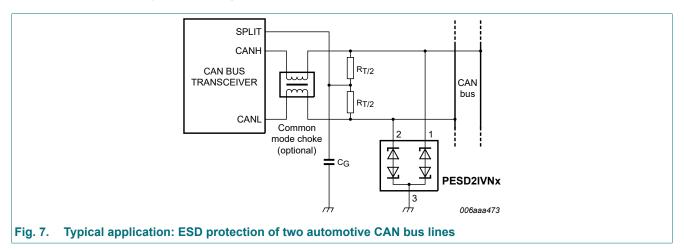


Fig. 6. Dynamic resistance with negative clamping; typical values

## 10. Application information

The device is designed for the protection of two automotive IVN bus lines from the damage caused by ESD and surge pulses.



#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

## 11. Test information

## **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline

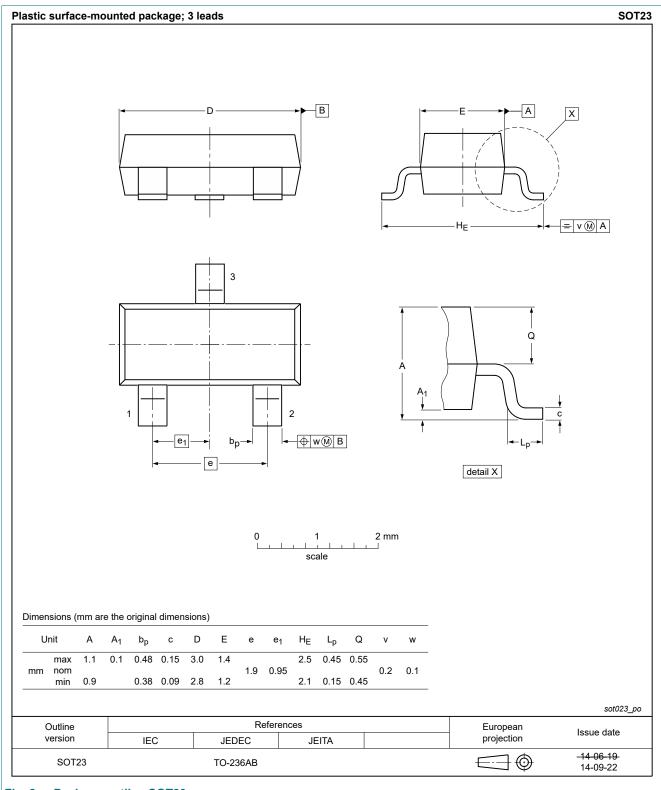
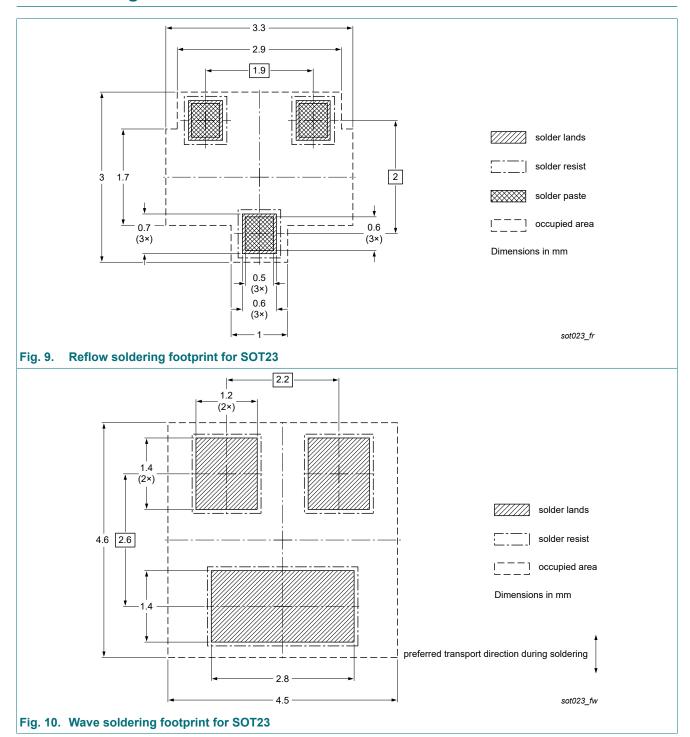


Fig. 8. Package outline SOT23

# 13. Soldering



# 14. Revision history

## **Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD2IVN48T-Q v.1	20220502	Product data sheet	-	-

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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