

Product data sheet

1. General description

Uni-directional double ESD protection diode in a SOT23 plastic package. Designed to protect up to two transmission or data lines from ElectroStatic Discharge (ESD) damage.

2. Features and benefits

- Uni-directional ESD protection of up to two lines
- Max. peak pulse power: P_{pp} = 260 W at t_p = 8/20 us
- Low clamping voltage: $V_{(CL)R} = 20 \text{ V}$ at $I_{pp} = 15 \text{ A}$
- Ultra-low reverse leakage current: I_{RM} < 150 nA
- ESD protection > 30 kV
- IEC 61000-4-2; level 4 (ESD)
- IEC 61000-4-5 (surge); I_{pp} = 15 A at t_p = 8/20 us

3. Applications

- · Computers and peripherals
- · Communication systems
- · Audio and video equipment
- · High speed data lines
- Parallel ports

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C	[1]	-	-	5.2	V
C_d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	152	200	pF

[1] Measured from pin 1 or 2 to pin 3.



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)	3	3
2	K2	cathode (diode 2)		
3	A	common anode	1 2 SOT23	1 2 006aaa154

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD5V2S2UT	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PESD5V2S2UT	%U1

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	260	W
I _{PPM}	rated peak pulse current		[1] [2]	-	15	Α
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	um ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[3] [2]	-	30	kV
	voltage	IEC 61000-4-2 (air discharge)	[3] [2]	-	15	kV
		MIL-STD-883 (human body model)		-	10	kV

- [1] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.
- [2] Measured from pin 1 or 2 to pin 3.
- [3] Device stressed with ten non-repetitive ESD pulses.

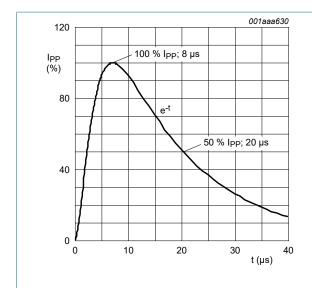


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

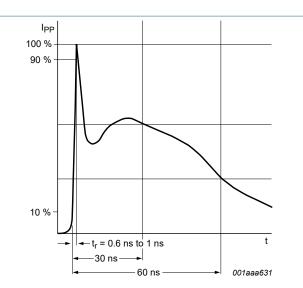


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

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9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C	[1]	-	-	5.2	V
V_{BR}	breakdown voltage	I _R = 5 mA; T _{amb} = 25 °C	[1]	6.4	6.8	7.2	V
I _{RM}	reverse leakage current	V _{RWM} = 5.2 V; T _{amb} = 25 °C	[1]	-	0.15	1	μA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	[1]	-	152	200	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; T _{amb} = 25 °C	[1] [2]	-	-	9	V
		I _{PP} = 15 A; T _{amb} = 25 °C	[1] [2]	-	-	20	V
R _{diff}	differential resistance	I _R = 1 mA; T _{amb} = 25 °C	[1]	-	-	80	Ω

- [1] Measured from pin 1 or 2 to pin 3.
- [2] Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5.

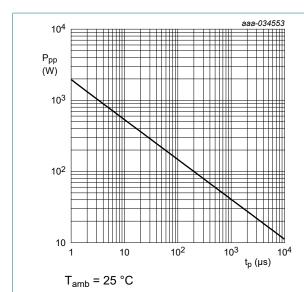


Fig. 3. Peak pulse power dissipation as a function of pulse time; typical values

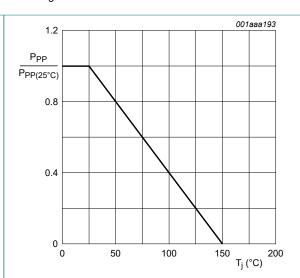


Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

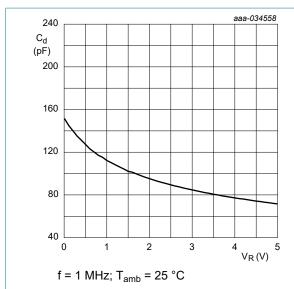


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

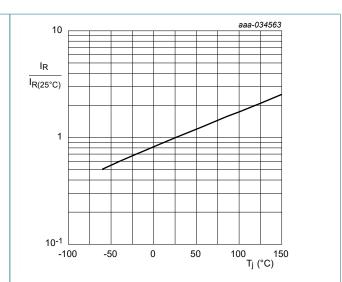
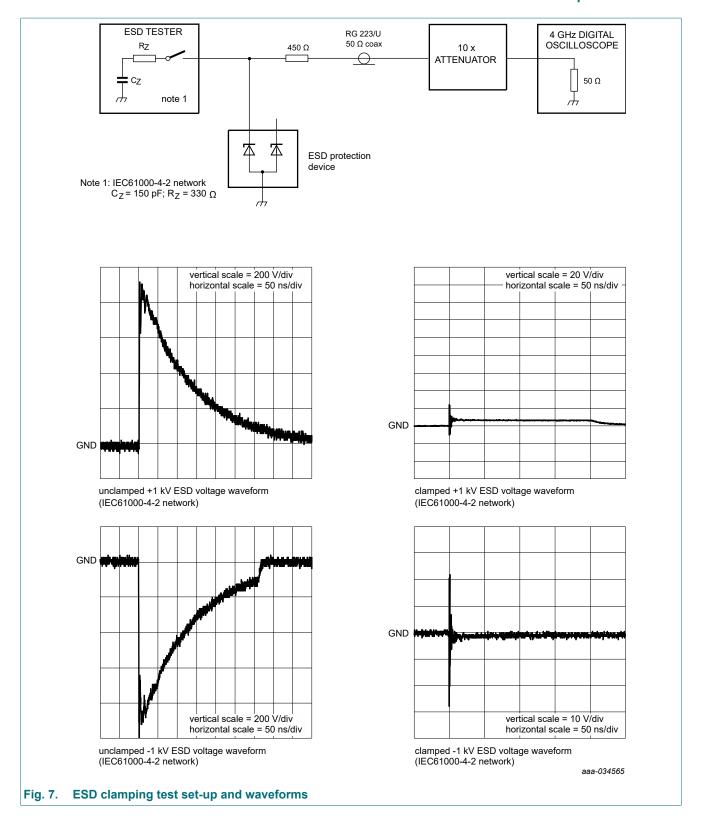
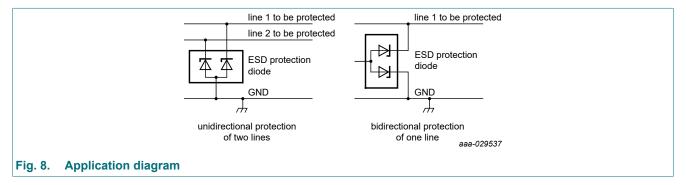


Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values



10. Application information

The device is designed for the protection of up to two unidirectional data or signal lines from the damage caused by ESD and surge pulses. The devices may be used on lines where the signal polarities are either positive or negative with respect to ground.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline

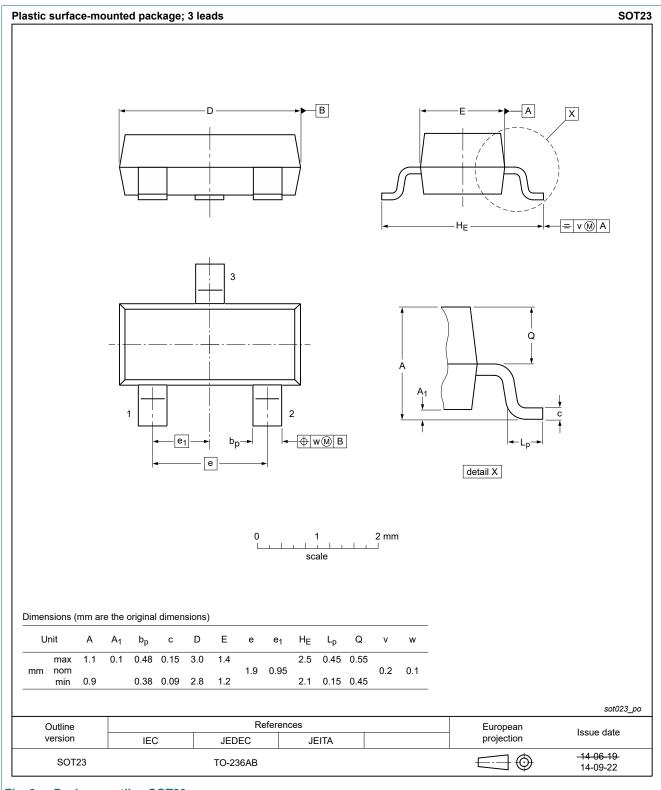
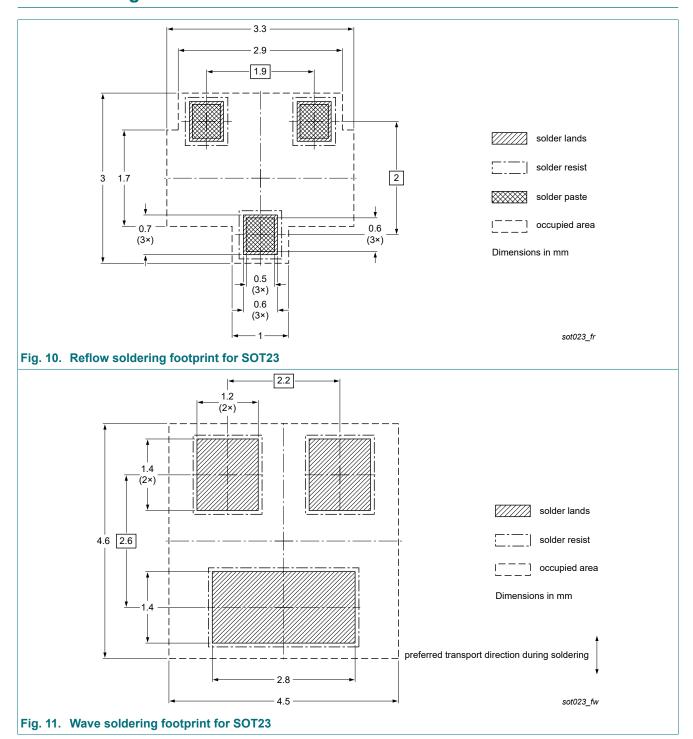


Fig. 9. Package outline SOT23

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12. Soldering



13. Revision history

Table 7. Revision history

Table 1. Revision misto	· y				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD5V2S2UT v.3	20230401	Product data sheet	-	PESDXS2UT_SERIES v.2	
Modifications:	automotive(-Q) prod	led to non-automotive qualification. Please refer to nexperia.com for) product alternative(s). leet reduced to single type data sheet			
PESDXS2UT_SERIES v.2	20040415	Product data sheet	-	PESDXS2UT_SERIES v.1	
PESDXS2UT_SERIES v.1	20030820	Product data sheet	-	-	

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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