## 1. General description

Ultra low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode, part of the TrEOS Protection family. This device is housed in a small leadless DFN1006-2 (SOD882-2) Surface-Mounted Device (SMD) plastic package. The combination of ultra low capacitance, high ESD maximum rating and ultra small package makes the device ideal for high-speed data lines protection.

### 2. Features and benefits

- Bidirectional ESD protection of one line
- Ultra low capacitance: C<sub>d</sub> < 0.3 pF</li>
- ESD protection up to 15 kV (IEC 61000-4-2)
- Deep snap-back combined with dynamic resistance of 0.35 Ohm
- IEC 61000-4-5 (surge): I<sub>PP</sub> = 8.8 A peak pulse (average measured)
- Extremely low clamping voltage to protect sensitive I/Os

## 3. Applications

- Ultra high-speed data lines such as USB 3.2 or HDMI 2.0
- Cellular handsets and accessories
- Portable electronics
- Communication systems
- · Computers and peripherals

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-5.5	-	5.5	<b>V</b>
C <sub>d</sub>	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$	[1]	-	0.245	0.3	pF

[1] Measured from pin 1 to pin 2.



## Extremely low clamping bidirectional ESD protection diode

# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)	Transparent top view  DFN1006-2 (SOD882-2)	K1 K2 sym045

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PESD5V5C1BL		Leadless ultra small plastic package; 2 terminals; body 1.0 x 0.6 x 0.47 mm	SOD882-2			

## 7. Marking

### Table 4. Marking codes

Type number	Marking code
PESD5V5C1BL	н8н

### Extremely low clamping bidirectional ESD protection diode

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	6.5	Α
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximi	um ratings					
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2; contact discharge	[2] [3]	-	15	kV
	voltage	ISO 10605: contact discharge; C = 330 pF, R = 330 $\Omega$	[2] [3]	-	15	kV
		ISO 10605: contact discharge; C = 150 pF, R = 330 $\Omega$	[2] [3]	-	15	kV

- [1] Device stressed with 8/20 μs exponential decay waveform according to IEC 61000-4-5.
- [2] Device stressed with ten non-repetitive ESD pulses.
- [3] Measured from pin 1 to pin 2.

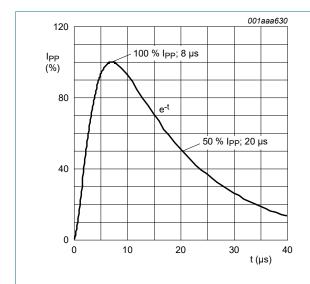


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

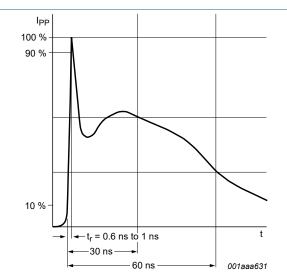


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## Extremely low clamping bidirectional ESD protection diode

## 9. Characteristics

**Table 6. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-5.5	-	5.5	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C	[1]	7.5	8	-	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 5.5 V; T <sub>amb</sub> = 25 °C	[1]	-	1	50	nA
$C_d$	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	[1]	-	0.245	0.3	pF
$V_{CL}$	clamping voltage	$I_{PP}$ = 8 A; $t_p$ = TLP; $T_{amb}$ = 25 °C	[2] [1]	-	5.4	-	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 10 A; T <sub>amb</sub> = 25 °C	[2] [1]	-	0.35	-	Ω

- [1] Measured from pin 1 to pin 2.
- [2] Non-repetitive current pulse, Transmission Line Pulse (TLP) tp = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008.

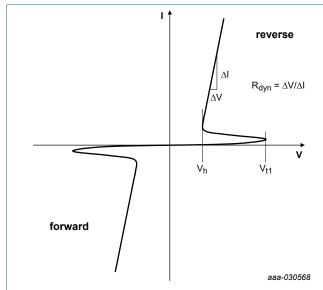


Fig. 3. V-I characteristics for a bidirectional ESD protection diode

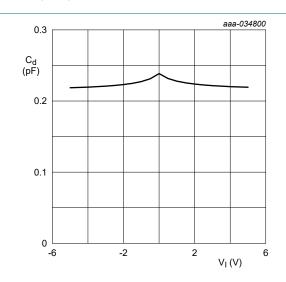


Fig. 4. Capacitance as a function of input voltage; typical values

## Extremely low clamping bidirectional ESD protection diode

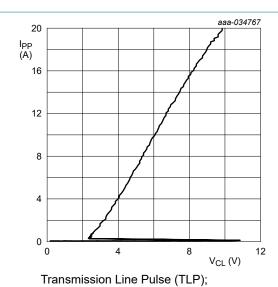


Fig. 5. Dynamic resistance with positive clamping; typical values

 $t_{\rm p}$  = 100 ns;  $t_{\rm r}$  = 1 ns

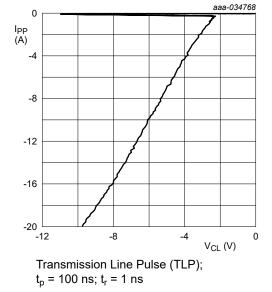
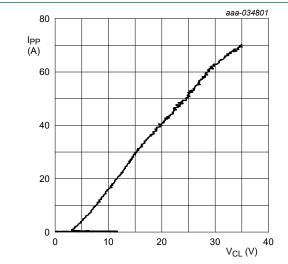
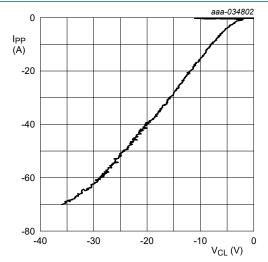


Fig. 6. Dynamic resistance with negative clamping; typical values



Very-Fast Transmission Line Pulse (VF-TLP);  $t_p = 5 \text{ ns}$ ;  $t_r = 600 \text{ ps}$ 





Very-Fast Transmission Line Pulse (VF-TLP);  $t_p = 5 \text{ ns}$ ;  $t_r = 600 \text{ ps}$ 

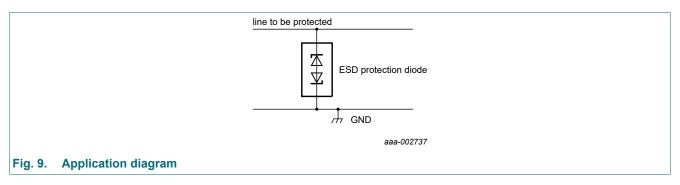
Fig. 8. Dynamic resistance with negative clamping; typical values

### Extremely low clamping bidirectional ESD protection diode

## 10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.

The device uses an advanced clamping structure showing a negative dynamic resistance. This snap-back behavior strongly reduces the clamping voltage to the system behind the ESD protection during an ESD event. Do not connect unlimited DC current sources to the data lines to avoid keeping the ESD protection device in snap-back state after exceeding breakdown voltage (due to an ESD pulse for instance).



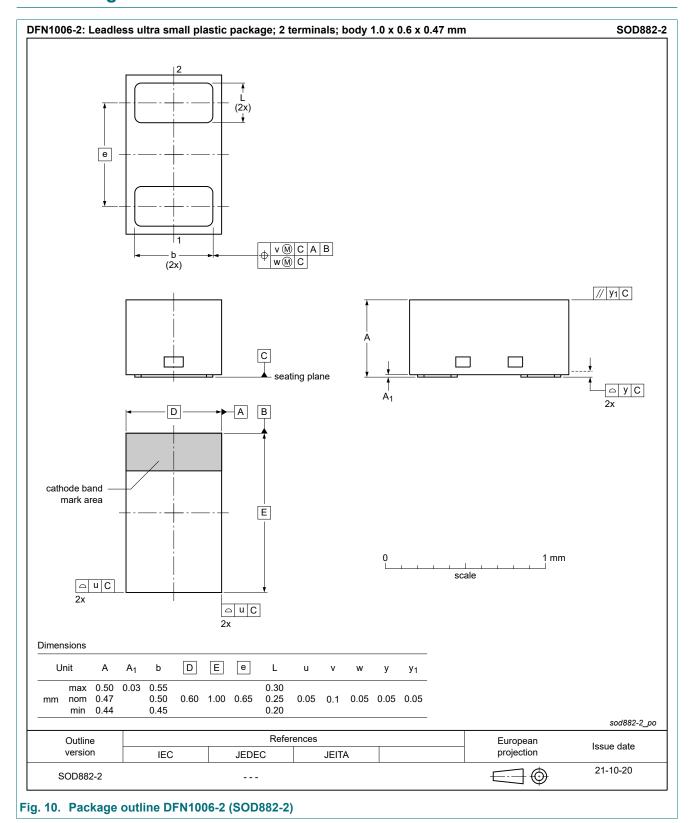
#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

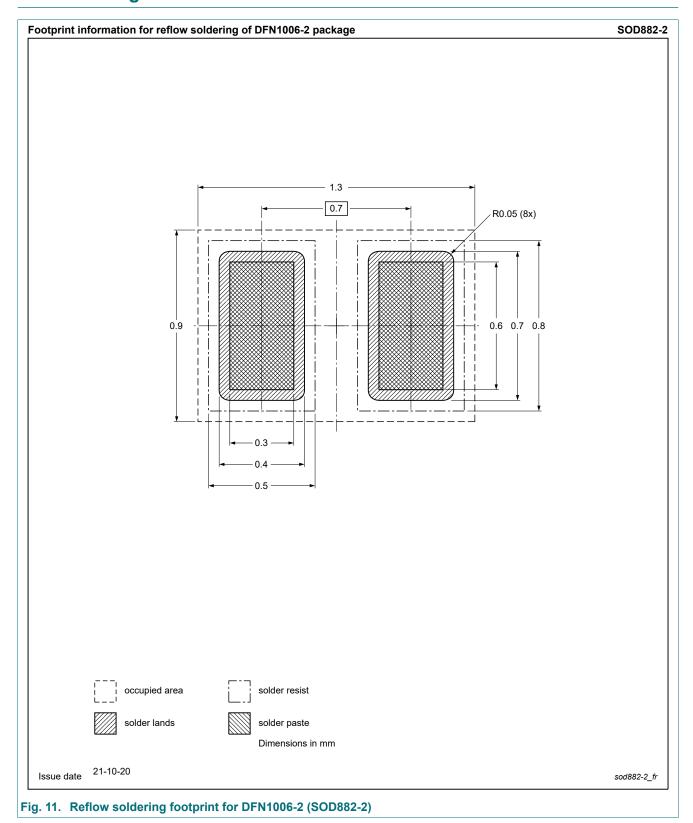
## Extremely low clamping bidirectional ESD protection diode

# 11. Package outline



## Extremely low clamping bidirectional ESD protection diode

# 12. Soldering



## Extremely low clamping bidirectional ESD protection diode

# 13. Revision history

#### Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PESD5V5C1BL v.2	20230406	Product data sheet	-	PESD5V5C1BL v.1			
Modifications:	Chapter "Characteris	Chapter "Characteristics": new graphs in Fig. 7 and Fig. 8					
PESD5V5C1BL v.1	20230315	Product data sheet	-	-			

### Extremely low clamping bidirectional ESD protection diode

## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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# PESD5V5C1BL

## Extremely low clamping bidirectional ESD protection diode

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