

25 November 2022

Product data sheet

1. General description

Low capacitance unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 (SC-79) ultra small and flat lead Surface-Mounted Device (SMD) plastic package, designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- ESD protection of one line
- Low diode capacitance
- Max. peak pulse power: P_{PPM} = 260 W
- Low clamping voltage: V_{CL} = 8 V
- Low leakage current: I_{RM} = 8 nA
- ESD protection up to 30 kV (IEC 61000-4-2)
- IEC 61000-4-5 (surge); I_{PPM} = 20 A
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Application information

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- 10/100/1000 Mbit/s Ethernet
- Communication systems
- Portable electronics
- SIM card protection
- High-speed data lines

4. Quick reference data

Table 1. Quie	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3.3	V
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1] [2]	-	-	20	A
V _{CL}	clamping voltage	I _{PPM} = 20 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1] [2]	-	-	18	V

[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC 61000-4-5.

Measured from pin 1 to pin 2. [2]

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		
2	A	anode	1 2 SC-79 (SOD523)	К А 006ааа152

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PESD5Z3.3-Q	SC-79	plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body	SOD523			

7. Marking

Table 4. Marking codes				
Type number	Marking code			
PESD5Z3.3-Q	N8			

8. Limiting values

Table 5. Limiting values

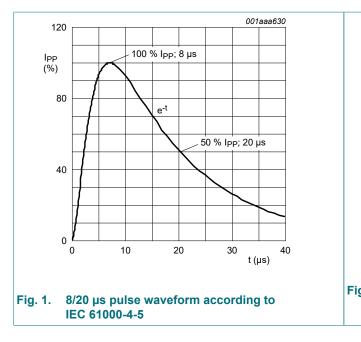
In accordance with the Absolute Maximum Rating System (IEC 60134).

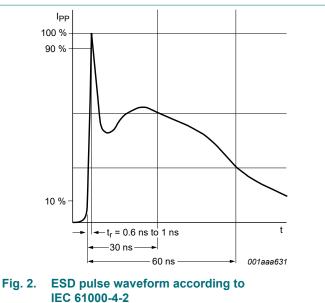
Symbol	Parameter	Conditions		Min	Мах	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	260	W
I _{PPM}	rated peak pulse current		[1] [2]	-	20	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximu	m ratings					
V _{ESD}	electrostatic discharge	IEC 61000-4-2; contact discharge	[3] [2]	-	30	kV
	voltage	MIL-STD-883; human body model (HBM)		-	10	kV
		machine model		-	400	V

[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC 61000-4-5.

[2] Measured from pin 1 to pin 2.

[3] Device stressed with ten non-repetitive ESD pulses.



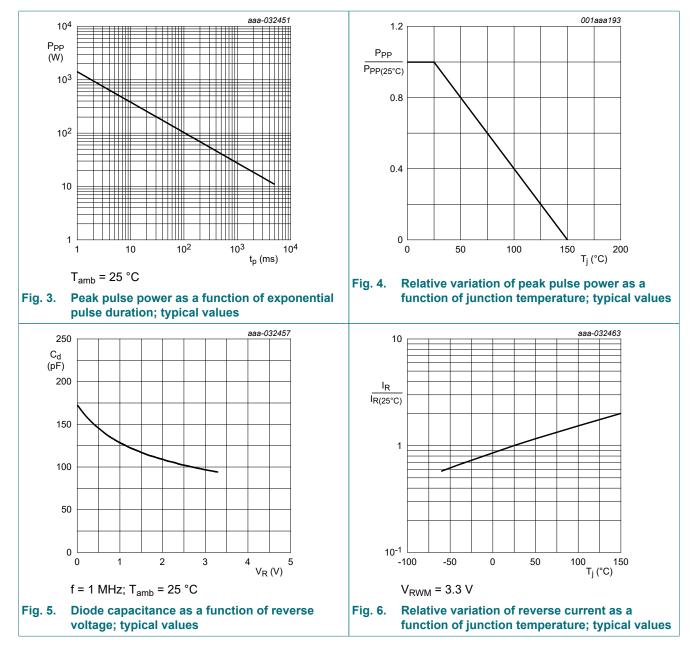


9. Characteristics

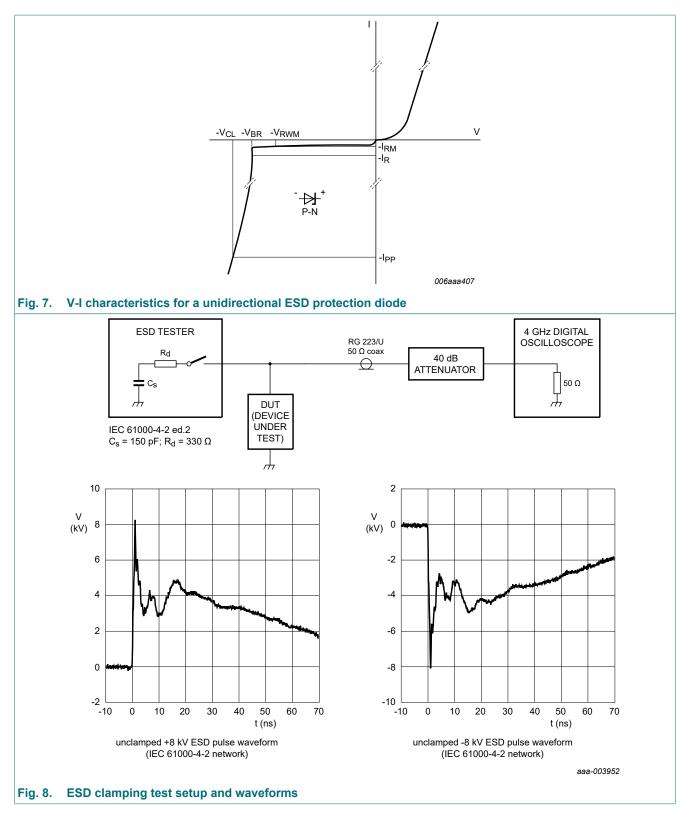
Table 6. Cha	aracteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3.3	V
V _{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		5	-	-	V
I _{RM}	reverse leakage current	V _{RWM} = 3.3 V; T _{amb} = 25 °C		-	8	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	172	200	pF
V _{CL}	clamping voltage	I _{PP} = 5 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1] [2]	-	8	10	V
		I _{PPM} = 20 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1] [2]	-	-	18	V
R _{diff}	differential resistance	I _R = 5 mA; T _{amb} = 25 °C		-	-	10	Ω

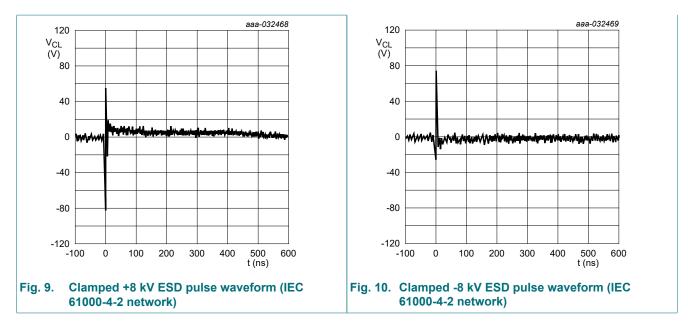
[1] Non-repetitive current pulse 8/20 µs exponentially decaying waveform according to IEC 61000-4-5.

[2] Measured from pin 1 to pin 2.



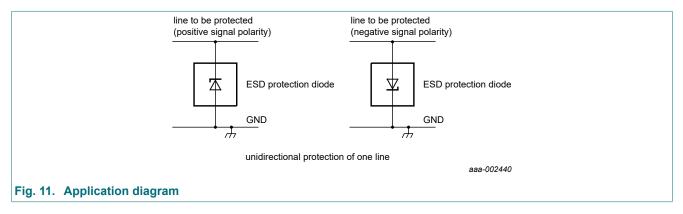
PESD5Z3_3-Q





10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

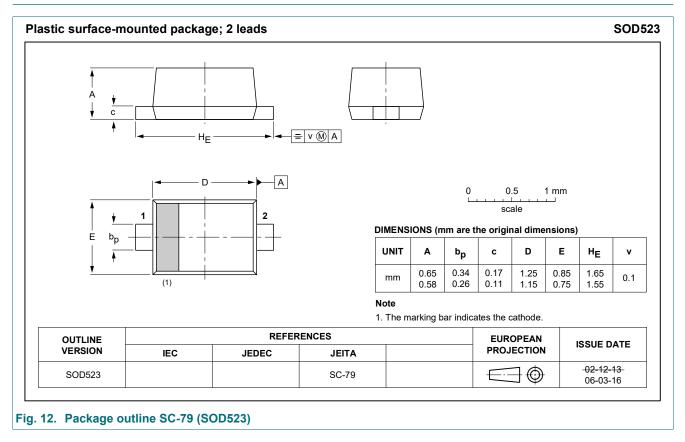
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

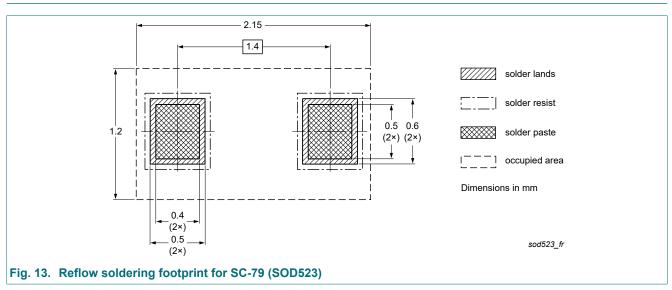
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 7. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD5Z3_3-Q v.1	20221125	Product data sheet	-	-		

PESD5Z3_3-Q

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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