

# **PLVA6xxA-Q** series

Low-voltage avalanche regulator diodes

Rev. 1 — 12 May 2022

**Product data sheet** 

### 1. General description

High performance voltage regulator diodes in a small SOT23 (TO-236AB), Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- · Very low dynamic impedance at low currents: approximately 5 % of conventional series
- Hard breakdown knee
- Low noise: approximately 10 % of conventional series
- Total power dissipation: max. 250 mW
- Small tolerances of V<sub>Z</sub>
- Working voltage range: nominal 5.00 to 6.80 V
- Non-repetitive peak reverse power dissipation: maximal 30 W at 150 °C
- Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- Low current, low power, low noise applications
- CMOS RAM back-up circuits
- Voltage stabilizers
- Voltage limiters
- Smoke detector relays



## 4. Quick reference data

#### Table 1. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
V <sub>n</sub>	noise voltage density	f = 1 kHz; B = 1 kHz; I <sub>Z</sub> = 250 μA	-	-	1.0	<u>μ</u> ν √Hz		
ΔV <sub>Z</sub>	line regulation		1					
	PLVA659A-Q to PLVA668A-Q	I <sub>LO</sub> = 10 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA656A-Q	I <sub>LO</sub> = 50 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA650A-Q	I <sub>LO</sub> = 100 μA; I <sub>HI</sub> = 1 mA	-	-	0.4	V		
PLVA653A-Q		I <sub>LO</sub> = 100 μA; I <sub>HI</sub> = 1 mA	-	-	0.2	V		
R <sub>Z</sub>	dynamic resistance	·				_		
	PLVA650A-Q	1 kHz superimposed;	-	-	700	Ω		
	PLVA653A-Q	I <sub>ZAC</sub> is 10 % of I <sub>ZDC</sub> I <sub>Z</sub> = 250 μΑ	-	-	250	Ω		
	PLVA656A-Q to PLVA668A-Q	- 12 - 200 μπ	-	-	100	Ω		
I <sub>R</sub>	reverse current							
	PLVA650A-Q	V <sub>R</sub> = 50 % V <sub>Z</sub> nominal	-	34	-	nA		
	PLVA653A-Q	_	-	22	-	nA		
	PLVA656A-Q	_	-	1.1	-	nA		
	PLVA659A-Q		-	0.9	-	nA		
	PLVA662A-Q		-	0.9	-	nA		
	PLVA665A-Q	-	-	0.9	-	nA		
	PLVA668A-Q	1	-	0.8	-	nA		

### 5. Pinning information

ng information			
Symbol	Descrition	Simlified outline	Graphic symbol
A	anode	3	K
n.c.	not connected		
К	cathode		aaa-006592
			aaa-000592
		1 2	
	Symbol       A       n.c.	Symbol         Descrition           A         anode           n.c.         not connected	Symbol     Descrition     Simlified outline       A     anode     3       n.c.     not connected     3       K     cathode

## 6. Ordering information

Type number	Package	ackage					
	Name	Description	Version				
PLVA650A-Q	TO-236AB	plastic surface-mounted package; 3 leads	<u>SOT23</u>				
PLVA653A-Q							
PLVA656A-Q							
PLVA659A-Q							
PLVA662A-Q							
PLVA665A-Q							
PLVA668A-Q							

### 7. Marking

Table 4. Marking codes		
Type number		Marking code
PLVA650A-Q	[1]	%9A
PLVA653A-Q	[1]	%9B
PLVA656A-Q	[1]	%9C
PLVA659A-Q	[1]	%9D
PLVA662A-Q	[1]	%9E
PLVA665A-Q	[1]	%9F
PLVA668A-Q	[1]	%9G

[1] % = placeholder for manufacturing site code

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
l <sub>F</sub>	continuous forward current			-	250	mA
I <sub>ZRM</sub>	repetitive peak working current	t <sub>p</sub> = 100 μs; δ = 10 %		-	250	mA
P <sub>ZSM</sub>	non-repetitive peak reverse power dissipation	t <sub>p</sub> = 100 μs; T <sub>j</sub> = 150 °C		-	30	W
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	330	K/W

[1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.

## **10. Characteristics**

#### **Table 7. Characteristics**

 $T_i = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit			
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 10 mA	-	-	0.9	V			
Vz	working voltage								
	PLVA650A-Q		4.80	5.00	5.20	V			
	PLVA653A-Q		5.10	5.30	5.50	V			
	PLVA656A-Q		5.40	5.60	5.80	V			
	PLVA659A-Q	I <sub>Z</sub> = 250 μA	5.70	5.90	6.10	V			
	PLVA662A-Q		6.00	6.20	6.40	V			
	PLVA665A-Q		6.30	6.50	6.70	V			
	PLVA668A-Q		6.60	6.80	7.00	V			
Vz	working voltage								
	PLVA650A-Q		-	4.30	-	V			
	PLVA653A-Q		-	5.20	-	V			
	PLVA656A-Q		-	5.51	-	V			
	PLVA659A-Q	I <sub>Z</sub> = 10 μA	-	5.85	-	V			
	PLVA662A-Q		-	6.19	-	V			
	PLVA665A-Q		-	6.49	-	V			
	PLVA668A-Q		-	6.80	-	V			
Rz	dynamic resistance								
	PLVA650A-Q		-	-	700	Ω			
	PLVA653A-Q	1 kHz superimposed; I <sub>ZAC</sub> is 10 % of I <sub>ZDC</sub> ;	-	-	250	Ω			
	PLVA656A-Q to PLVA668A-Q	$I_Z = 250 \ \mu A$	-	-	100	Ω			
SZ	temperature coefficient								
	PLVA650A-Q		-	0.20	-	mv/K			
	PLVA653A-Q		-	1.60	-	mv/K			
	PLVA656A-Q		-	1.90	-	mv/K			
	PLVA659A-Q	I <sub>Z</sub> = 250 μA	-	2.40	-	mv/K			
	PLVA662A-Q		-	2.65	-	mv/K			
	PLVA665A-Q		-	2.90	-	mv/K			
	PLVA668A-Q		-	3.40	-	mv/K			

## **PLVA6xxA-Q series**

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
I <sub>R</sub>	reverse current			I	_1			
	PLVA650A-Q		-	-	20000	nA		
	PLVA653A-Q	_	-	-	5000	nA		
	PLVA656A-Q	_	-	-	1000	nA		
	PLVA659A-Q	V <sub>R</sub> = 80 % V <sub>Z</sub> nominal	-	-	500	nA		
	PLVA662A-Q		-	-	100	nA		
	PLVA665A-Q		-		50	nA		
	PLVA668A-Q		-		10	nA		
R	reverse current							
	PLVA650A-Q		-	34	-	nA		
	PLVA653A-Q		-	22	-	nA		
	PLVA656A-Q	1	-	1.1	-	nA		
	PLVA659A-Q	V <sub>R</sub> = 50 % V <sub>Z</sub> nominal	-	0.9	-	nA		
	PLVA662A-Q	_	-	0.9	-	nA		
	PLVA665A-Q	_	-	0.9	-	nA		
	PLVA668A-Q		-	0.8	-	nA		
I <sub>R</sub>	reverse current							
	PLVA650A-Q		-	21	-	μA		
	PLVA653A-Q		-	3.5	-	μA		
	PLVA656A-Q		-	1.3	-	μA		
	PLVA659A-Q	V <sub>R</sub> = 90 % V <sub>Z</sub> nominal	-	1.0	-	μA		
	PLVA662A-Q		-	0.05	-	μA		
	PLVA665A-Q		-	0.04	-	μA		
	PLVA668A-Q	_	-	0.006	-	μA		
ΔV <sub>Z</sub>	line regulation	- L	I					
	PLVA650A-Q to PLVA668A-Q	I <sub>LO</sub> = 10 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA656A-Q	I <sub>LO</sub> = 50 μA; I <sub>HI</sub> = 1 mA	-	-	0.1	V		
	PLVA650A-Q	I <sub>LO</sub> = 100 μA; I <sub>HI</sub> = 1 mA	-	-	0.4	V		
	PLVA653A-Q	I <sub>LO</sub> = 100 μA; I <sub>HI</sub> = 1 mA	-	-	0.2	V		
V <sub>n</sub>	noise voltage density	f = 1 kHz; B = 1 kHz; I <sub>Z</sub> = 250 μA	-	-	1.0	<u>μν</u> √Η:		

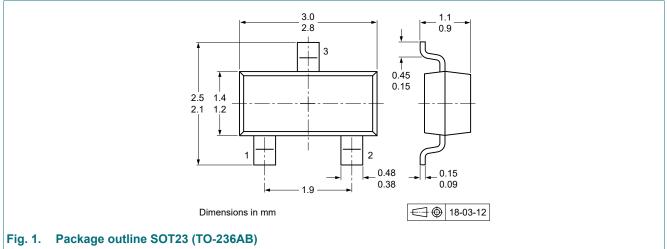
## **11. Test information**

#### **11.1. Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

#### 12. Package outline

#### Table 8. Package outline

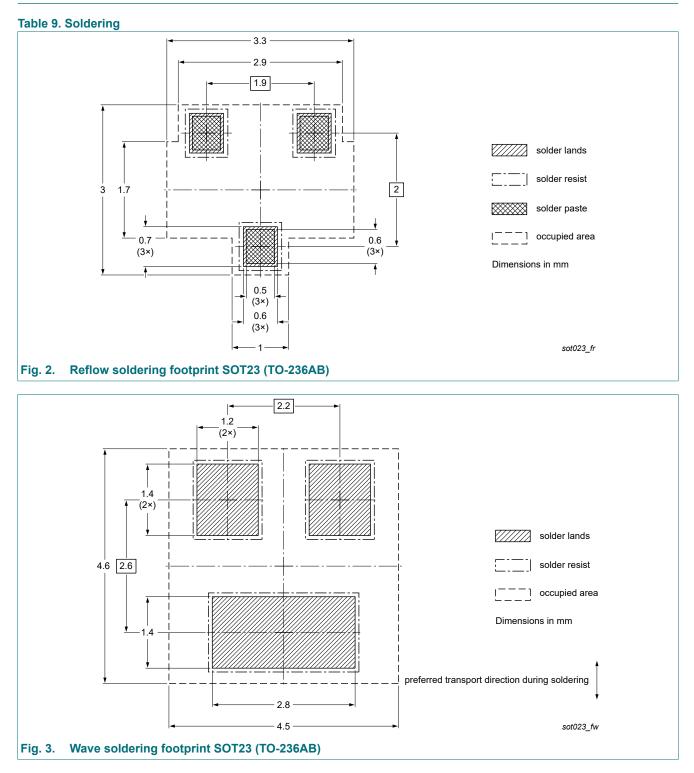


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## 13. Soldering



## 14. Revision history

Table 10. Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
PLVA6XXA-Q v.1	20220512	Product data sheet	-	-

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## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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