

20 V, 1 A low Vf Schottky barrier rectifier 16 November 2022

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD123W small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 1 A
- Reverse voltage: V_R ≤ 20 V
- Low forward voltage
- High power capability due to clip-bond technology
- Small and flat lead SMD plastic package
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} ≤ [1] 125 °C	-	-	1	A
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 140 °C	-	-	1	A
V _R	reverse voltage	T _j = 25 °C	-	-	20	V
V _F	forward voltage	I _F = 1 A; T _j = 25 °C	-	395	450	mV
I _R	reverse current	V _R = 20 V; T _i = 25 °C	-	8	50	μA

[1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]	1	K- F A-A
2	A	anode	ے ان کے ان کے ان کے ان کے ان کے ان کے کر کے کر کے کر کے کر	sym001

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMEG2010BER-Q	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	<u>SOD123W</u>			

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG2010BER-Q	В6

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	20	V
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} ≤ 125 °C	[1]	-	1	A
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 140 °C		-	1	A
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; half sine wave; $T_{j(init)}$ = 25 °C		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	0.57	W
			[3]	-	0.95	W
			[1]	-	1.8	W
Tj	junction temperature			-	150	°C
Г _{атb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	220	K/W
			[1] [3]	-	-	130	K/W
			[1] [4]	-	-	70	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	18	K/W

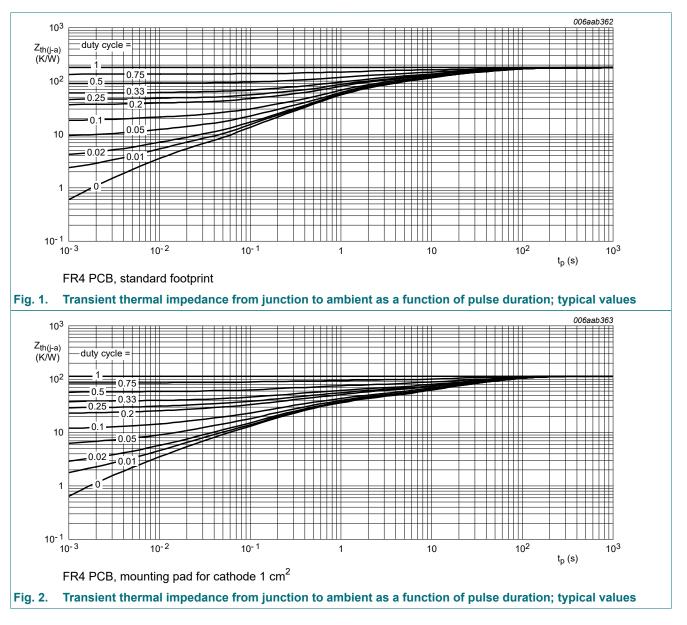
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

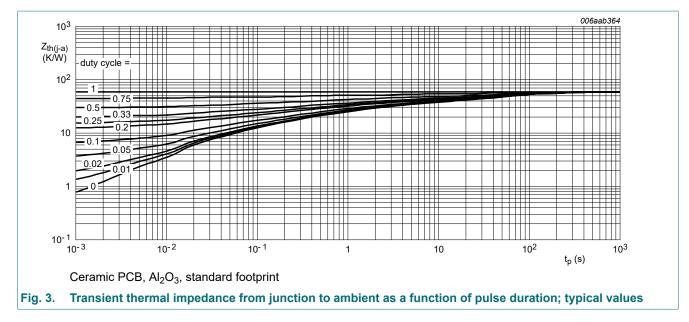
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

[5] Soldering point of cathode tab.



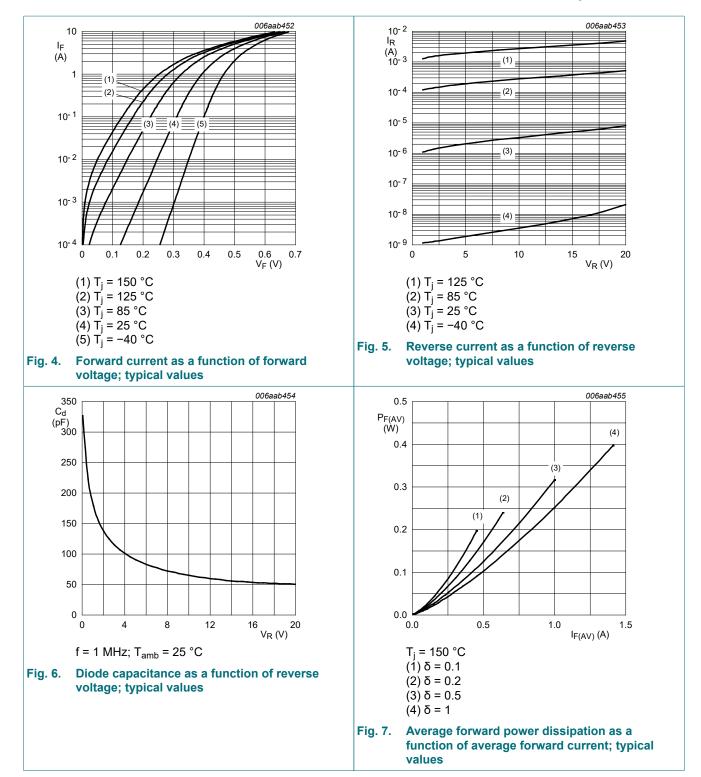
20 V, 1 A low Vf Schottky barrier rectifier



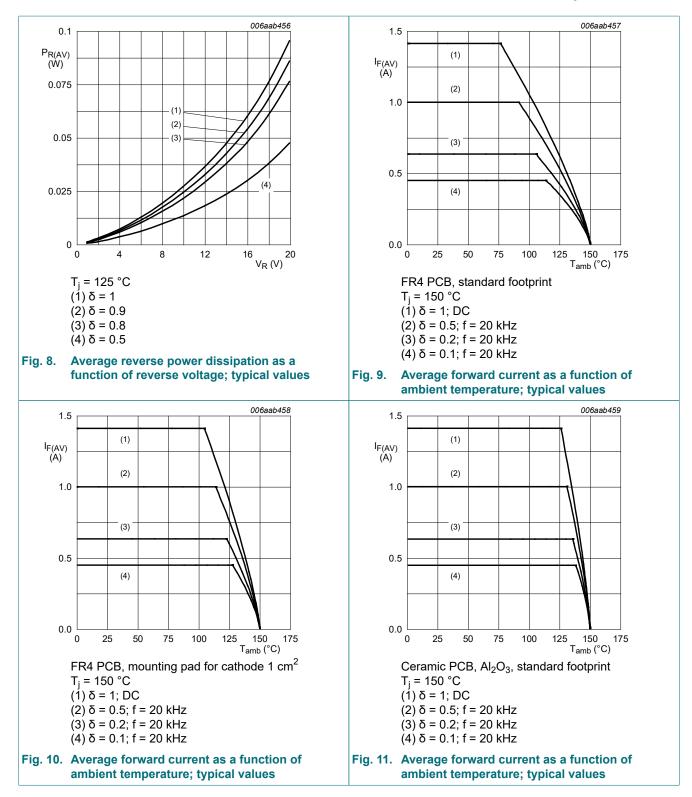
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I _F = 0.1 A; T _j = 25 °C	-	310	340	mV
		I _F = 0.7 A; T _j = 25 °C	-	380	430	mV
		I _F = 1 A; T _j = 25 °C	-	395	450	mV
I _R	reverse current	V _R = 5 V; T _j = 25 °C	-	2	-	μA
		V _R = 10 V; T _j = 25 °C	-	3	-	μA
		V _R = 20 V; T _j = 25 °C	-	8	50	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	185	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	65	-	pF

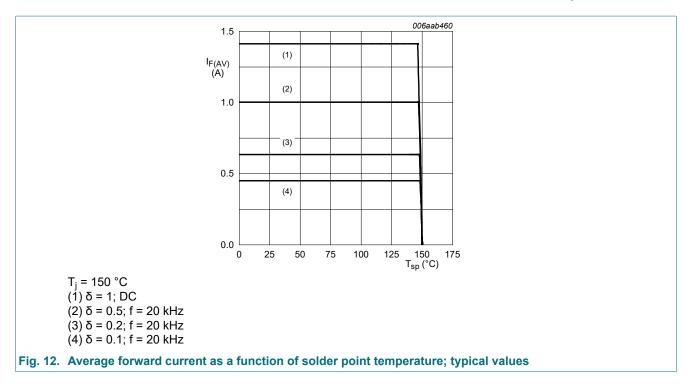
20 V, 1 A low Vf Schottky barrier rectifier



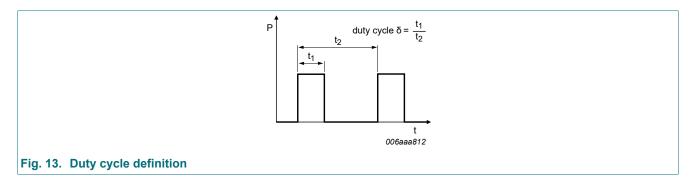
20 V, 1 A low Vf Schottky barrier rectifier



20 V, 1 A low Vf Schottky barrier rectifier



11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

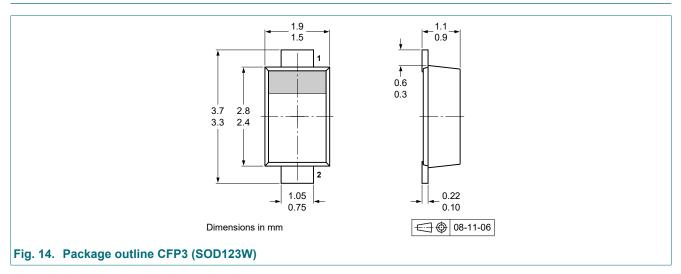
I_{RMS}=I_{F(AV)} at DC

 $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

Quality information

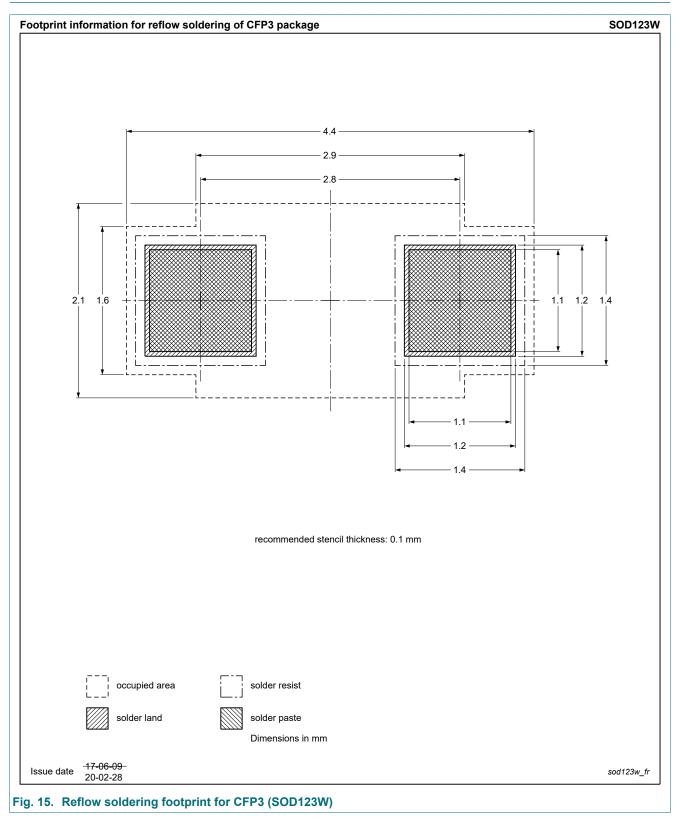
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

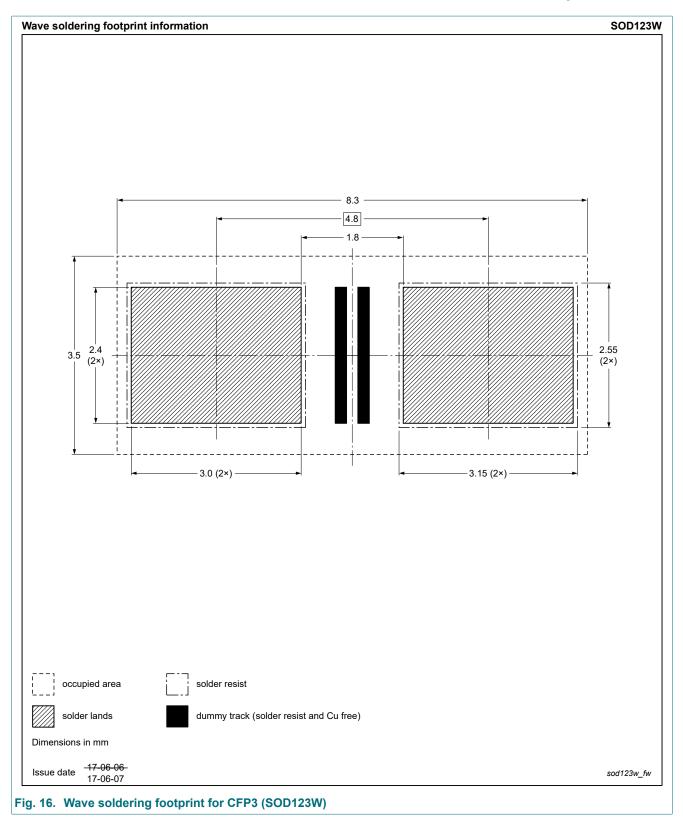


20 V, 1 A low Vf Schottky barrier rectifier

13. Soldering



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14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG2010BER-Q v.2	20221116	Product data sheet	-	PMEG2010BER-Q v.1			
Modifications:	 Limiting values: Measurement conditions for I_{FSM} changed from square wave to half-sine wave. 						
PMEG2010BER-Q v.1	20210608	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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Contents

1.	General description	.1
2.	Features and benefits	. 1
3.	Applications	. 1
4.	Quick reference data	.1
5.	Pinning information	.2
6.	Ordering information	.2
7.	Marking	. 2
8.	Limiting values	2
9.	Thermal characteristics	3
10.	Characteristics	.4
11.	Test information	.7
12.	Package outline	. 8
13.	Soldering	9
14.	Revision history	11
	Legal information	

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