

PMEG3005ELS-Q

30 V, 0.5 A very low VF Schottky barrier rectifier

21 April 2022

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in an ultra small SOD882BD (DFN1006BD-2) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Forward current: I_F ≤ 0.5 A
- Reverse voltage: V_R ≤ 30 V
- Ultra small SMD plastic package
- Very low forward voltage
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I _F	forward current	T _{sp} ≤ 55 °C	-	-	0.5	А
V _R	reverse voltage		-	-	30	V
V _F	forward voltage	I_F = 0.5 A; t _p ≤ 300 μs; δ ≤ 0.02; pulsed; T _{amb} = 25 °C	-	430	500	mV
I _R	reverse current	V _R = 30 V; T _{amb} = 25 °C	-	70	500	μA

5. Pinning information

Table 2	. Pinning info	ormation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]		
2	A	anode		К-ҚА
			Transparent top view	sym001
			DFN1006BD-2 (SOD882BD)	

[1] The marking bar indicates the cathode

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6. Ordering information

Table 3. Ordering information Type number	Package	ackage				
	Name	Description	Version			
PMEG3005ELS-Q	DFN1006BD-2	Leadless ultra small plastic package with side-wettable flanks (SWF); 2 terminals; 0.65 mm pitch; 1 mm x 0.6 mm x 0.47 mm body	SOD882BD			

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG3005ELS-Q	9A

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage			-	30	V
l _F	forward current	T _{sp} ≤ 55 °C		-	0.5	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	1	A
I _{FSM}	non-repetitive peak forward current	t _p = 8 ms; square wave		-	3	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	335	mW
			[2]	-	610	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), 70 µm single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, 70 µm single-sided copper, tin-plated mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	375	K/W
	junction to ambient		[3]	-	-	205	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, 70 µm single-sided copper, tin-plated and standard footprint.

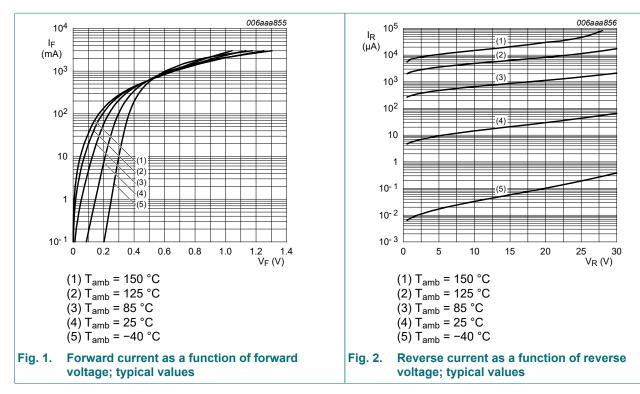
[3] Device mounted on an FR4 PCB, 70 µm single-sided copper, tin-plated mounting pad for cathode 1 cm².

10. Characteristics

Table 7. Characteristics

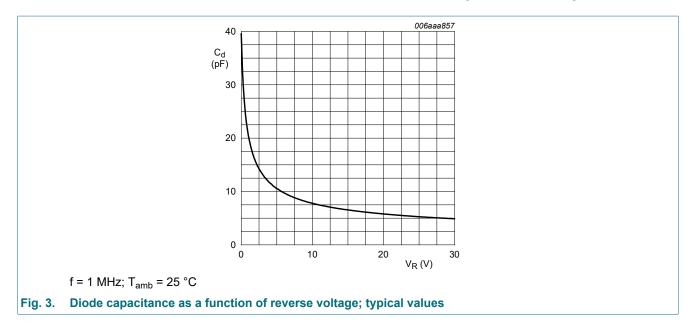
 T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _F	forward voltage	I_F = 0.1 mA; t _p ≤ 300 μs; δ ≤ 0.02; pulsed; T _{amb} = 25 °C	-	90	180	mV
		I _F = 1 mA; t _p ≤ 300 μs; δ ≤ 0.02; pulsed; T _{amb} = 25 °C	-	150	200	mV
		I _F = 10 mA; t _p ≤ 300 μs; δ ≤ 0.02; pulsed; T _{amb} = 25 °C	-	210	270	mV
		I_F = 0.1 A; t _p ≤ 300 μs; δ ≤ 0.02; pulsed; T _{amb} = 25 °C	-	295	360	mV
		$ I_{\text{F}} = 0.5 \text{ A}; t_{\text{p}} \le 300 \mu\text{s}; \delta \le 0.02; $ pulsed; $T_{\text{amb}} = 25 ^{\circ}\text{C} $	-	430	500	mV
I _R	reverse current	V _R = 10 V; T _{amb} = 25 °C	-	15	200	μA
		V _R = 30 V; T _{amb} = 25 °C	-	70	500	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz	-	24	30	pF
t _{rr}	reverse recovery time ramp recovery	$ dI_F/dt = 125 \; A/\mu s; \; I_F = 0.5 \; A; \; V_R = 26 \; V; \\ T_{amb} = 25 \; ^\circ C $	-	-	5	ns

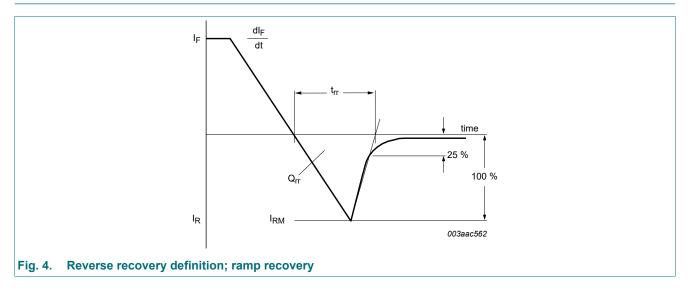


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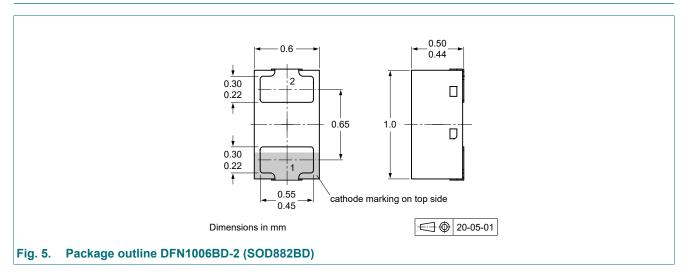
11. Test information



Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

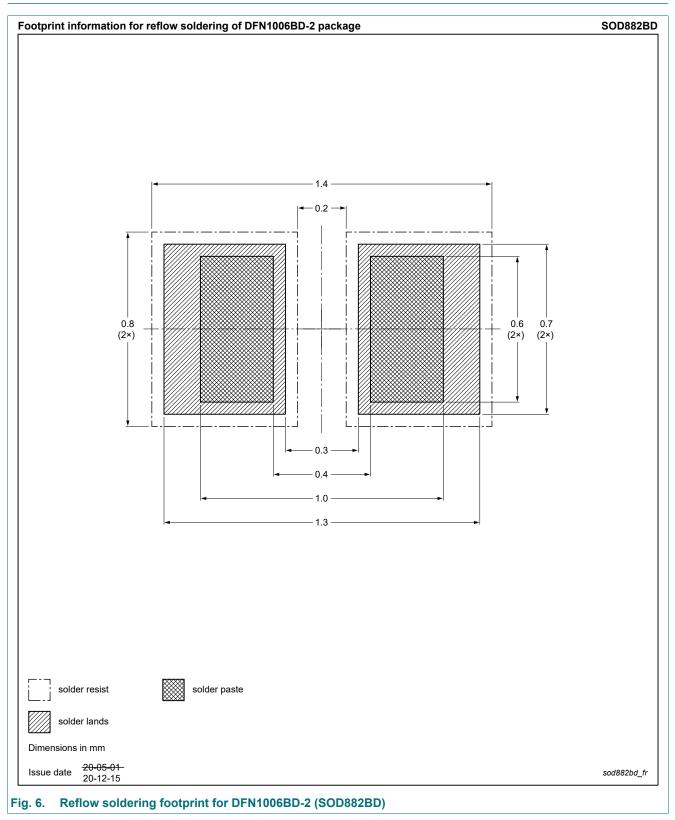


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13. Soldering



14. Revision history

Table 8. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3005ELS-Q v.1	20220421	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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