

PMEG3015EH

30 V, 1.5 A ultra low VF Schottky barrier rectifier

26 June 2023

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in small and flat lead SOD123F SMD plastic package.

2. Features and benefits

- Forward current: ≤ 1.5 A
- Reverse voltage: ≤ 30 V
- Ultra low forward voltage
- Small and flat lead SMD plastic package
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _R	reverse voltage			-	-	30	V
I _F	forward current	T _{sp} ≤ 55 °C		-	-	1.5	А
V _F	forward voltage	I _F = 1.5 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C		-	440	550	mV
I _R	reverse current	V _R = 30 V; T _{amb} = 25 °C		-	400	1000	μA

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]		K 🔣 A
2	A	anode		sym001
			SOD123F	5,

[1] The marking bar indicates the cathode.



6. Ordering information

Table 3. Ordering information						
Type number Package						
	Name	Description	Version			
PMEG3015EH		plastic, surface-mounted package; 2 leads; 2.6 mm x 1.6 mm x 1.1 mm body	SOD123F			

7. Marking

Table 4. Marking codes					
Type number	Marking code				
PMEG3015EH	AE				

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage			-	30	V
I _F	forward current	T _{sp} ≤ 55 °C		-	1.5	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	4.5	A
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; square wave; $T_{j(init)}$ = 25 °C		-	9	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	375	mW
			[2]	-	830	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	330	K/W
	junction to ambient		[1] [3]	-	-	150	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	60	K/W

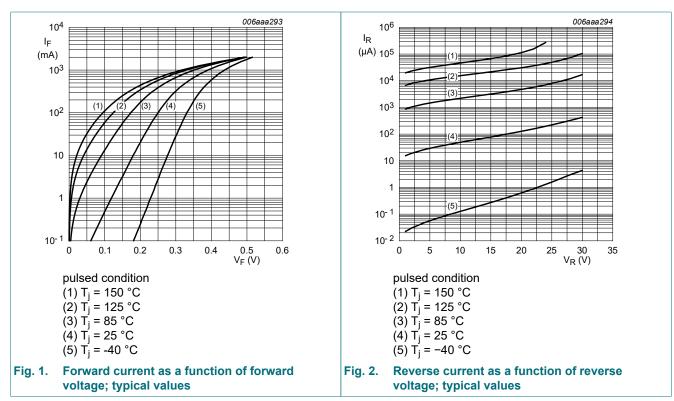
[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

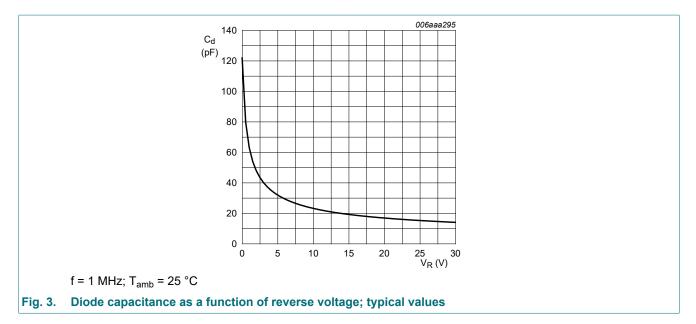
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

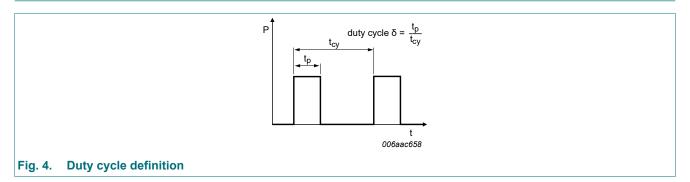
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F forward	forward voltage	I_F = 1 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	125	160	mV
		I _F = 10 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	185	220	mV
		I _F = 100 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	255	290	mV
		I _F = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	330	380	mV
		I_F = 1 A; pulsed; $t_p \le 300 \ \mu s$; δ ≤ 0.02 ; T_{amb} = 25 °C	-	400	480	mV
		I_F = 1.5 A; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T _{amb} = 25 °C	-	440	550	mV
I _R	reverse current	V _R = 10 V; T _{amb} = 25 °C	-	60	150	μA
		V _R = 30 V; T _{amb} = 25 °C	-	400	1000	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _{amb} = 25 °C	-	60	72	pF





11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

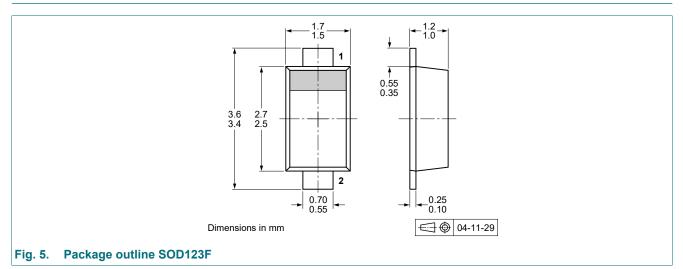
 $I_{RMS} = I_{F(AV)}$ at DC

 $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

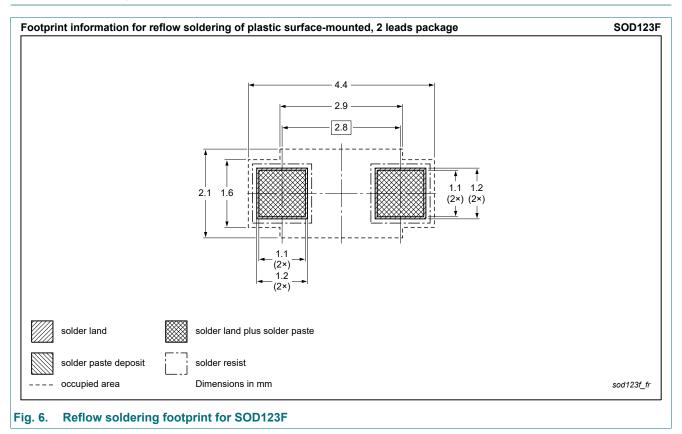
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG3015EH v.4	20230626	Product data sheet	-	PMEG3015EH_EJ_3			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Family data sheet splitted to single type data sheets. Section "Packing information" removed. 						
PMEG3015EH_EJ_3	20100113	Product data sheet	-	PMEG3015EH_EJ_2			
PMEG3015EH_EJ_2	20050408	Product data sheet	-	PMEG3015EJ_1			
PMEG3015EJ_1	20050303	Product data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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