

PMEG4010EGW

40 V, 1 A low VF MEGA Schottky barrier rectifier

12 October 2023

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection encapsulated in small SOD123 Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Forward current: $I_F \le 1 A$
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage typ. $V_F = 540 \text{ mV}$
- Low reverse current typ. $I_R = 30 \ \mu A$
- Small SMD plastic package •

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion •
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
I _F	forward current	T _{sp} ≤ 55 °C		-	-	1	А
V _R	reverse voltage	T _j = 25 °C		-	-	40	V
V _F	forward voltage	$I_F = 1 \text{ A}; t_p \le 300 \text{ μs}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$		-	540	640	mV
I _R	reverse current	V_R = 40 V; pulsed; T _j = 25 °C	[1]	-	30	100	μA

[1] Very short test pulse to prevent junction self-heating.



5. Pinning information

Table 2.	Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	К	cathode[1]		K- K A				
2	A	anode	SOD123	sym001				

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMEG4010EGW	SOD123	plastic, surface-mounted package; 2 leads; 2.675 mm x 1.6 mm x 1.15 mm body	<u>SOD123</u>		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG4010EGW	G5

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
l _F	forward current	T _{sp} ≤ 55 °C		-	1	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} ≤ 60 °C	[1]	-	1	A
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 130 °C		-	1	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	7	A
I _{FSM}	non-repetitive peak forward current	t _p = 8 ms; square wave; T _{j(init)} = 25 °C		-	9	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	400	mW
			[1]	-	660	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	310	K/W
	junction to ambient		[1] [3]	-	-	190	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	29	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

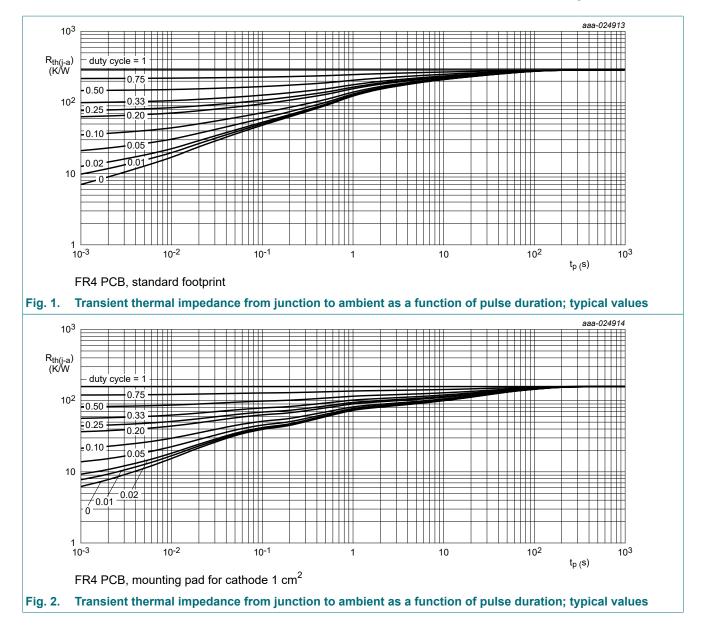
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

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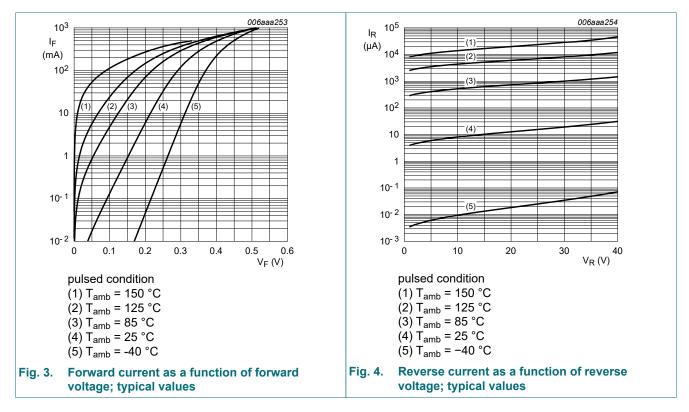
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10. Characteristics

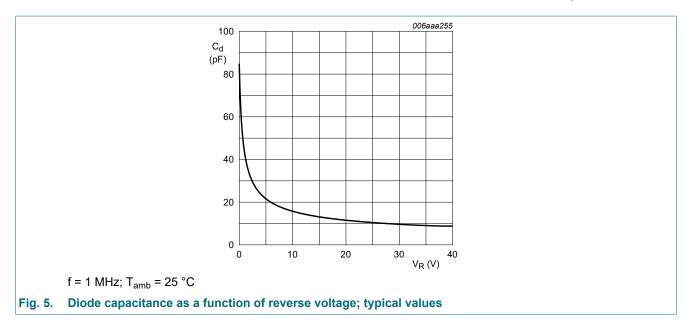
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	$I_R = 1 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$		40	-	-	V
VF	forward voltage	I_F = 0.1 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C		-	95	130	mV
		$I_F = 1 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 \text{ °C}$		-	155	210	mV
		I_F = 10 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C		-	220	270	mV
		I_F = 100 mA; t _p ≤ 300 μs; δ ≤ 0.02; T _j = 25 °C		-	295	350	mV
		$ \begin{array}{l} {\sf I_F} = 500 \mbox{ mA; } t_p \leq \ 300 \mu s; \delta \leq \ 0.02; \\ {\sf T}_j = 25 ^\circ {\sf C} \end{array} $		-	420	470	mV
		I_F = 1 A; $t_p \le 300 \ \mu$ s; δ ≤ 0.02 ; T _j = 25 °C		-	540	640	mV
R	reverse current	V_R = 10 V; pulsed; T _j = 25 °C	[1]	-	7	20	μA
		V_R = 40 V; pulsed; T_j = 25 °C	[1]	-	30	100	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _i = 25 °C		-	43	50	pF

[1] Very short test pulse to prevent junction self-heating.

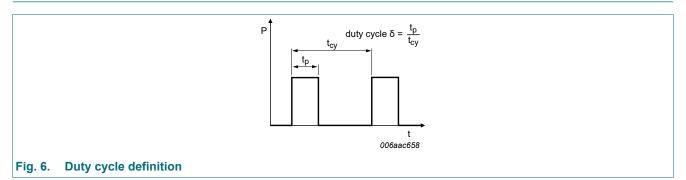


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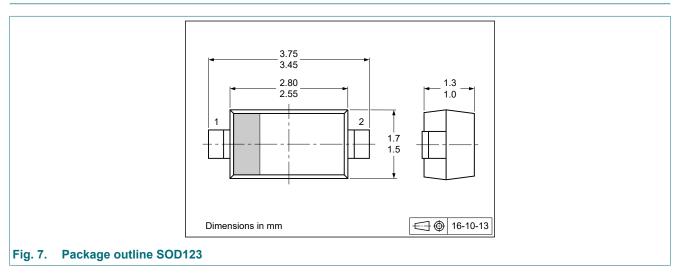


11. Test information



The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

12. Package outline



13. Soldering

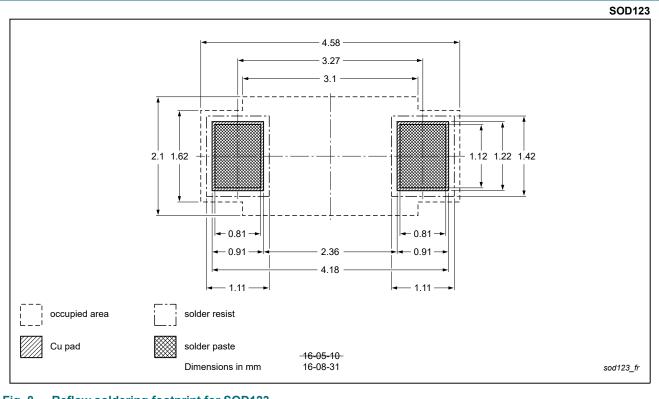
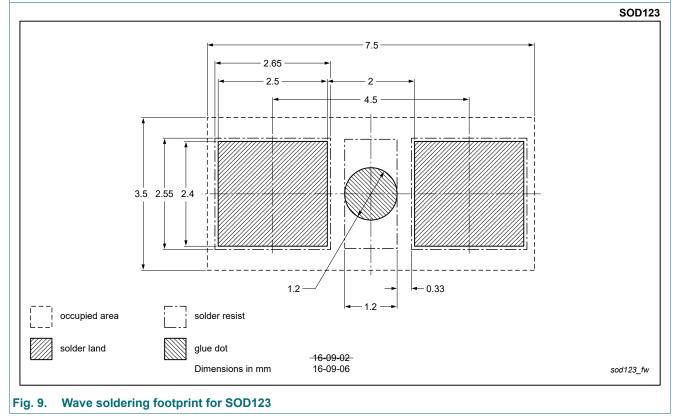


Fig. 8. Reflow soldering footprint for SOD123



14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG4010EGW v.2	20231012	Product data sheet	-	PMEG4010EGW v.1			
Modifications:	Product changed to	Product changed to non automotive. Please refer to the automotive product(s) with -Q.					
PMEG4010EGW v.1	20161207	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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