

PMEG60T10ELR-Q

60 V, 1 A low leakage current Trench MEGA Schottky barrier rectifier 1 April 2022

Product data sheet

nexperia

1. General description

Trench Maximum Efficiency General Application (MEGA) Schottky barrier rectifier encapsulated in a CFP3 (SOD123W) small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \le 1 A$
- Reverse voltage: $V_R \le 60 V$ •
- Low forward voltage •
- Low leakage current due to Trench MEGA Schottky technology •
- High power capability due to clip-bonding technology
- Small and flat lead SMD power plastic package
- Suitable for both reflow and wave soldering
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 167 °C		-	-	1	A
V _R	reverse voltage	T _j = 25 °C		-	-	60	V
V _F	forward voltage	I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	525	600	mV
I _R	reverse current	V _R = 10 V; pulsed; T _j = 25 °C	[1]	-	0.06	0.35	μA
		V _R = 60 V; pulsed; T _j = 25 °C	[1]	-	0.12	0.65	μA

[1] Very short pulse, in order to maintain a stable junction temperature.

5. Pinning information

Table 2. F	able 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol				
1	К	cathode	1 2	к . [{ А				
2	A	anode	CFP3 (SOD123W)	sym001				

6. Ordering information

Table 3. Ordering information	Package		
	Name	Description	Version
PMEG60T10ELR-Q	CFP3	plastic, surface mounted package; 2 terminals; 2.6 mm x 1.7 mm x 1 mm body	SOD123W

7. Marking

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Table 4. Marking codes	
Type number	Marking code
PMEG60T10ELR-Q	L6

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _R	reverse voltage	T _j = 25 °C		-	60	V
l _F	forward current	δ = 1; T _{sp} ≤ 165 °C		-	1.4	А
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 167 °C		-	1	A
I _{FSM}	non-repetitive peak forward current	t _p = 8 ms; square wave; T _{j(init)} = 25 °C		-	30	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.68	W
			[2]	-	1.15	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

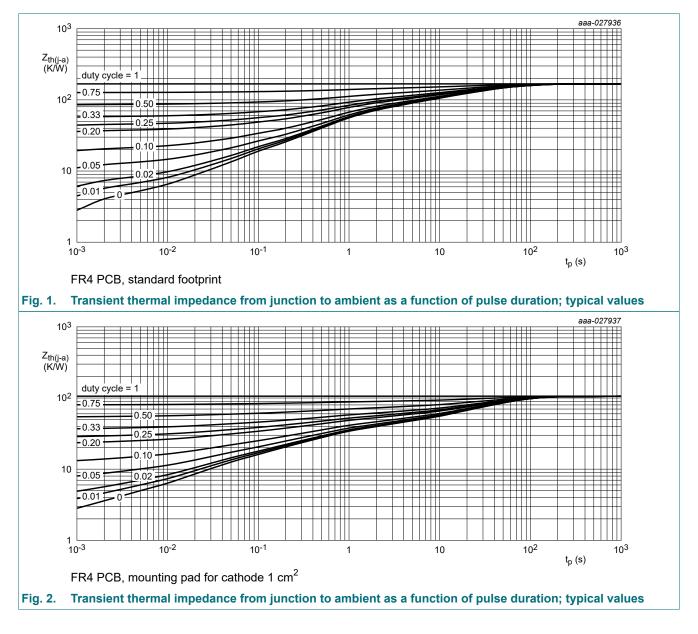
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	220	K/W
junc	junction to ambient		[1] [3]	-	-	130	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	18	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

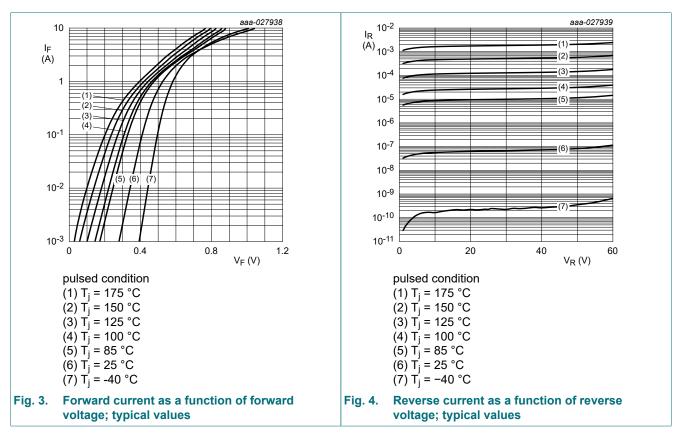
[4] Soldering point of cathode tab.



10. Characteristics

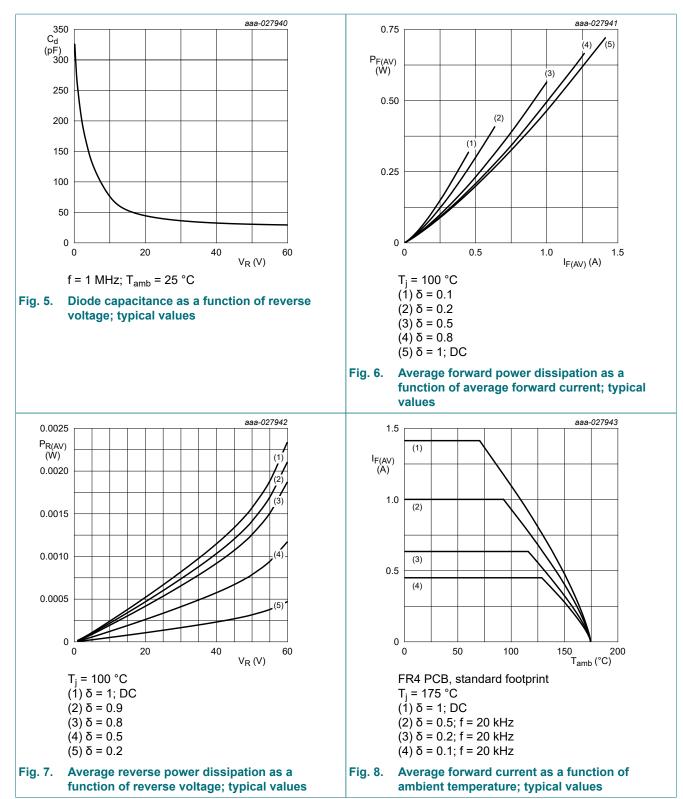
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I _R = 1 mA; pulsed; T _j = 25 °C	[1]	60	-	-	V
V _F	forward voltage	I _F = 0.1 A; pulsed; T _j = 25 °C	[1]	-	410	485	mV
		I _F = 0.5 A; pulsed; T _j = 25 °C	[1]	-	480	560	mV
		I _F = 1 A; pulsed; T _j = 25 °C	[1]	-	525	600	mV
		I _F = 1 A; pulsed; T _j = -40 °C	[1]	-	580	-	mV
		I _F = 1 A; pulsed; T _j = 125 °C	[1]	-	440	-	mV
I _R	reverse current	V _R = 10 V; pulsed; T _j = 25 °C	[1]	-	0.06	0.35	μA
		V _R = 40 V; pulsed; T _j = 25 °C	[1]	-	0.07	-	μA
		V _R = 60 V; pulsed; T _j = 25 °C	[1]	-	0.12	0.65	μA
		V _R = 60 V; pulsed; T _j = 125 °C	[1]	-	0.2	-	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C		-	245	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C		-	75	-	pF
t _{rr}	reverse recovery time step recovery	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_j = 25 ^{\circ}\text{C}$		-	7	-	ns
	reverse recovery time ramp recovery	dI _F /dt = 200 A/µs; I _F = 6 A; V _R = 26 V; T _j = 25 °C		-	13	-	ns
V _{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; \text{ d}_F/\text{d}t = 20 \text{ A}/\mu\text{s}; T_j = 25 \text{ °C}$		-	500	-	mV

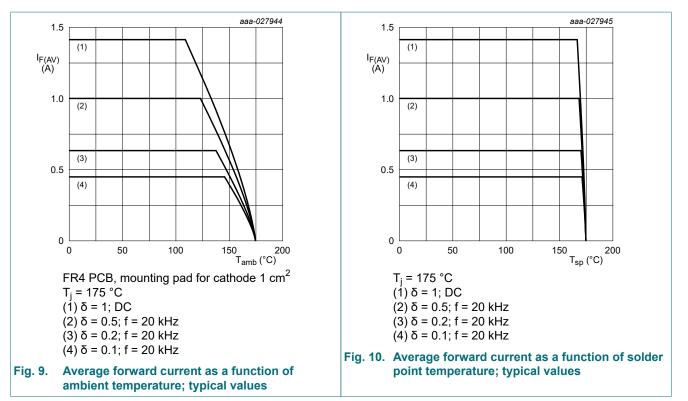
[1] Very short pulse, in order to maintain a stable junction temperature.



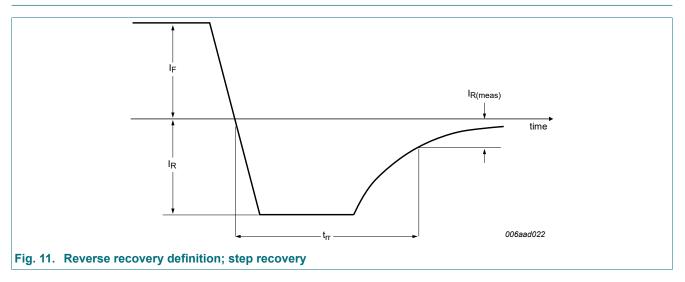
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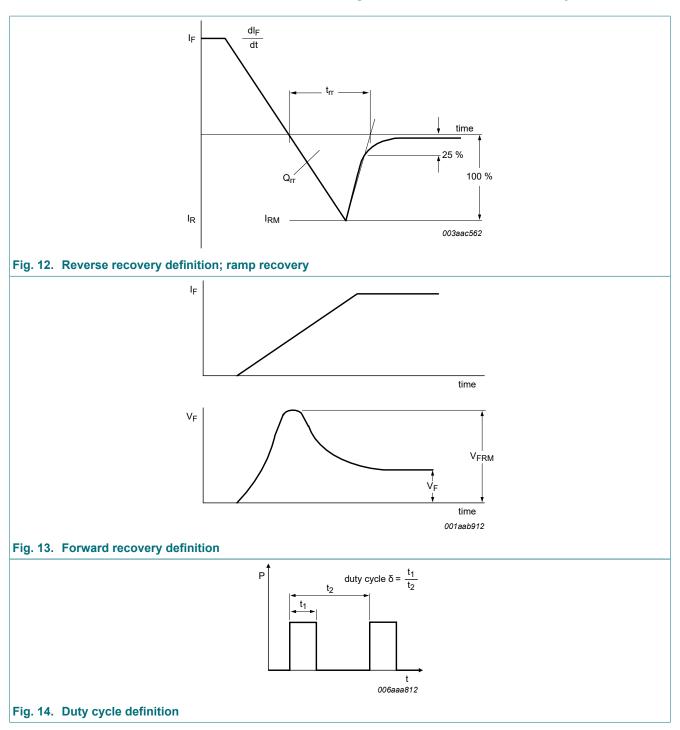
60 V, 1 A low leakage current Trench MEGA Schottky barrier rectifier





11. Test information





The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)}=I_M \times \delta$ with I_M defined as peak current

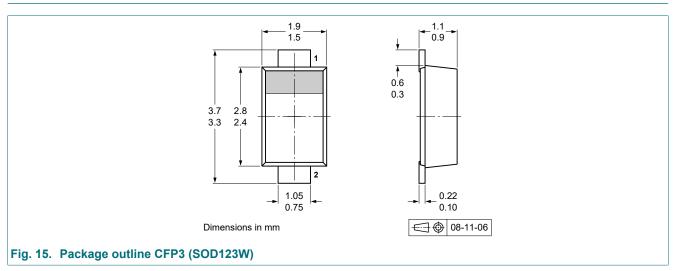
 $I_{RMS}=I_{F(AV)}$ at DC, and $I_{RMS}=I_M \times \sqrt{\delta}$

with I_{RMS} defined as RMS current.

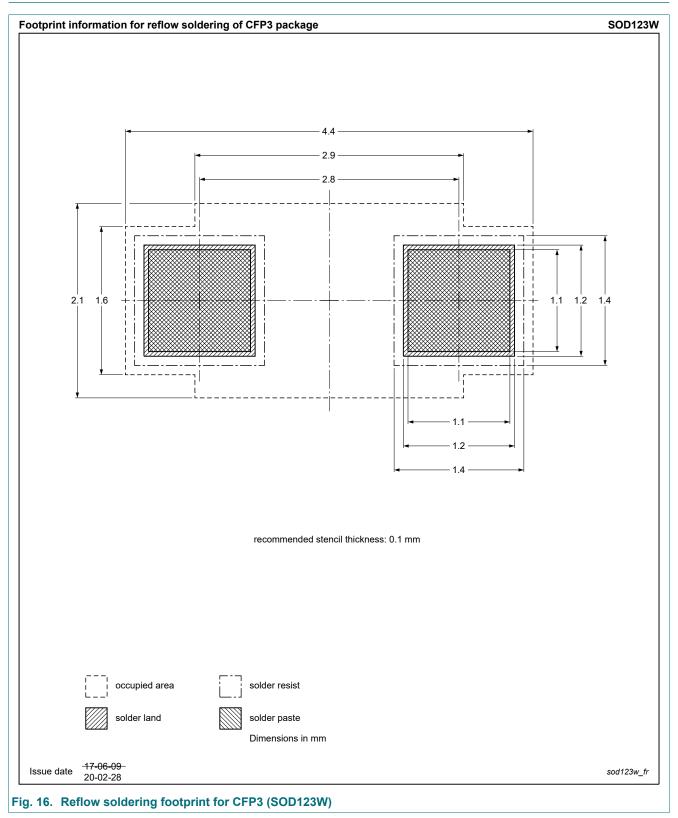
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

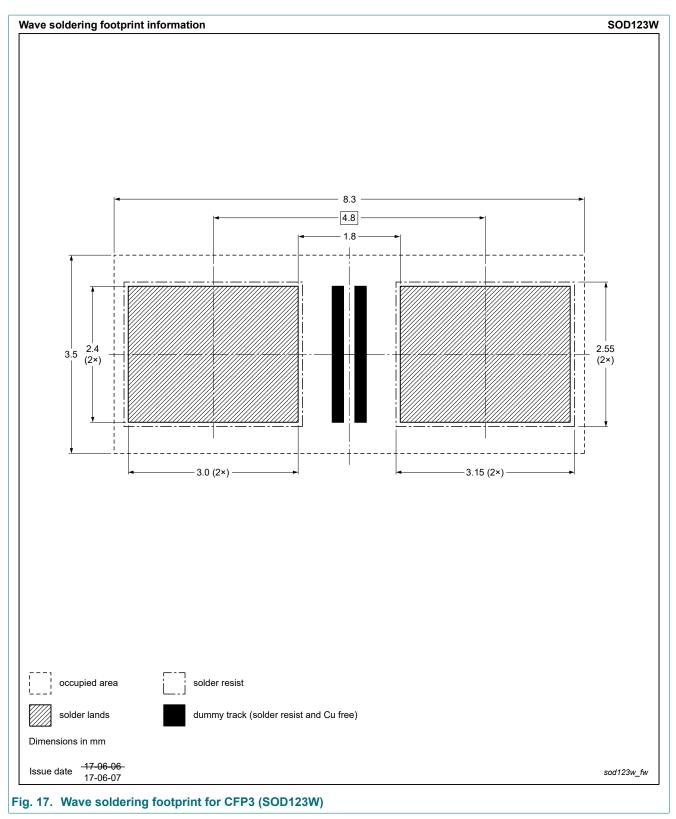


13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG60T10ELR-Q v.1	20220401	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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