

60 V, N-channel Trench MOSFET

29 November 2021

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- Extended temperature range T_i = 175 °C
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM (class H2)

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	60	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	3.6	A
Static characteristics							
R _{DSon}	drain-source on-state resistance	V _{GS} = 10 V; I _D = 3.6 A; T _j = 25 °C		-	46	60	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².



5. Pinning information

Table 2.	Pinning info	rmation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain		D
2	D	drain	□6 □5 □4	
3	G	gate		_G , ↓ G , 本 \
4	S	source	o	
5	D	drain		
6	D	drain	SC-74; TSOP6 (SOT457)	s
				017aaa255

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PMN37ENE	SC-74; TSOP6	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457		

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMN37ENE	4L

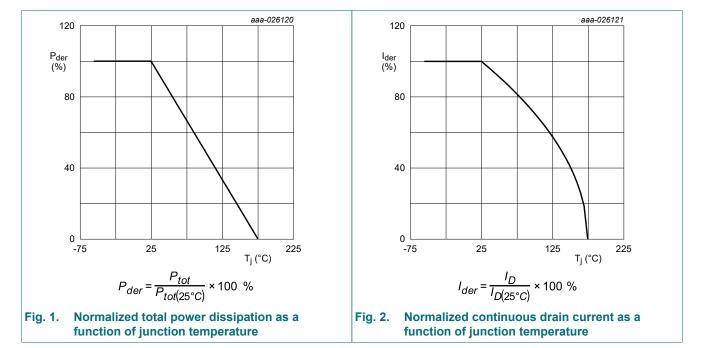
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

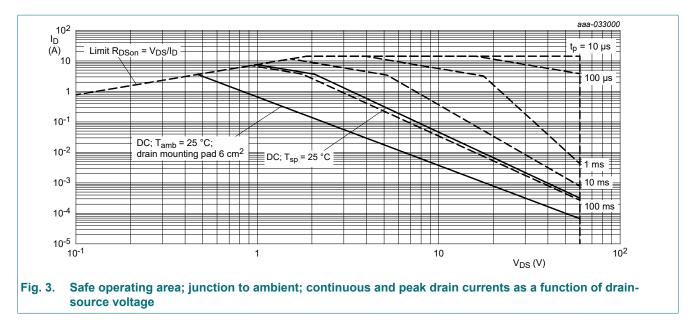
Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	60	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	3.6	А
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	2.5	A
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	14	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	667	mW
			[1]	-	1.7	W
		T _{sp} = 25 °C		-	7.5	W
Tj	junction temperature			-55	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C
Source Drai	n Diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	1.7	А

Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



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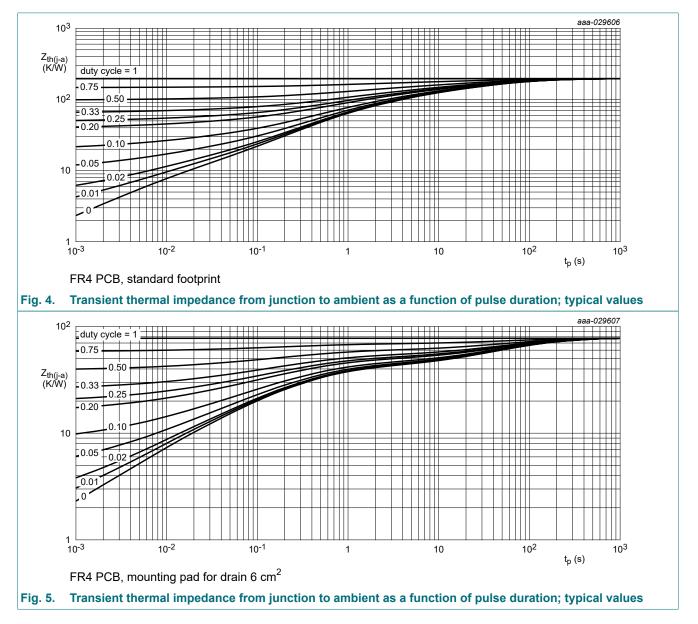
PMN37ENE

9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistance from junction to ambient	in free air	[1]	-	195	225	K/W	
		[2]	-	78	90	K/W	
R _{th(j-sp)}	thermal resistance from junction to solder point			-	15	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

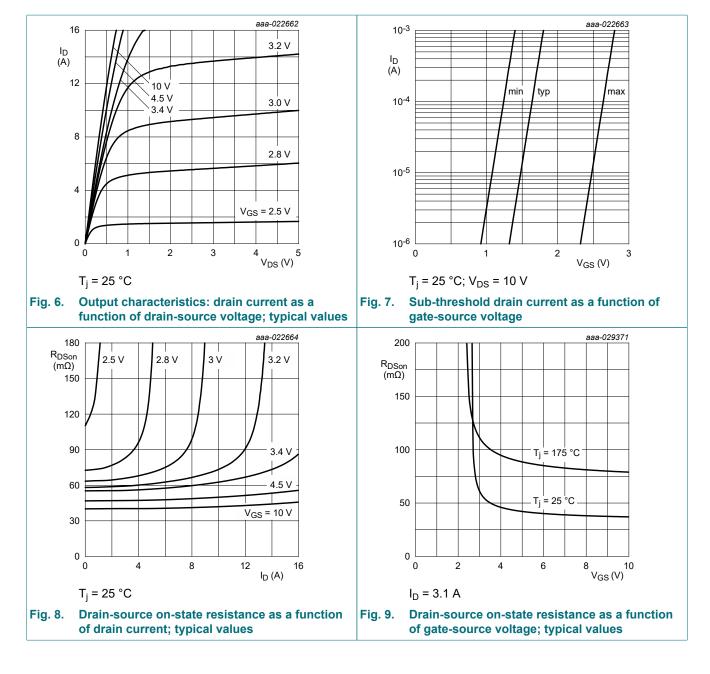
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².



10. Characteristics

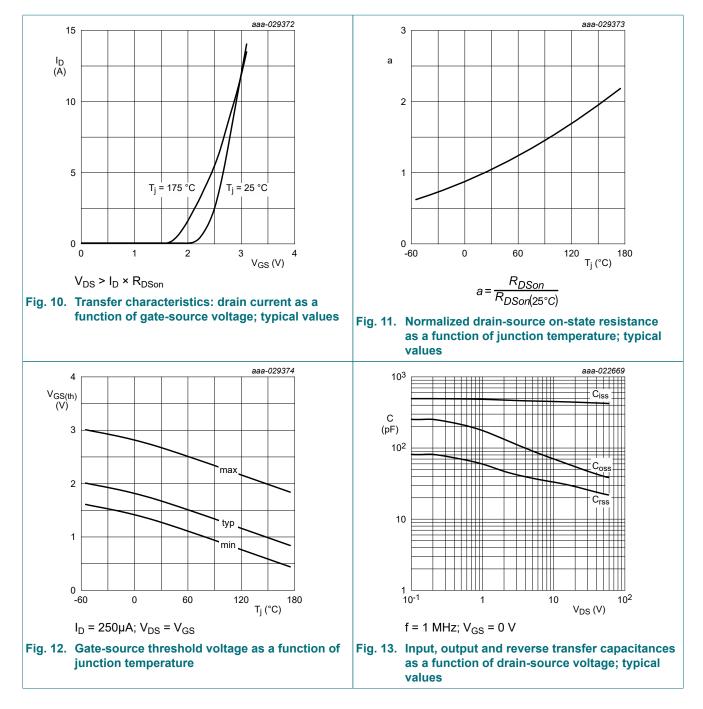
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	60	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	1.3	1.7	2.7	V
I _{DSS}	drain leakage current	V _{DS} = 60 V; V _{GS} = 0 V; T _j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	10	μA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	1	μA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-1	μA
R _{DSon}	drain-source on-state	V _{GS} = 10 V; I _D = 3.6 A; T _j = 25 °C	-	46	60	mΩ
resistance	resistance	V _{GS} = 10 V; I _D = 3.6 A; T _j = 175 °C	-	100	130	mΩ
		V _{GS} = 4.5 V; I _D = 3.3 A; T _j = 25 °C	-	52	70	mΩ
9fs	forward transconductance	V _{DS} = 10 V; I _D = 3.6 A; T _j = 25 °C	-	18.2	-	S
R _G	gate resistance	f = 1 MHz	-	2	-	Ω
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	V_{DS} = 30 V; I _D = 3.1 A; V _{GS} = 10 V;	-	8.8	13.2	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1.1	-	nC
Q _{GD}	gate-drain charge		-	1.8	-	nC
C _{iss}	input capacitance	V_{DS} = 30 V; f = 1 MHz; V_{GS} = 0 V;	-	450	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	49	-	pF
C _{rss}	reverse transfer capacitance		-	30	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; I _D = 3.1 A; V _{GS} = 10 V;	-	5	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	7	-	ns
t _{d(off)}	turn-off delay time		-	13	-	ns
t _f	fall time		-	4	-	ns
Source-drai	n diode		I			
V _{SD}	source-drain voltage	I _S = 1.7 A; V _{GS} = 0 V; T _i = 25 °C	-	0.8	1.2	V

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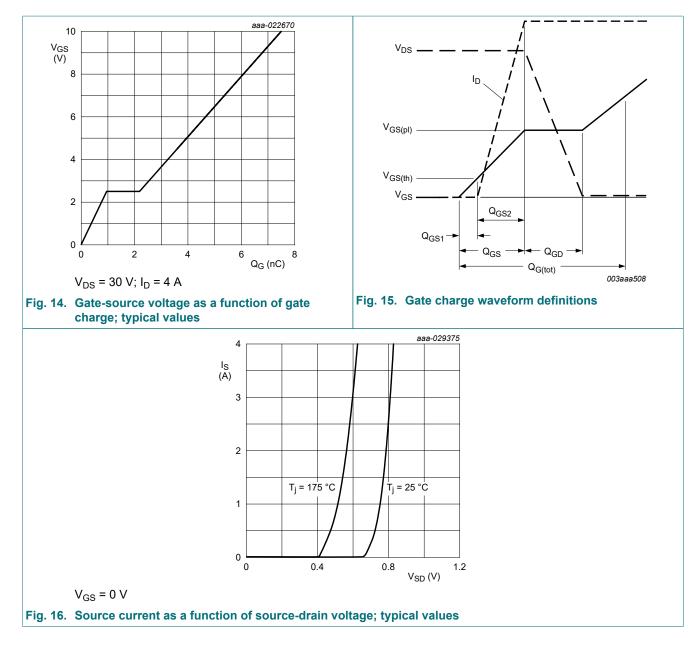
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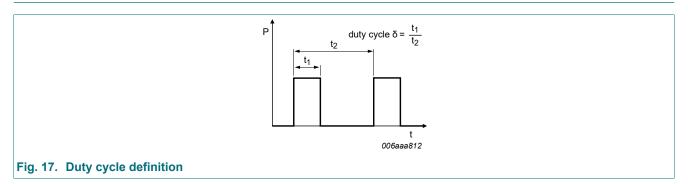
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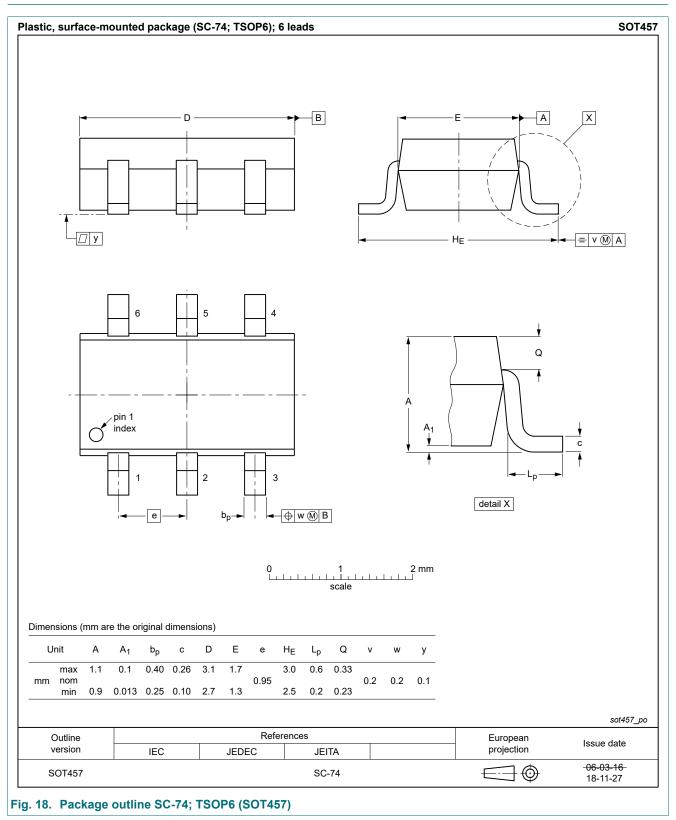


Product data sheet

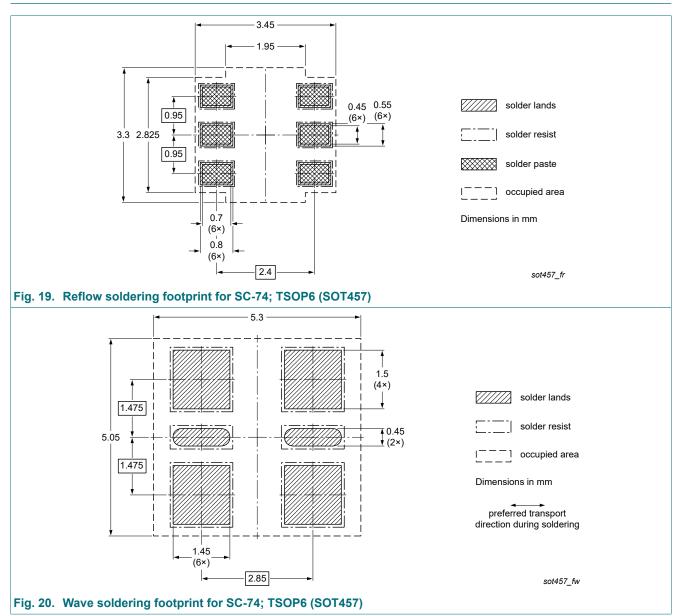
11. Test information



12. Package outline



13. Soldering



Product data sheet

14. Revision history

Table 8. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PMN37ENE v.1	20211129	Product data sheet	-	-	

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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