

PMV16XN 20 V, N-channel Trench MOSFET 11 November 2014

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Trench MOSFET technology
- Low threshold voltage
- Very fast switching
- Enhanced power dissipation capability of 1200 mW

3. Applications

- LED driver
- Power management
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data								
Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	20	V	
V _{GS}	gate-source voltage			-12	-	12	V	
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	8.6	А	
Static characteristics								
R _{DSon}	drain-source on-state resistance	V _{GS} = 4.5 V; I _D = 6.8 A; T _j = 25 °C		-	16	20	mΩ	

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².

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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	1 2 TO-236AB (SOT23)	017aaa253

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMV16XN	TO-236AB	plastic surface-mounted package; 3 leads	SOT23			

6. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
PMV16XN	%BZ

[1] % = placeholder for manufacturing site code

7. Limiting values

Table 5.Limiting values

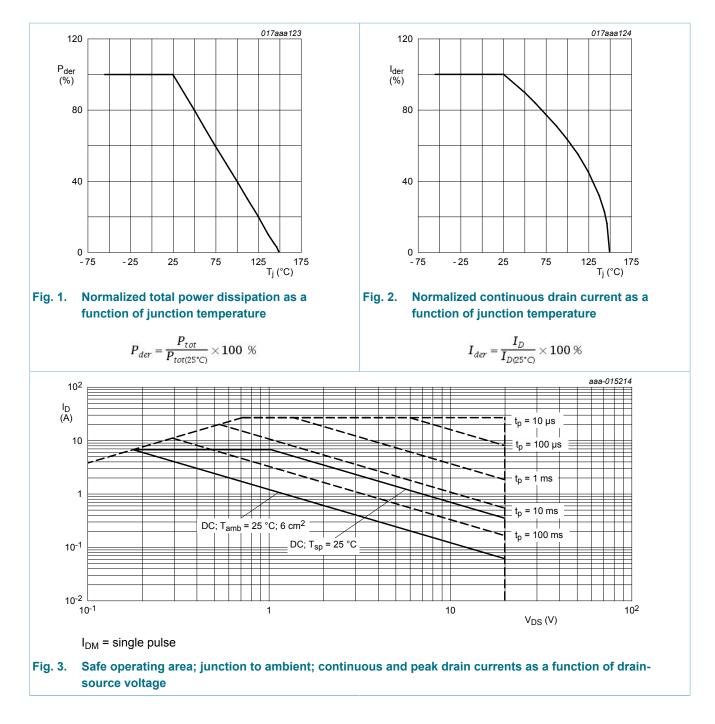
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	20	V
V _{GS}	gate-source voltage			-12	12	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	8.6	А
		V _{GS} = 4.5 V; T _{amb} = 25 °C	[1]	-	6.8	А
		V _{GS} = 4.5 V; T _{amb} = 100 °C	[1]	-	4.3	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	27	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	510	mW
			[1]	-	1200	mW
		T _{sp} = 25 °C		-	6940	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	in diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	1.2	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

20 V, N-channel Trench MOSFET



8. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistan from junction to ambient	thermal resistance	in free air	[1]	-	208	245	K/W
	-		[2]	-	88	104	K/W
	ambient	t ≤ 5 s	[2]	-	55	65	K/W

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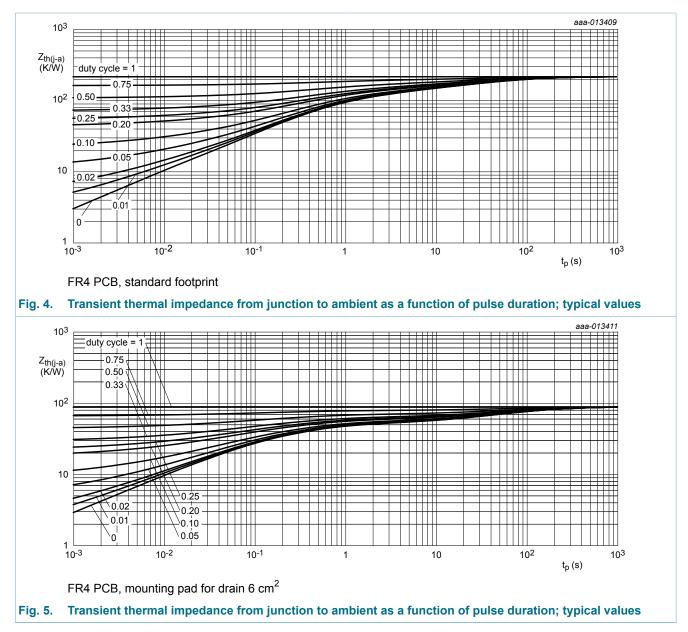
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		-	13	18	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

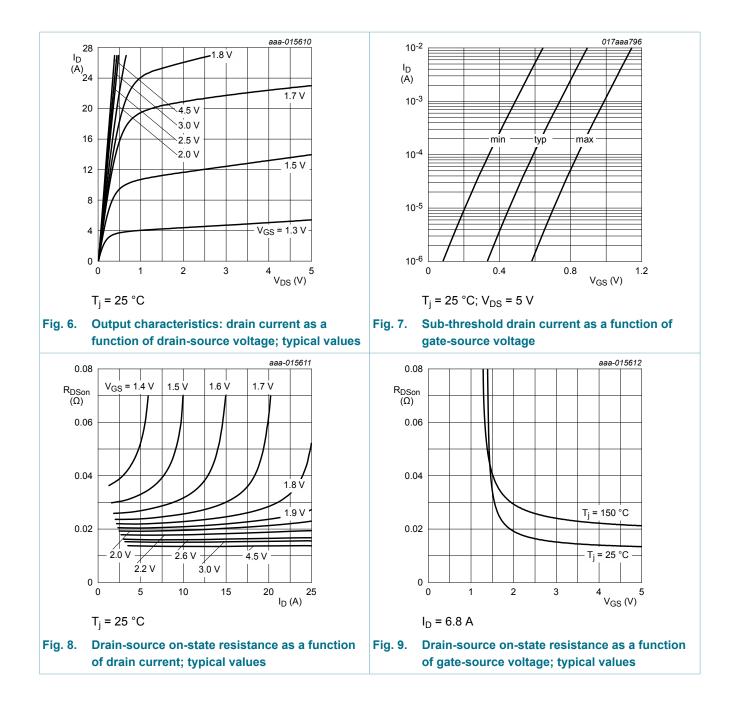
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².



9. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics	1				
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	20	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	0.4	0.65	0.9	V
I _{DSS}	drain leakage current	V_{DS} = 20 V; V_{GS} = 0 V; T_j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V_{GS} = 12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
		V _{GS} = -12 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
R _{DSon} drain-source on-state resistance	drain-source on-state	V_{GS} = 4.5 V; I _D = 6.8 A; T _j = 25 °C	-	16	20	mΩ
	resistance	V _{GS} = 4.5 V; I _D = 6.8 A; T _j = 150 °C	-	23	29	mΩ
		V_{GS} = 2.5 V; I _D = 6.0 A; T _j = 25 °C	-	18	24	mΩ
		V _{GS} = 1.8 V; I _D = 2.1 A; T _j = 25 °C	-	22	33	mΩ
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 2 A; T _j = 25 °C	-	12	-	S
R _G	gate resistance	f = 1 MHz	-	2	-	Ω
Dynamic ch	aracteristics	· · · · ·				
Q _{G(tot)}	total gate charge	V_{DS} = 10 V; I _D = 7.3 A; V _{GS} = 4.5 V;	-	13.4	20.2	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1.5	-	nC
Q _{GD}	gate-drain charge		-	2.6	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V;	-	1240	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	145	-	pF
C _{rss}	reverse transfer capacitance		-	125	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 10 V; I _D = 7.3 A; V _{GS} = 4.5 V;	-	9	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	24	-	ns
t _{d(off)}	turn-off delay time		-	31	-	ns
t _f	fall time		-	36	-	ns
Source-drai	in diode	1				
V _{SD}	source-drain voltage	I _S = 1.2 A; V _{GS} = 0 V; T _i = 25 °C	-	0.65	1.2	V

20 V, N-channel Trench MOSFET

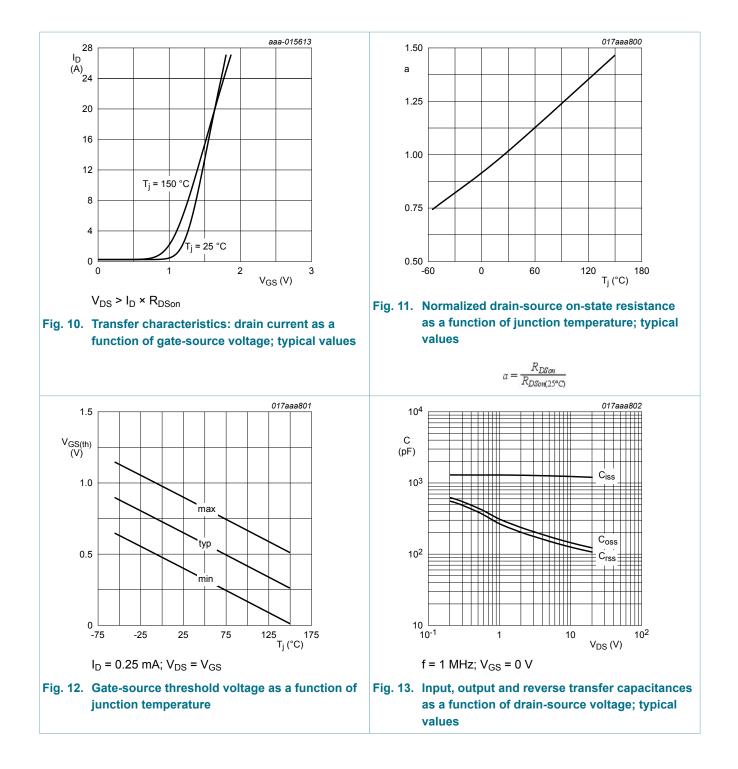


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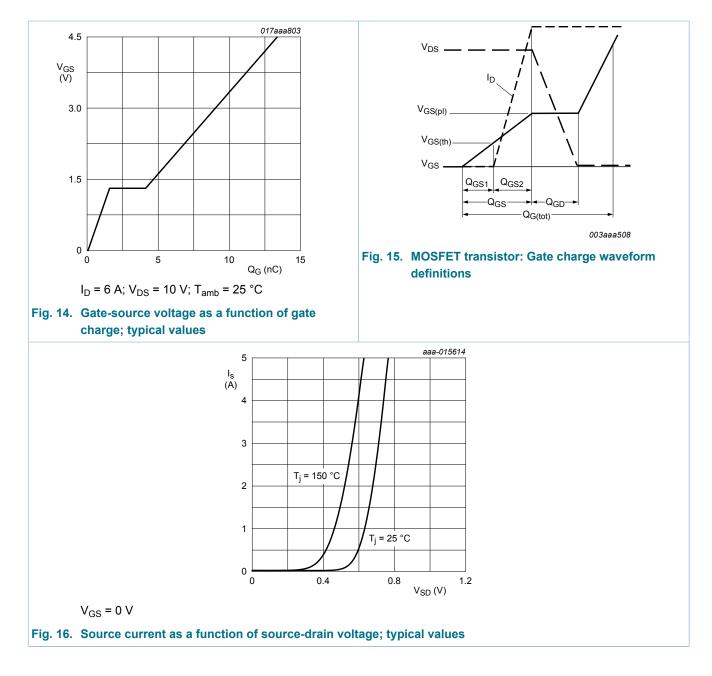
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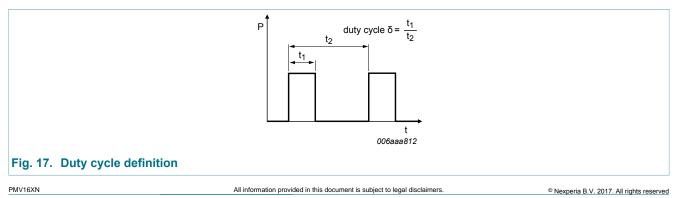
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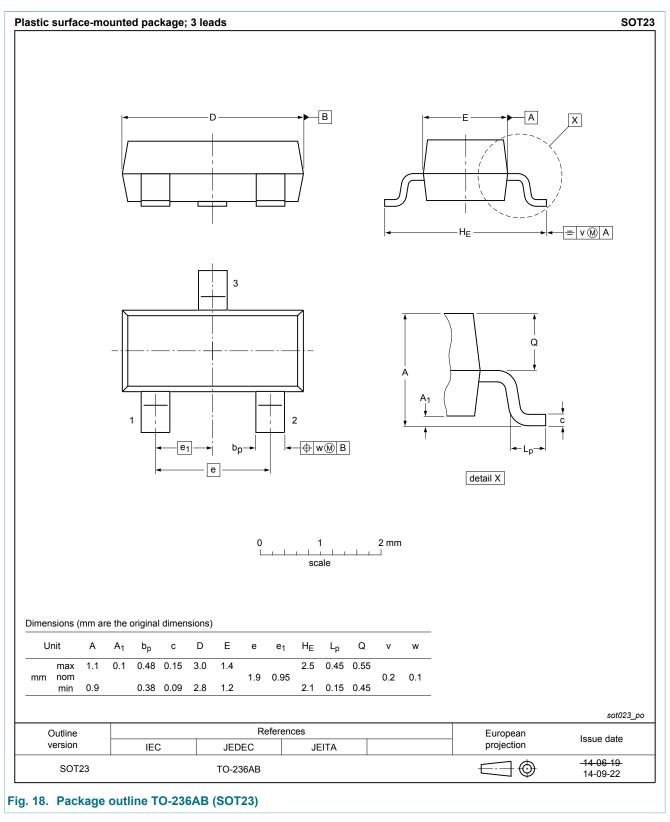
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10. Test information

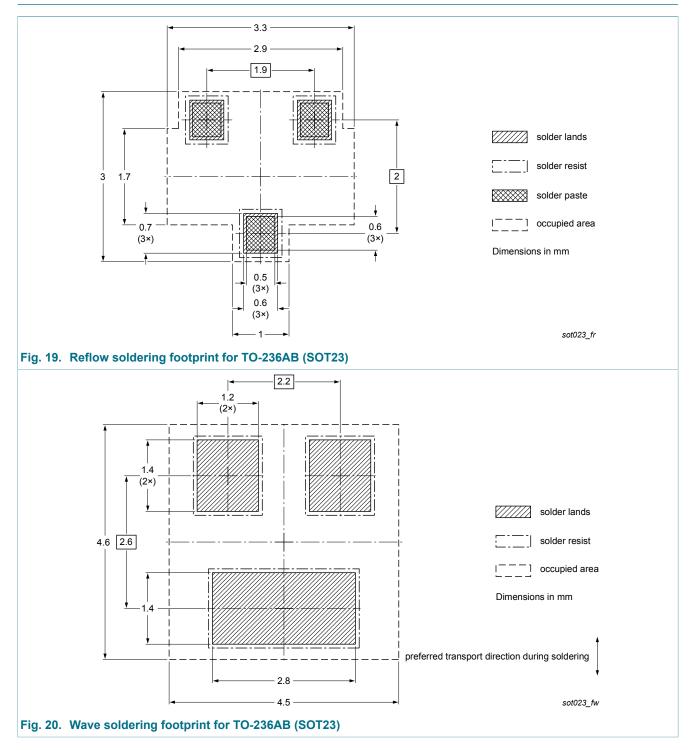


11. Package outline



20 V, N-channel Trench MOSFET

12. Soldering



13. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMV16XN v.1	20141111	Product data sheet	-	-

20 V, N-channel Trench MOSFET

14. Legal information

14.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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20 V, N-channel Trench MOSFET

15. Contents

General description	1
Features and benefits	1
Applications	1
Quick reference data	1
Pinning information	2
Marking	2
Limiting values	3
Thermal characteristics	4
Characteristics	6
Test information	9
Package outline	10
Soldering	11
Revision history	12
Legal information	13
Data sheet status	13
Definitions	13
Disclaimers	13
Trademarks	14
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