

30 V, P-channel Trench MOSFET

20 August 2019

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- · Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection > 2 kV HBM

3. Applications

- Relay driver
- High-speed line driver
- High-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-30	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = -10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-	-3.4	А
Static characte	eristics						
R _{DSon}	drain-source on-state resistance	V _{GS} = -10 V; I _D = -2.8 A; T _j = 25 °C		-	74	90	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².



5. Pinning information

Table 2. F	Pinning infor	mation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	1 2 TO-236AB (SOT23)	G G S 017aaa259

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMV74EPE	TO-236AB	plastic surface-mounted package; 3 leads	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PMV74EPE	2P%

[1] % = placeholder for manufacturing site code

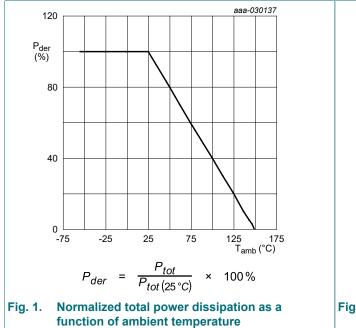
8. Limiting values

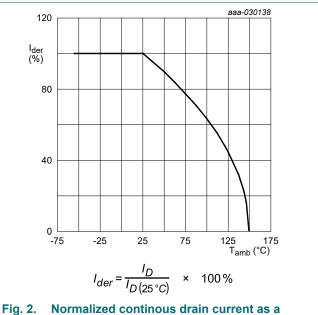
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-30	V
V _{GS}	gate-source voltage	_		-20	20	V
I _D	drain current	V _{GS} = -10 V; T _{amb} = 25 °C; t ≤ 5 s	[1]	-	-3.4	А
		V _{GS} = -10 V; T _{amb} = 25 °C	[1]	-	-2.8	А
		V _{GS} = -10 V; T _{amb} = 100 °C	[1]	-	-1.8	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-11	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	510	mW
			[1]	-	1	W
		T _{sp} = 25 °C		-	6.4	W
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	n diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	-1.1	А

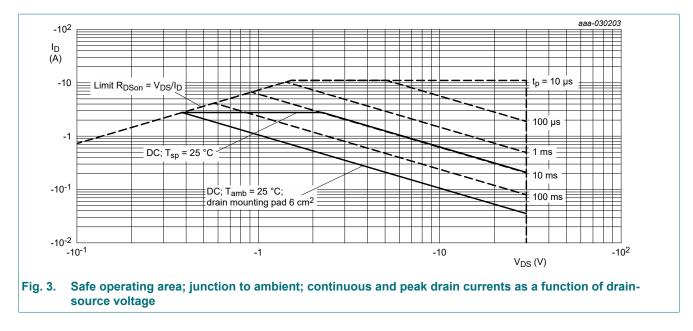
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm².
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.





function of ambient temperature

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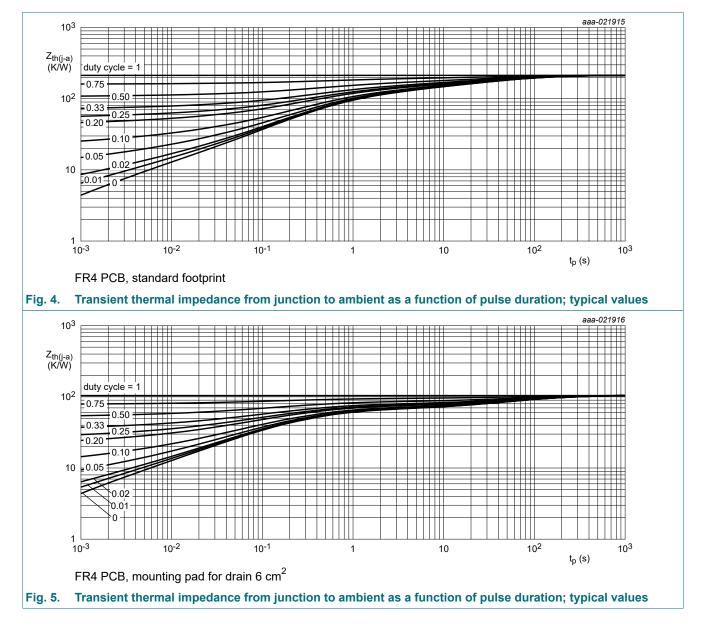
PMV74EPE

9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
u iu-a)	thermal resistance from	in free air	[1]	-	212	244	K/W
	junction to ambient		[2]	-	104	119	K/W
		in free air; t ≤ 5 s	[2]	-	69	79	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	17	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

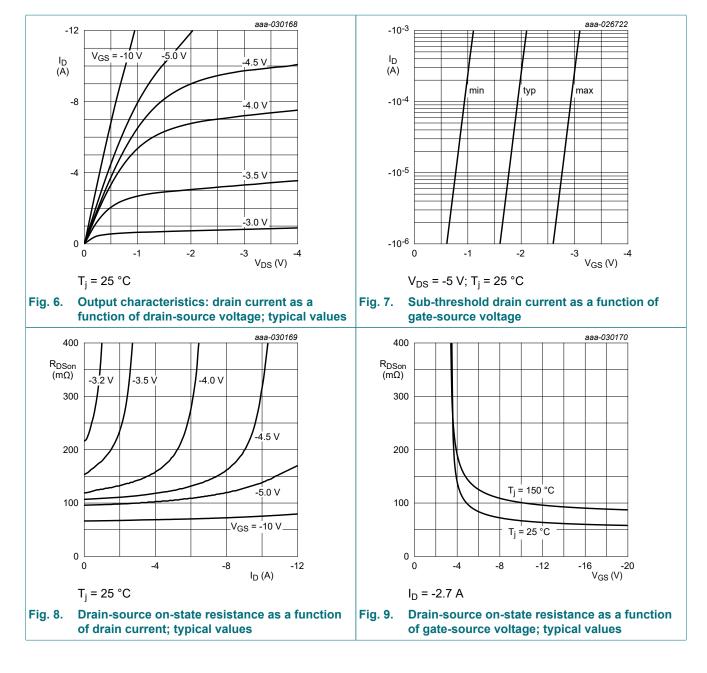
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 6 cm².



10. Characteristics

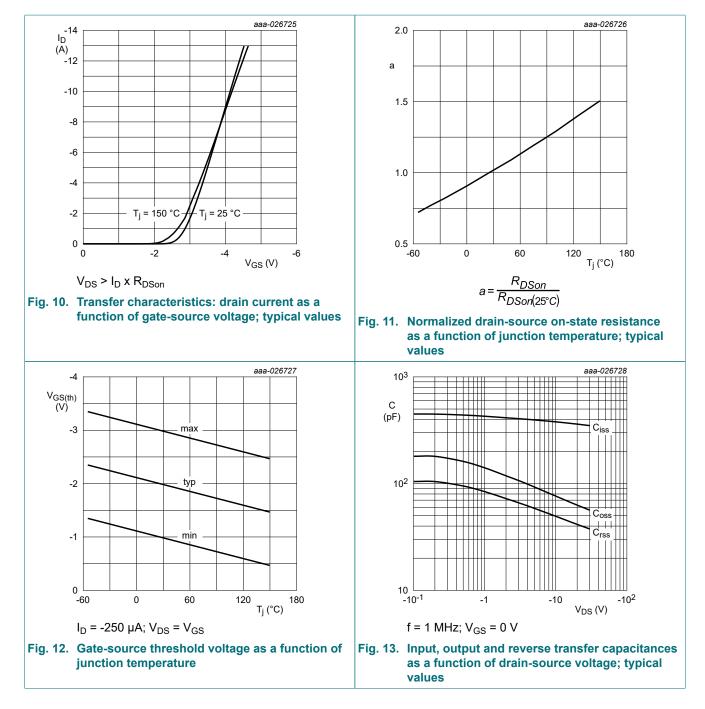
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C	-30	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = -250 μA; V _{DS} = V _{GS} ; T _j = 25 °C	-1	-2	-3	V
I _{DSS}	drain leakage current	V _{DS} = -30 V; V _{GS} = 0 V; T _j = 25 °C	-	-	-1	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	10	μA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	2	μA
		V _{GS} = -10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-2	μA
R _{DSon}	drain-source on-state	V _{GS} = -10 V; I _D = -2.8 A; T _j = 25 °C	-	74	90	mΩ
	resistance	V _{GS} = -10 V; I _D = -2.8 A; T _j = 150 °C	-	112	137	mΩ
		V _{GS} = -4.5 V; I _D = -2.1 A; T _j = 25 °C	-	116	150	mΩ
9 _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -2.7 A; T _j = 25 °C	-	12	-	S
R _G	gate resistance	f = 1 MHz	-	12	-	Ω
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	V _{DS} = -15 V; I _D = -2.7 A; V _{GS} = -10 V;	-	5.7	10	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1.2	-	nC
Q _{GD}	gate-drain charge		-	1.1	-	nC
C _{iss}	input capacitance	V _{DS} = -15 V; f = 1 MHz; V _{GS} = 0 V;	-	356	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	60	-	pF
C _{rss}	reverse transfer capacitance		-	38	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = -15 V; I _D = -2.7 A; V _{GS} = -10 V;	-	5	-	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	11	-	ns
t _{d(off)}	turn-off delay time	1	-	19	-	ns
t _f	fall time	1	-	8	-	ns
Source-drai	n diode					
V _{SD}	source-drain voltage	I _S = -1.1 A; V _{GS} = 0 V; T _i = 25 °C	-	-0.8	-1.2	V

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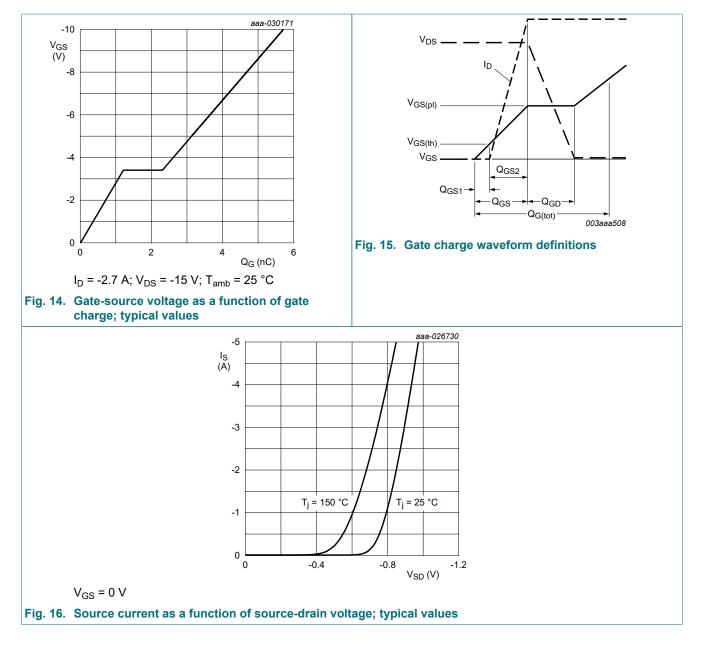


Product data sheet

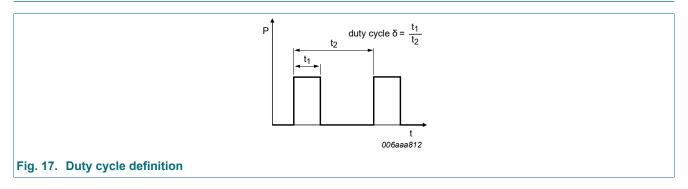
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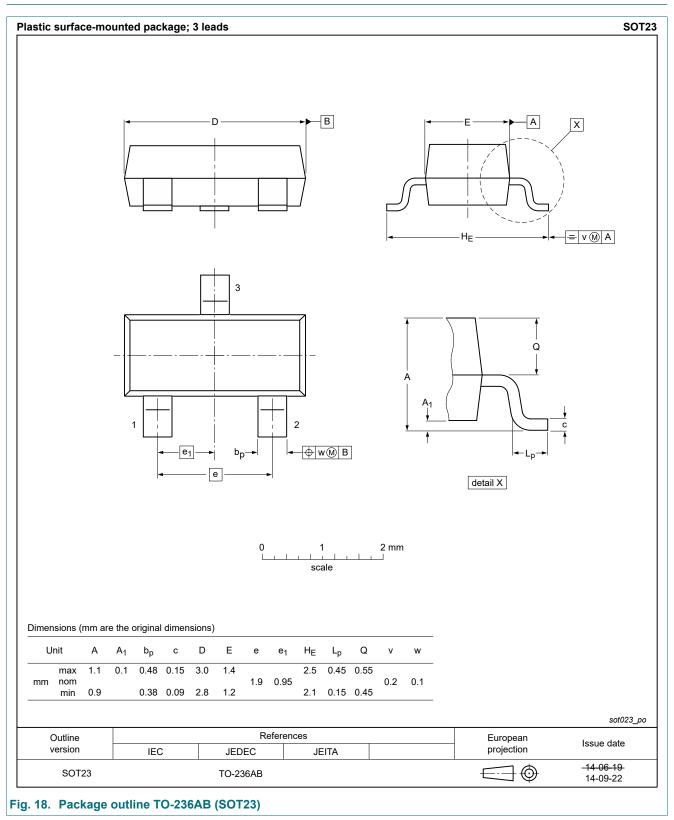
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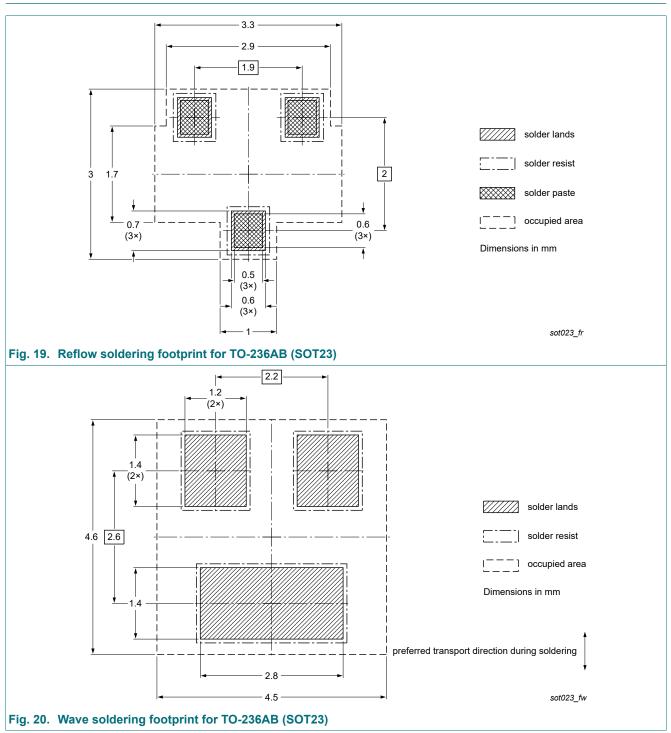
11. Test information



12. Package outline



13. Soldering



Product data sheet

14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMV74EPE v.1	20190820	Product data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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Contents

1. General description	1
2. Features and benefits	1
3. Applications	1
4. Quick reference data	1
5. Pinning information	2
6. Ordering information	2
7. Marking	2
8. Limiting values	3
9. Thermal characteristics	
10. Characteristics	6
11. Test information	9
12. Package outline	10
13. Soldering	
14. Revision history	12
15. Legal information	

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