

#### 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN0603-3 (SOT8013) Surface-Mounted Device (SMD) using Trench MOSFET technology.

#### 2. Features and benefits

- Logic-level compatible
- Leadless ultra small package 0.63mm x 0.33 mm x 0.25 mm
- Trench MOSFET technology
- Low profile (0.25 mm)
- ElectroStatic Discharge (ESD) protection typically > 1 kV HBM

#### 3. Applications

- Battery switch
- High-speed line driver
- Low-side load switch
- Switching circuits

#### 4. Quick reference data

#### Table 1. Quick reference data

| Symbol            | Parameter                        | Conditions   |     | Min | Тур | Мах  | Unit |
|-------------------|----------------------------------|--|-----|-----|-----|------|------|
| V <sub>DS</sub>   | drain-source voltage             | T <sub>j</sub> = 25 °C   |     | -   | -   | 60   | V    |
| V <sub>GS</sub>   | gate-source voltage              |  |     | -20 | -   | 20   | V    |
| I <sub>D</sub>    | drain current                    | V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C                       | [1] | -   | -   | 0.5  | А    |
| Static chara      | acteristics                      |  | •   |     |     |      |      |
| R <sub>DSon</sub> | drain-source on-state resistance | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 0.4 A; T <sub>j</sub> = 25 °C |     | -   | 800 | 1100 | mΩ   |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

# nexperia

# 5. Pinning information

| Table 2. F | Pinning infor | mation      |  |                          |
|------------|---------------|-------------|--|--------------------------|
| Pin        | Symbol        | Description | Simplified outline                         | Graphic symbol           |
| 1          | G             | gate        |  | D                        |
| 2          | S             | source      |  |                          |
| 3          | D             | drain       | 123Transparent top viewDFN0603-3 (SOT8013) | G<br>G<br>S<br>017aaa255 |

### 6. Ordering information

#### Table 3. Ordering information

| Type number | Package |   |         |  |  |  |
|-------------|---------|---|---------|--|--|--|
|             | Name    | Description   | Version |  |  |  |
| PMX800ENE   |         | DFN0603-3; plastic, ultra small and leadless full<br>encapsulated package; 3 terminals; 0.225 mm pitch; 0.63<br>mm x 0.33 mm x 0.25 mm body | SOT8013 |  |  |  |

## 7. Marking

| Table 4. Marking codes |              |  |  |  |  |
|------------------------|--------------|--|--|--|--|
| Type number            | Marking code |  |  |  |  |
| PMX800ENE              | J            |  |  |  |  |

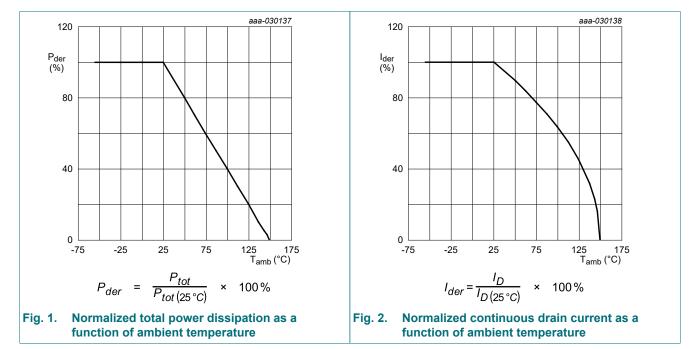
#### 8. Limiting values

#### Table 5. Limiting values

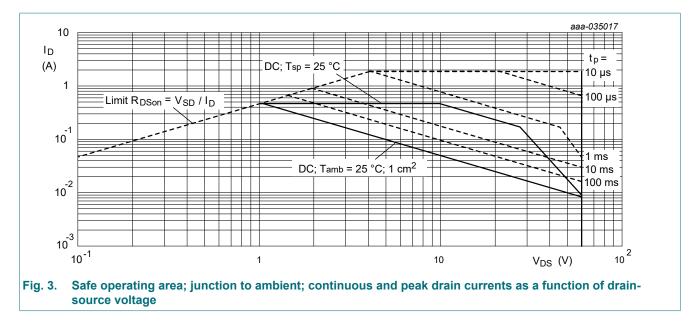
In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter               | Conditions  |     | Min | Мах | Unit |
|------------------|-------------------------|---|-----|-----|-----|------|
| V <sub>DS</sub>  | drain-source voltage    | T <sub>j</sub> = 25 °C                                |     | -   | 60  | V    |
| V <sub>GS</sub>  | gate-source voltage     | _   |     | -20 | 20  | V    |
| I <sub>D</sub>   | drain current           | V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C      | [1] | -   | 0.5 | А    |
|                  |                         | V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C     | [1] | -   | 0.3 | A    |
| I <sub>DM</sub>  | peak drain current      | $T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$ |     | -   | 1.9 | А    |
| P <sub>tot</sub> | total power dissipation | T <sub>amb</sub> = 25 °C                              | [2] | -   | 300 | mW   |
|                  |                         |   | [1] | -   | 500 | mW   |
|                  |                         | T <sub>sp</sub> = 25 °C                               |     | -   | 4.7 | W    |
| Tj               | junction temperature    |   |     | -55 | 150 | °C   |
| T <sub>amb</sub> | ambient temperature     |   |     | -55 | 150 | °C   |
| T <sub>stg</sub> | storage temperature     |   |     | -65 | 150 | °C   |
| Source-drai      | n diode                 |   |     |     |     |      |
| Is               | source current          | T <sub>amb</sub> = 25 °C                              | [1] | -   | 0.5 | А    |

Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.
 Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



#### 60 V, N-channel Trench MOSFET

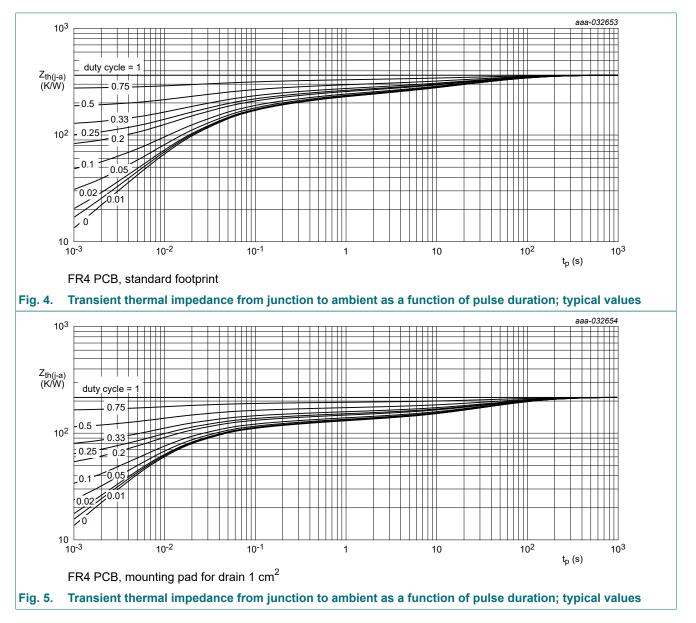


### 9. Thermal characteristics

| Symbol                | Parameter  | Conditions     |     | Min | Тур | Max  | Unit |
|-----------------------|--|----------------|-----|-----|-----|------|------|
| ui(j-a)               | thermal resistance from                          | om in free air | [1] | -   | 360 | 415  | K/W  |
|                       | junction to ambient                              |                | [2] | -   | 215 | 250  | K/W  |
| R <sub>th(j-sp)</sub> | thermal resistance from junction to solder point |                |     | -   | 23  | 26.5 | K/W  |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm<sup>2</sup>.

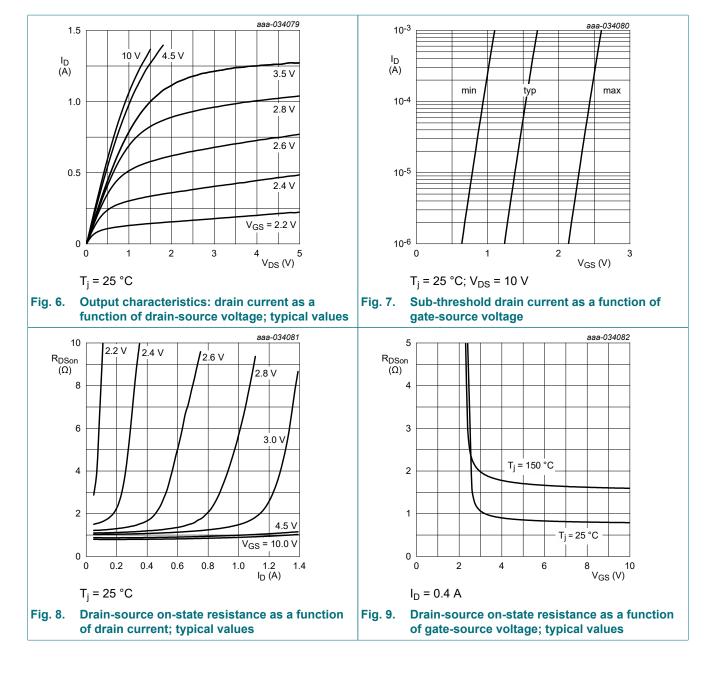


### **10. Characteristics**

| Symbol               | Parameter                         | Conditions  | Min | Тур  | Max  | Unit |
|----------------------|-----------------------------------|---|-----|------|------|------|
| Static chara         | octeristics                       | l l   |     |      |      |      |
| V <sub>(BR)DSS</sub> | drain-source<br>breakdown voltage | I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C              | 60  | -    | -    | V    |
| V <sub>GSth</sub>    | gate-source threshold voltage     | I <sub>D</sub> = 250 μA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C | 1   | 1.6  | 2.5  | V    |
| I <sub>DSS</sub>     | drain leakage current             | V <sub>DS</sub> = 60 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C               | -   | -    | 1    | μA   |
| I <sub>GSS</sub>     | gate leakage current              | V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C              | -   | -    | -10  | μA   |
|                      |                                   | V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C               | -   | -    | 10   | μA   |
| R <sub>DSon</sub>    | drain-source on-state             | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 0.4 A; T <sub>j</sub> = 25 °C              | -   | 800  | 1100 | mΩ   |
|                      | resistance                        | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 0.4 A; T <sub>j</sub> = 150 °C             | -   | 1600 | 2100 | mΩ   |
|                      |                                   | $V_{GS}$ = 4.5 V; I <sub>D</sub> = 0.3 A; T <sub>j</sub> = 25 °C                    | -   | 870  | 1200 | mΩ   |
| 9fs                  | forward<br>transconductance       | V <sub>DS</sub> = 10 V; I <sub>D</sub> = 0.4 A; T <sub>j</sub> = 25 °C              | -   | 1.5  | -    | S    |
| R <sub>G</sub>       | gate resistance                   | f = 1 MHz   | -   | 205  | -    | Ω    |
| Dynamic ch           | aracteristics                     | · · · ·   |     |      |      |      |
| Q <sub>G(tot)</sub>  | total gate charge                 | $V_{DS}$ = 30 V; I <sub>D</sub> = 0.4 A; V <sub>GS</sub> = 10 V;                    | -   | 0.6  | 1    | nC   |
| Q <sub>GS</sub>      | gate-source charge                | T <sub>j</sub> = 25 °C  | -   | 0.1  | -    | nC   |
| Q <sub>GD</sub>      | gate-drain charge                 |   | -   | 0.1  | -    | nC   |
| C <sub>iss</sub>     | input capacitance                 | V <sub>DS</sub> = 30 V; f = 1 MHz; V <sub>GS</sub> = 0 V;                           | -   | 32   | -    | pF   |
| C <sub>oss</sub>     | output capacitance                | T <sub>j</sub> = 25 °C  | -   | 4    | -    | pF   |
| C <sub>rss</sub>     | reverse transfer capacitance      |   | -   | 2    | -    | pF   |
| t <sub>d(on)</sub>   | turn-on delay time                | $V_{DS}$ = 30 V; I <sub>D</sub> = 0.4 A; V <sub>GS</sub> = 10 V;                    | -   | 2    | -    | ns   |
| t <sub>r</sub>       | rise time                         | $R_{G(ext)} = 6 \Omega; T_j = 25 °C$  | -   | 2    | -    | ns   |
| t <sub>d(off)</sub>  | turn-off delay time               | -   | -   | 20   | -    | ns   |
| t <sub>f</sub>       | fall time                         |   | -   | 9    | -    | ns   |
| Source-drai          | n diode                           | · · · ·   |     |      |      |      |
| V <sub>SD</sub>      | source-drain voltage              | I <sub>S</sub> = 0.48 A; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C              | -   | 0.8  | 1.2  | V    |

PMX800ENE

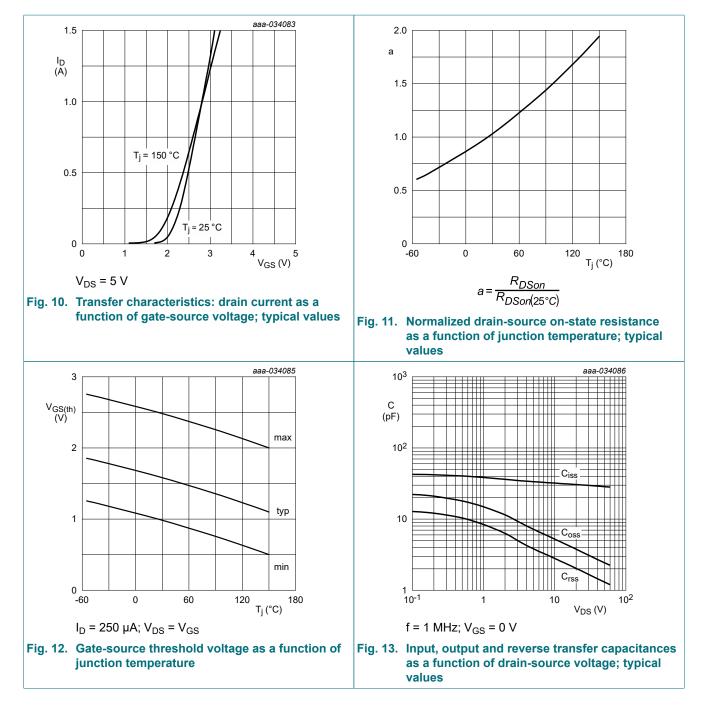
#### 60 V, N-channel Trench MOSFET



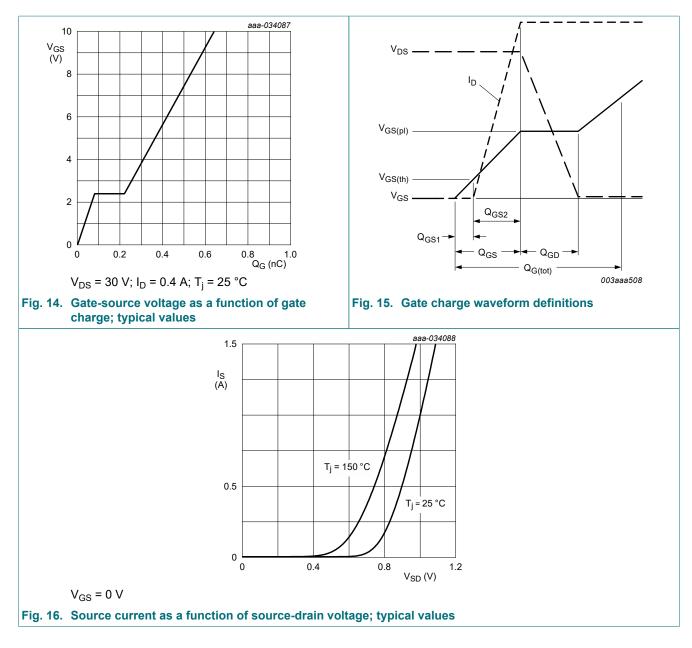
**Product data sheet** 

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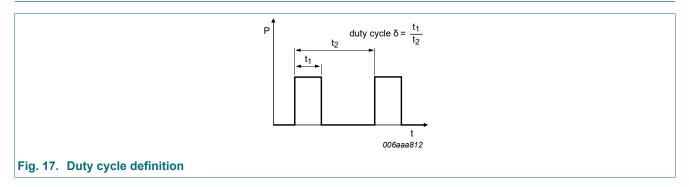
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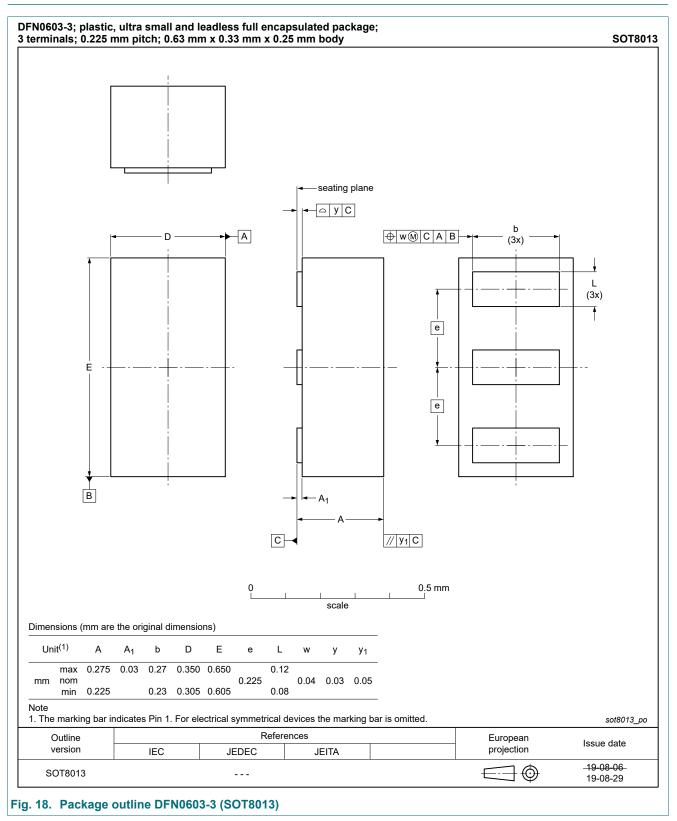
#### 60 V, N-channel Trench MOSFET



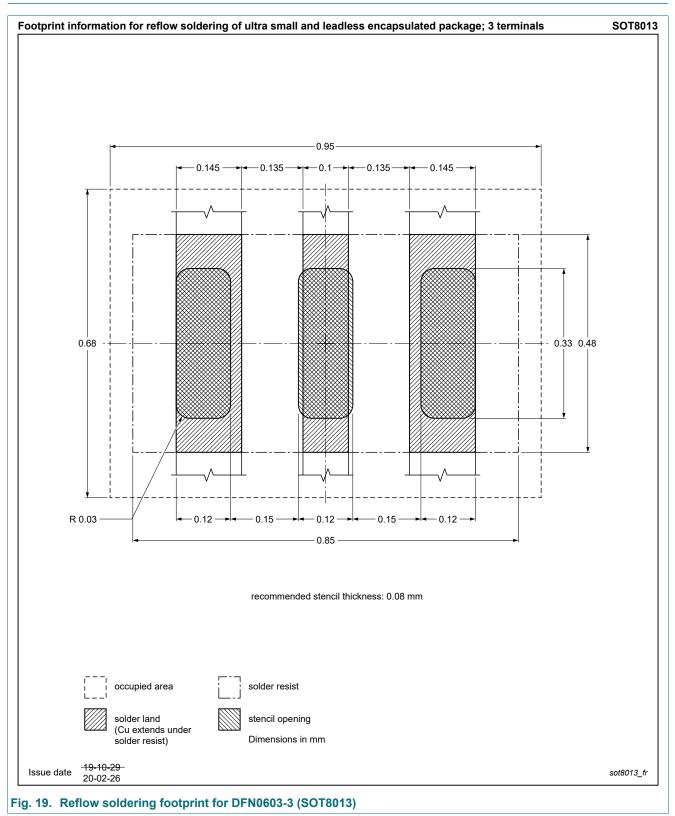
#### **11. Test information**



#### 12. Package outline



### 13. Soldering



# 14. Revision history

| Table 8. Revision history |   |                        |               |               |  |  |
|---------------------------|---|------------------------|---------------|---------------|--|--|
| Data sheet ID             | Release date                                    | Data sheet status      | Change notice | Supersedes    |  |  |
| PMX800ENE v.2             | 20230712  | Product data sheet     | -             | PMX800ENE v.1 |  |  |
| Modifications:            | Changed document status to "Product data sheet" |                        |               |               |  |  |
| PMX800ENE v.1             | 20220609  | Preliminary data sheet | -             | -             |  |  |

PMX800ENE

# 15. Legal information

#### **Data sheet status**

| Document status [1][2]            | Product<br>status [3] | Definition  |
|-----------------------------------|-----------------------|---|
| Objective [short]<br>data sheet   | Development           | This document contains data from<br>the objective specification for<br>product development. |
| Preliminary [short]<br>data sheet | Qualification         | This document contains data from the preliminary specification.                             |
| Product [short]<br>data sheet     | Production            | This document contains the product specification.   |

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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