

# 50 V, 100 mA NPN/NPN resistor-equipped transistor; R1 = 47 k $\Omega$ , R2 = open

1 March 2024

Product data sheet

# 1. General description

NPN/NPN Resistor-Equipped Transistor (RET) in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

## 2. Features and benefits

- Built-in bias resistors
- Simplified circuit design
- Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

- Low current peripheral driver
- · Controlling IC inputs
- · Replacement of general purpose transistors in digital applications

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor	Per transistor						
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	33	47	61	kΩ

[1] See section "Test information" for resistor calculation and test conditions.



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# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	GND1	GND (emitter) TR1		O1 I2 GND2
2	I1	input (base) TR1	□6 □5 □4	
3	O2	output (collector) TR2		R1 TR2
4	GND2	GND (emitter) TR2	0	TR1
5	12	input (base) TR2	1 1 2 3	
6	O1	output (collector) TR1	TSSOP6 (SOT363)	GND1 I1 O2 sym090

# 6. Ordering information

**Table 3. Ordering information** 

Type number	Package					
	Name	Description	Version			
PUMH14-Q		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>			

# 7. Marking

#### **Table 4. Marking codes**

Type number	Marking code[1]
PUMH14-Q	H1%

[1] % = placeholder for manufacturing site code

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# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transiste	or		•			
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	50	V
$V_{EBO}$	emitter-base voltage	open collector		-	5	V
Io	output current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Per device			•			
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

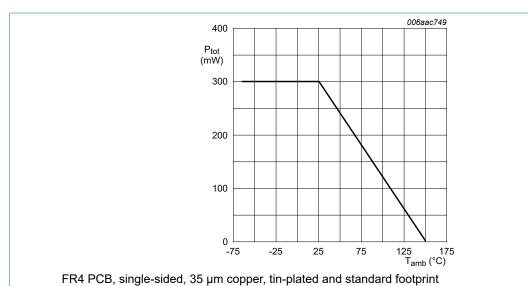


Fig. 1. Per device: Power derating curve

50 V, 100 mA NPN/NPN resistor-equipped transistor; R1 = 47 k $\Omega$ , R2 = open

## 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor	Per transistor						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W
Per device	Per device						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

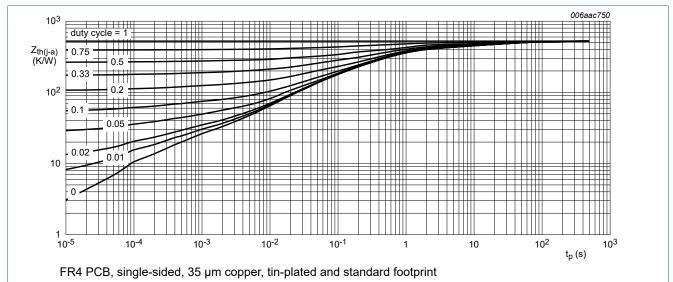


Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or						
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		50	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = 50 \text{ V}; I_{E} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		-	-	100	nA
I <sub>CEO</sub> collector-emitter cut-off current	collector-emitter cut-off	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
	current	V <sub>CE</sub> = 30 V; I <sub>B</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 mA; T <sub>amb</sub> = 25 °C		100	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 0.5 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	150	mV
R1	bias resistor 1 (input)		[1]	33	47	61	kΩ
C <sub>c</sub>	collector capacitance	$V_{CB}$ = 10 V; $I_{E}$ = 0 A; $i_{e}$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C		-	-	2.5	pF

[1] See section "Test information" for resistor calculation and test conditions.

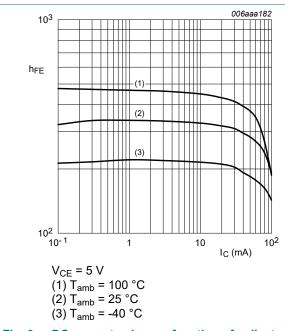


Fig. 3. DC current gain as a function of collector current; typical values

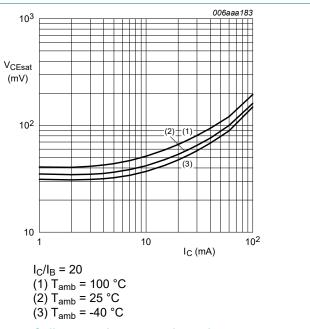


Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values

50 V, 100 mA NPN/NPN resistor-equipped transistor; R1 = 47 k $\Omega$ , R2 = open

## 11. Test information

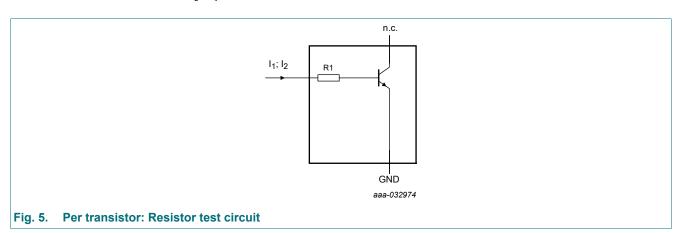
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

#### **Resistor calculation**

· Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{I})}{I_{2} - I_{I}}$$

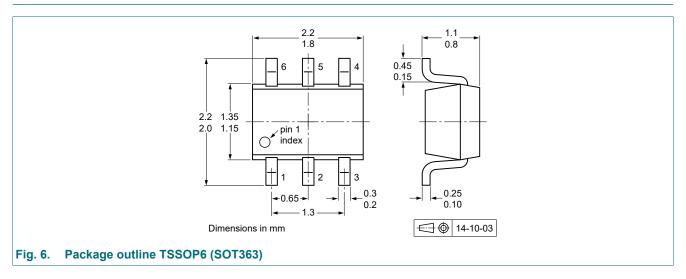


#### **Resistor test conditions**

Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions	
			l <sub>1</sub>	l <sub>2</sub>
PUMH14-Q	47	open	55 μΑ	105 μΑ

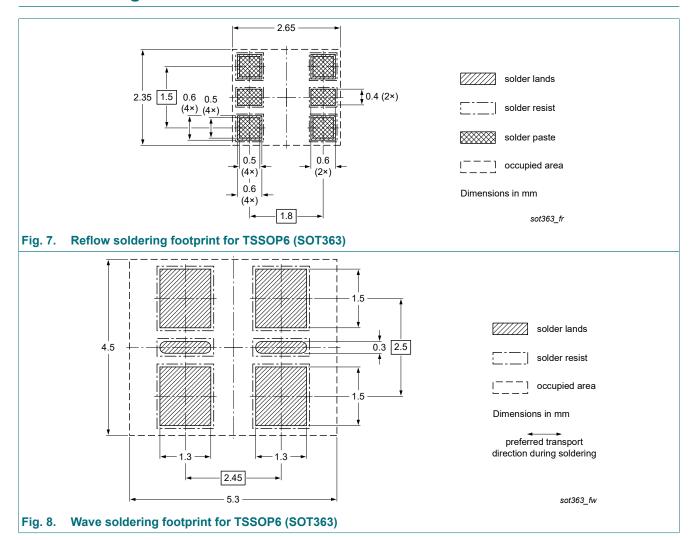
# 12. Package outline



PUMH14-Q

50 V, 100 mA NPN/NPN resistor-equipped transistor; R1 = 47 k $\Omega$ , R2 = open

# 13. Soldering



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# 14. Revision history

## Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PUMH14-Q v.1	20240301	Product data sheet	-	-

#### 50 V, 100 mA NPN/NPN resistor-equipped transistor; R1 = 47 k $\Omega$ , R2 = open

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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PUMH14-Q

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