



SOT8057-1

wafer level chip-scale package; 9 bumps; 0.92 × 0.92 × 0.42 mm body

13 June 2023

Package information

1. Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP9
Package type industry code	WLCSP9
Package style descriptive code	WLCSP (wafer level chip-size package)
Package body material type	X (other)
Mounting method type	S (surface mount)
Number of package outline detail graphic references	0
Issue date	1-6-2023
Manufacturer package code	SOT8057
Footprint dimensions (mm)	1.15 x 1.15
Footprint area (mm²)	1.32
Package dimensions including leads (l x w) (mm)	0.92 x 0.92
Package area (mm²)	0.85

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	-	-	0.92	-	mm
E	package width	-	-	0.92	-	mm
A	seated height	-	-	-	0.415	mm
A ₂	package height	0.225	-	0.25	0.275	mm
e	nominal pitch	-	-	0.3	-	mm
e ₁	minimal pitch	-	-	0.6	-	mm
n ₂	actual quantity of termination	-	-	9	-	

2. Package outline

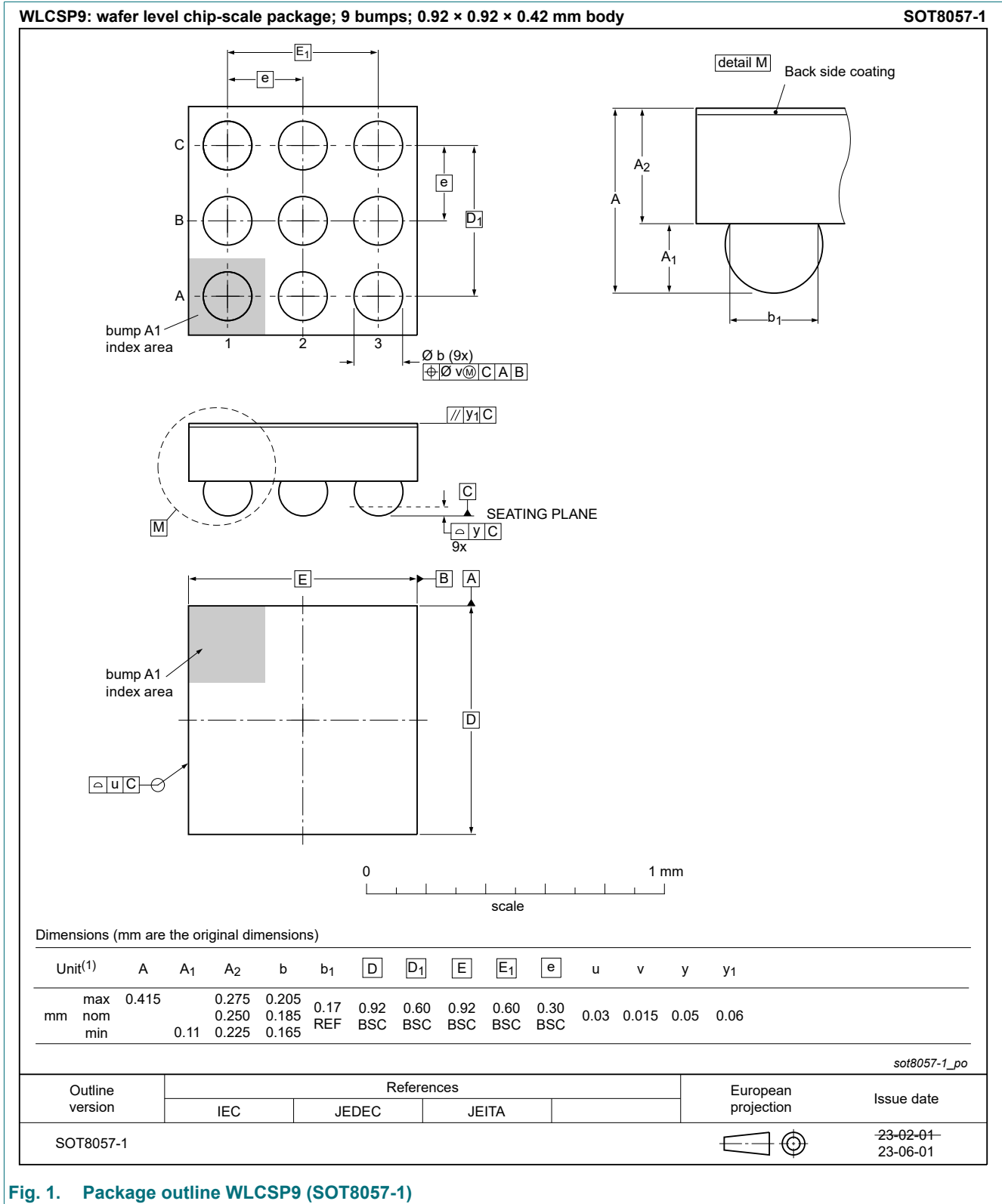


Fig. 1. Package outline WLCSP9 (SOT8057-1)

3. Soldering

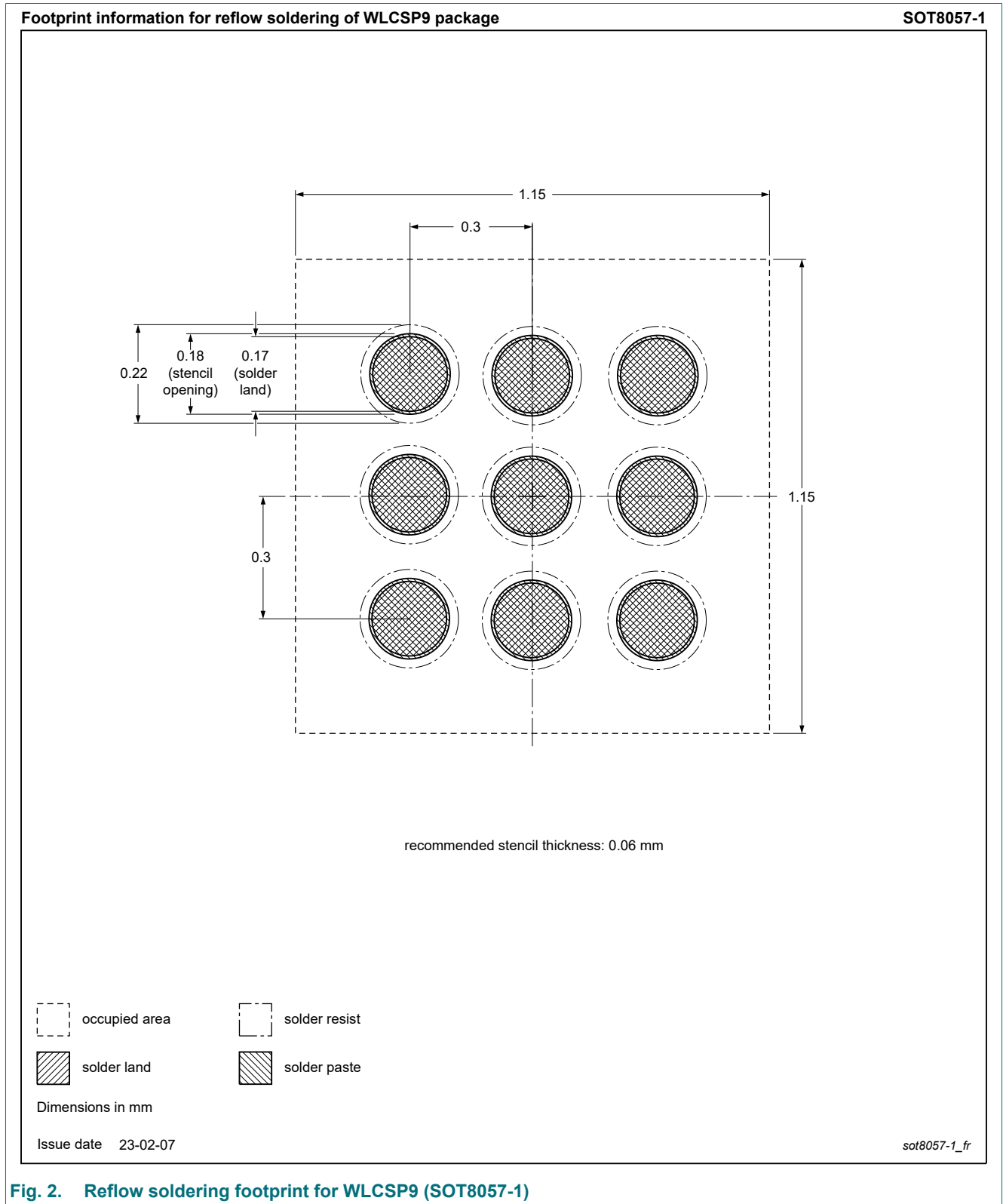


Fig. 2. Reflow soldering footprint for WLCSP9 (SOT8057-1)

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